Alcatel-Lucent 1675

LambdaUnite MultiService Switch (MSS) | Release 10.05.54

Product Information and Planning Guide

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WEEE directive

The *Waste from Electrical and Electronic Equipment (WEEE) directive* for this product can be found in this document at "Eco-environmental statements" (p. 9-5).

Software

Legal Notices applicable to any software distributed alone or in connection with the product to which this document pertains, are contained in files within the software itself located at:

- the software distribution CD available with the product at */Licenses*
- the *WaveStar*® CIT interface; selecting the **Help** → **About** → **Licenses** button from the *WaveStar*® CIT system view.
  See the *1675 LambdaUnite MSS User Provisioning Guide* for additional information about how to access the *WaveStar*® CIT.

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About this document

Purpose

This Product Information and Planning Guide (PIPG) provides the following information about 1675 LambdaUnite MultiService Switch (MSS):

- Features
- Applications
- Product description
- Operations and maintenance
- System engineering
- Product support
- Technical and reliability specifications.

Intended audience

The 1675 LambdaUnite MultiService Switch (MSS) Product Information and Planning Guide (PIPG) is primarily intended for network planners and engineers. In addition, others who need specific information about the features, applications, operation, and engineering of 1675 LambdaUnite MSS may find the information in this manual useful.

How to use this document

Each chapter of this manual treats a specific aspect of the system and can be regarded as an independent description. This ensures that readers can inform themselves according to their special needs. This also means that the manual provides more information than needed by many of the readers. Before you start reading the manual, it is therefore necessary to assess which aspects or chapters will cover the individual area of interest.

The following table briefly describes the type of information found in each chapter.

Chapter	Title	Description
Preface	About this document	This chapter • describes the guide's purpose, intended audience, and organization • lists related documentation • explains how to comment on this document
1	Introduction	This chapter • presents network application solutions • provides a high-level product overview • describes the product family • lists features
2	Features	Describes the features of 1675 LambdaUnite MSS
3	Network topologies	Describes some of the main network topologies possible with 1675 LambdaUnite MSS
4	Product description	This chapter • provides a functional overview of the system • describes the hardware and configurations available for the product
5	Operations, administration, maintenance, and provisioning	Describes OAM&P features (such as alarms, operation interfaces, security, and performance monitoring)
6	System planning and engineering	Provides planning information necessary to deploy the system
7	Ordering	Describes how to order 1675 LambdaUnite MultiService Switch (MSS).
8	Product support	This chapter • describes engineering and installation services • explains documentation and technical support • leads to training delivery options
9	Quality and reliability	This chapter • provides the Alcatel-Lucent quality policy • lists the reliability specifications
10	Technical specifications	Lists the technical specifications
Appendix A	SDH Overview	Describes the standards for optical signal rates and formats (SDH)

Chapter	Title	Description
Appendix B	SONET Overview	Describes the standards for optical signal rates and formats (SONET)
Appendix C	Remote test access	Describes the remote test access feature
Glossary	Defines telecommunication terms and explains abbreviations and acronyms	
Index	Lists specific subjects and their corresponding page numbers	

Safety information

For your safety, this document contains safety statements. Safety statements are given at points where risks of damage to personnel, equipment, and operation may exist. Failure to follow the directions in a safety statement may result in serious consequences.

Optical safety

IEC customer laser safety guidelines

Alcatel-Lucent declares that this product is compliant with all essential safety requirements as stated in IEC 60825-Part 1 and 2 “Safety of laser products” and “Safety of optical fibre telecommunication systems”. Futhermore Alcatel-Lucent declares that the warning statements on labels on this equipment are in accordance with the specified laser radiation class.

Optical safety declaration (if laser modules used)

Alcatel-Lucent declares that this product is compliant with all essential safety requirements as stated in IEC 60825-Part 1 and 2 “Safety of Laser Products” and “Safety of Optical Fiber Telecommunication Systems”. Furthermore Alcatel-Lucent declares that the warning statements on labels on this equipment are in accordance with the specified laser radiation class.

Optical fiber communications

This equipment contains an Optical Fiber Communications semiconductor laser/LED transmitter. The following Laser Safety Guidelines are provided for this product.

General laser information

Optical fiber telecommunication systems, their associated test sets, and similar operating systems use semiconductor laser transmitters that emit infrared (IR) light at wavelengths between approximately 800 nanometers (nm) and 1600 nm. The emitted light is above the red end of the visible spectrum, which is normally not visible to the human eye. Although radiant en at near-IR wavelengths is officially designated invisible, some people can see the shorter wavelength energy even at power levels several orders of magnitude below any that have been shown to cause injury to the eye.

Conventional lasers can produce an intense beam of monochromatic light. The term "monochromaticity" means a single wavelength output of pure color that may be visible or invisible to the eye. A conventional laser produces a small-size beam of light, and because the beam size is small the power density (also called irradiance) is very high. Consequently, lasers and laser products are subject to federal and applicable state regulations, as well as international standards, for their safe operation.

A conventional laser beam expands very little over distance, or is said to be very well collimated. Thus, conventional laser irradiance remains relatively constant over distance. However, lasers used in lightwave systems have a large beam divergence, typically 10 to 20 degrees. Here, irradiance obeys the inverse square law (doubling the distance reduces the irradiance by a factor of 4) and rapidly decreases over distance.

Lasers and eye damage

The optical energy emitted by laser and high-radiance LEDs in the 400-1400 nm range may cause eye damage if absorbed by the retina. When a beam of light enters the eye, the eye magnifies and focuses the energy on the retina magnifying the irradiance. The irradiance of the energy that reaches the retina is approximately 10^5, or 100,000 times more than at the cornea and, if sufficiently intense, may cause a retinal burn.

The damage mechanism at the wavelengths used in an optical fiber telecommunications is thermal in origin, i.e., damage caused by heating. Therefore, a specific amount of energy is required for a definite time to heat an area of retinal tissue. Damage to the retina occurs only when one looks at the light long enough that the product of the retinal irradiance and the viewing time exceeds the damage threshold. Optical energies above 1400 nm cause corneal and skin burns, but do not affect the retina. The thresholds for injury at wavelengths greater than 1400 nm are significantly higher than for wavelengths in the retinal hazard region.

Classification of lasers

Manufacturers of lasers and laser products in the U.S. are regulated by the Food and Drug Administration's Center for Devices and Radiological Health (FDA/CDRH) under 21 CFR 1040. These regulations require manufacturers to certify each laser or laser product as belonging to one of four major Classes: I, II, lla, IIIa, lllb, or IV. The International Electro-technical Commission is an international standards body that writes laser safety standards under IEC-60825. Classification schemes are similar with Classes divided into Classes 1, 1M, 2, 2M, 3R, 3B, and 4. Lasers are classified according to the accessible emission limits and their potential for causing injury. Optical fiber telecommunication systems are generally classified as Class I/1 because, under normal operating conditions, all energized laser transmitting circuit packs are terminated on optical fibers which enclose the laser energy with the fiber sheath forming a protective housing. Also, a protective housing/access panel is typically installed in front of the laser circuit pack shelves The circuit packs themselves, however, may be FDA/CDRH Class I, IIIb, or IV or IEC Class 1, 1M, 3R, 3B, or 4.

Laser safety precautions for optical fiber telecommunication systems

In its normal operating mode, an optical fiber telecommunication system is totally enclosed and presents no risk of eye injury. It is a Class I/1 system under the FDA and IEC classifications.

The fiber optic cables that interconnect various components of an optical fiber telecommunication system can disconnect or break, and may expose people to laser emissions. Also, certain measures and maintenance procedures may expose the technician to emission from the semiconductor laser during installation and servicing. Unlike more familiar laser devices such as solid-state and gas lasers, the emission pattern of a semiconductor laser results in a highly divergent beam. In a divergent beam, the irradiance (power density) decreases rapidly with distance. The greater the distance, the less energy will enter the eye, and the less potential risk for eye injury. Inadvertently viewing an un-terminated fiber or damaged fiber with the unaided eye at distances greater than 5 to 6 inches normally will not cause eye injury, provided the power in the fiber is less than a few milliwatts at the near IR wavelengths and a few tens of milliwatts at the far IR wavelengths. However, damage may occur if an optical instrument such as a microscope, magnifying glass, or eye loupe is used to stare at the energized fiber end.

Laser safety precautions for enclosed systems

Under normal operating conditions, optical fiber telecommunication systems are completely enclosed; nonetheless, the following precautions shall be observed:

1. Because of the potential for eye damage, technicians should not stare into optical connectors or broken fibers
2. Under no circumstance shall laser/fiber optic operations be performed by a technician before satisfactorily completing an approved training course
3. Since viewing laser emissions directly in excess of Class I/1 limits with an optical instrument such as an eye loupe greatly increases the risk of eye damage, appropriate labels must appear in plain view, in close proximity to the optical port on the protective housing/access panel of the terminal equipment.

Laser safety precautions for unenclosed systems

During service, maintenance, or restoration, an optical fiber telecommunication system is considered unenclosed. Under these conditions, follow these practices:

1. Only authorized, trained personnel shall be permitted to do service, maintenance and restoration. Avoid exposing the eye to emissions from un-terminated, energized optical connectors at close distances. Laser modules associated with the optical ports of laser circuit packs are typically recessed, which limits the exposure distance. Optical port shutters, Automatic Power Reduction (APR), and

Automatic Power Shut Down (APSD) are engineering controls that are also used to limit emissions. However, technicians removing or replacing laser circuit packs should not stare or look directly into the optical port with optical instruments or magnifying lenses. (Normal eye wear or indirect viewing instruments such as Find-R-Scopes are not considered magnifying lenses or optical instruments.)

2. Only authorized, trained personnel shall use optical test equipment during installation or servicing since this equipment contains semiconductor lasers (Some examples of optical test equipment are Optical Time Domain Reflectometers (OTDRs), Hand-Held Loss Test Sets.)
3. Under no circumstances shall any personnel scan a fiber with an optical test set without verifying that all laser sources on the fiber are turned off
4. All unauthorized personnel shall be excluded from the immediate area of the optical fiber telecommunication systems during installation and service.

Consult ANSI Z136.2, American National Standard for Safe Use of Lasers in the U.S.; or, outside the U.S., IEC-60825, Part 2 for guidance on the safe use of optical fiber optic communication in the workplace.

For the optical specifications please refer to “Transmission parameters ” (p. 10-6).

Documented feature set

This manual describes 1675 LambdaUnite MSS Release 10.05.54. For technical reasons some of the documented features might not be available until later software versions. For precise information about the availability of features, please consult the Software Release Description (SRD) that is distributed with the network element software. It provides details of the status at the time of software delivery.

Conventions used

These conventions are used in this document:

Numbering

The chapters of this document are numbered consecutively. The page numbering restarts at “1” in each chapter. To facilitate identifying pages in different chapters, the page numbers are prefixed with the chapter number. For example, page 2-3 is the third page in chapter 2.

Cross-references

Cross-reference conventions are identical with those used for numbering, i.e. the first number in a reference to a particular page refers to the corresponding chapter.

Keyword blocks

This document contains so-called keyword blocks to facilitate the location of specific text passages. The keyword blocks are placed to the left of the main text and indicate the contents of a paragraph or group of paragraphs.

Typographical conventions

Special typographical conventions apply to elements of the graphical user interface (GUI), file names and system path information, keyboard entries, alarm messages etc.

- Elements of the graphical user interface (GUI)

  These are examples of text that appears on a graphical user interface (GUI), such as menu options, window titles or push buttons:

  – **Provision...**, **Delete**, **Apply**, **Close**, **OK** (push-button)

  – **Provision Timing/Sync** (window title)

  – **Administration → Security → User Provisioning...** (path for invoking a window)

- File names and system path information

  These are examples of file names and system path information:

  – *setup.exe*

  – *C:\Program Files\Alcatel-Lucent*

- Keyboard entries

  These are examples of keyboard entries:

  – **F1**, **Esc X**, **Alt-F**, **Ctrl-D**, **Ctrl-Alt-Del** (simple keyboard entries)

  A hyphen between two keys means that both keys have to be pressed simultaneously. Otherwise, a single key has to be pressed, or several keys have to be pressed in sequence.

  – **`copy abc xyz`** (command)

  A complete command has to be entered.

- Alarms and error messages

  These are examples of alarms and error messages:

  – `Loss of Signal`

  – `HP-UNEQ`, `MS-AIS`, `LOS`, `LOF`

Abbreviations

Abbreviations used in this document can be found in the “Glossary” unless it can be assumed that the reader is familiar with the abbreviation.

Related information

The manuals related to 1675 LambdaUnite MSS are shown in the following table:

Document title	Document code
1675 LambdaUnite MSS Safety Guide	109510909 (365-374-159R10.05.54)

Document title	Document code
1675 LambdaUnite MSS Product Information and Planning Guide Presents a detailed overview of the system, describes its applications, gives planning requirements, engineering rules, ordering information, and technical specifications.	109786855 (365-374-195R10.05.54)
1675 LambdaUnite MSS User Provisioning Guide Provides step-by-step information for use in daily system operations. The manual demonstrates how to perform system provisioning, operations, and administrative tasks by use of *WaveStar*® CIT.	109786871 (365-374-196R10.05.54)
1675 LambdaUnite MSS Maintenance and Trouble-Clearing Guide Gives detailed information on each possible alarm message. Furthermore, it provides procedures for routine maintenance, troubleshooting, diagnostics, and component replacement.	109786863 (365-374-197R10.05.54)
1675 LambdaUnite MSS Installation and System Turn-Up Guide A step-by-step guide to system installation and set up. It also includes information needed for pre-installation site planning and post-installation acceptance testing.	109786897 (365-374-198R10.05.54)
1675 LambdaUnite MSS TL1 Command Guide Describes the external TL1 interface for 1675 LambdaUnite MSS in terms of TL1 command, responses, and notification definitions.	109786889 (365-374-199R10.05.54)
1675 LambdaUnite MSS Optical Network Navigation System (ONNS) - Applications and Planning Guide Gives an ONNS functional overview, informs about network engineering and planning and ONNS management.	109786962 (365-375-026R10.05.54)
Optical Management System (OMS) Provisioning Guide for 1675 LambdaUnite MSS	109786947 (365-312-876R10.05.54)
1675 LambdaUnite MSS Electronic Download Library Package Contains all documents related to 1675 LambdaUnite MSS in electronic formats.	109786905 (365-374-181R10.05.54)
1675 LambdaUnite MSS Declaration of Conformity	109787234
1675 LambdaUnite MSS Software Release Description	This document is delivered with the NE software.
1675 LambdaUnite MSS Engineering and Ordering Information	Drawing ED8C948-10
1675 LambdaUnite MSS Interconnect and Circuit Information	Drawing ED8C948-20

These documents can be ordered at or downloaded from the Alcatel-Lucent Online Customer Support Site (OLCS) (https://support.alcatel-lucent.com) or through your Local Customer Support.

Technical support

For technical support, contact your local Alcatel-Lucent customer support team. See the Alcatel-Lucent Support web site (http://www.alcatel-lucent.com/support/) for contact information.

How to comment

To comment on this document, go to the Online Comment Form (http://infodoc.alcatel-lucent.com/comments/) or e-mail your comments to the Comments Hotline (comments@alcatel-lucent.com).

1 Introduction

Overview

Purpose

This chapter introduces the 1675 LambdaUnite MultiService Switch (MSS).

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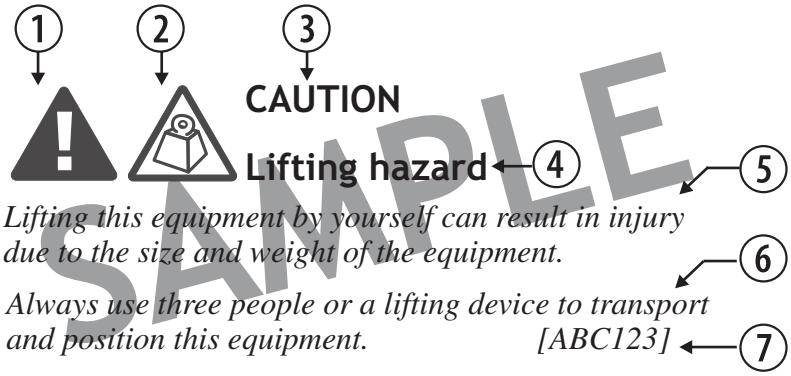
Structure of safety statements

Overview

This topic describes the components of safety statements that appear in this document.

General structure

Safety statements include the following structural elements:



SAMPLE

Item	Structure element	Purpose
1	Safety alert symbol	Indicates the potential for personal injury (optional)
2	Safety symbol	Indicates hazard type (optional)
3	Signal word	Indicates the severity of the hazard
4	Hazard type	Describes the source of the risk of damage or injury
5	Safety message	Consequences if protective measures fail
6	Avoidance message	Protective measures to take to avoid the hazard
7	Identifier	The reference ID of the safety statement (optional)

Signal words

The signal words identify the hazard severity levels as follows:

Signal word	Meaning
DANGER	Indicates an extremely hazardous situation which, if not avoided, will result in death or serious injury.
WARNING	Indicates a hazardous situation which, if not avoided, could result in death or serious injury.
CAUTION	Indicates a hazardous situation which, if not avoided, could result in minor or moderate injury.
NOTICE	Indicates a hazardous situation not related to personal injury.

1675 LambdaUnite MSS network solutions

1675 LambdaUnite MultiService Switch (MSS) is a global platform design supporting both the Synchronous Optical NETwork (SONET) standards as well as the Synchronous Digital Hierarchy (SDH) standards.

Using the experience Alcatel-Lucent gained with Time Division Multiplexing (TDM) products in several years of successful field trials, 1675 LambdaUnite MSS is the next generation of Alcatel-Lucent's high speed TDM equipment for various 10-Gbit/s applications built upon a cost optimized, high density and future proof platform. The feature set in this Release 10.05.54 has common points with existing SDH and SONET transport products as well as an advanced set of market-proven features. The feature set will continue to grow continuously in future releases. For planning reasons, major future features will also be mentioned within this manual.

Key features

Key features of 1675 LambdaUnite MSS include:

- 10-Gbit/s, 2.5-Gbit/s, 622-Mbit/s and 155-Mbit/s optical, 155-Mbit/s and 45- / 51-Mbit/s electrical synchronous interfaces
- Direct 1-Gbit/s Ethernet and 10-Gbit/s Ethernet WANPHY compatible optical data interfaces
- Direct 10-Gbit/s Ethernet LANPHY compatible optical data interfaces
- The GE10PL1/1A8 Gigabit Ethernet unit supports forwarding, encapsulation and mapping of Ethernet frames with lengths up to 9242 bytes (“jumbo frames”).
- DWDM, CWDM and passive WDM compatible optics
- Optical Network Navigation System (ONNS) (ASON), offering automatic connection setup and removal, automatic restoration, automatic topology discovery and dynamic network optimization in meshed topologies
- 2-fiber BLSR/MS-SPRing on 10-Gbit/s and 2.5-Gbit/s interfaces
- 4-fiber BLSR/MS-SPRing on 10-Gbit/s and on 2.5 Gbit/s interfaces with asymmetric ring support
- 4-fiber MS-SPRing with TransOceanic Protocol (“TOP”) on 10-Gbit/s and on 2.5-Gbit/s interfaces
- 4-fiber MS-SPRing with EXtra traffic re-establishment (“TOP+EX”) on 10-Gbit/s interfaces
- 1+1 linear APS / MSP for 10-Gbit/s, 2.5-Gbit/s, 622-Mbit/s and 155-Mbit/s interface ports, provisionable on existing cross connections
- 1:1 MSP (with Preemptible Protection Access) for 10-Gbit/s, 2.5-Gbit/s, 622-Mbit/s and 155-Mbit/s interface ports

- Unidirectional Path Switched Ring (UPSR) / Subnetwork Connection Protection (SNC/I and SNC/N) for all types of cross connections and any mix of supported interfaces
- Dual Ring Interworking (DRI, SONET) / Dual Node Interworking (DNI, SDH) between two BLSR / MS-SPRing / UPSR / SNCP protected rings
- Flexible, non-blocking VT1.5, VC-12, VC-3 (lower-order), STS-1/VC-3 (higher-order), STS-3c/VC-4, STS-12c/VC-4-4c, STS-48c/VC-4-16c and STS-192c/VC-4-64c cross-connection granularity
- Capacity of the main switching units (XC): 160 Gbit/s (3072 × 3072 STS-1 / 1024 × 1024 VC-4), respectively 320 Gbit/s (6144 × 6144 STS-1 / 2048 × 2048 VC-4), or 640 Gbit/s (12288 × 12288 STS-1 / 4096 × 4096 VC-4)
- Capacity of the lower-order switching units:
  - 15 Gbit/s (288 × 288 VC-3 (lower order), 6048 × 6048 VC-12 or 8064 × 8064 VT1.5.)
  - 40 Gbit/s (768 × 768 VC-3 (lower order), 16128 × 16128 VC-12 or 21504 × 21504 VT1.5)
- TransMUX feature: end-to-end connectivity across a transport network consisting of legacy PDH equipment and newer SONET transport nodes. Using the Portless TransMUX Card (TMUX2G5/1), it is possible to remap channelized DS3 signals carrying DS1s into VT1.5 signals and vice versa.
- Multiple Ring Closure
- *Telcordia*™ Management Support
- TL1 operations interface
- Manageable by Optical Management System (OMS) and by *WaveStar*® Craft Interface Terminal (CIT).

Applications

1675 LambdaUnite MSS is designed to cover a variety of 10-Gbit/s applications in the metro and backbone domain, based on the same common hardware and software for both SONET and SDH applications. 1675 LambdaUnite MSS can comprise one or more Terminal Multiplexer (TM) or Add/Drop Multiplexer (ADM) functions in a single node, but it can also act as an optical switch or cross-connect. As a combination of the ADM function with the XC function, also multi ring applications are supported to directly interconnect added/dropped tributaries between 10-Gbit/s and 2.5-Gbit/s rings.

Additionally with the flexible ONNS feature 1675 LambdaUnite MSS provides full Automatically Switched Optical Network (ASON) / Generalized Multi Protocol Label Switching (GMPLS) functionality, and as a hybrid node it allows to integrate ONNS domains into existing classical networks (please refer to “Optical Network Navigation System (ONNS)” (p. 2-66).)

The ability to support and efficiently interconnect multiple rings using a single network element combined with the ONNS integration capacities provide the basis for advanced networking capabilities and potential cost savings to a large amount.

Differentiators

The main differentiators of the product are:

- Minimized number of equipment types
  - Innovative high flexible architectural design
  - Full configuration & application coverage with single sub-rack
  - Easy, restriction-less configuration via simple I/O pack plugging
- All configurations based on common HW/SW components
  - Same sub-rack, same units, same SW
  - Upgrade just means plugging of additional cards and new configuration
  - Drastically reduced spare part, maintenance and training costs for operators
- Minimized floor space and equipment cost
  - Lowest foot print by ultra compact single sub-rack
  - Outstanding architectural support for pay as you grow
  - High interface density merging today's multiplexer farms into a single sub-rack
  - Multi Ring closure architecture eliminates the need for back-to-back ADM arrays
- Multi service support
  - Global product design covering SONET, SDH, transoceanic and flexible automatically switched transport network (ASON) / generalized multi protocol label switching (GMPLS) applications
  - Data transport with link capacity adjustment scheme (LCAS), and direct low cost 1 Gigabit Ethernet interfacing. Low cost VSR OC/STM optics and electrical STM-1 interfaces towards routers at full concatenation support
- Future proof investment
  - 640 Gbit/s switch capacity upgrade improves return on investment
  - 160 Gbit/s switch capacity provides pay-as-you-grow opportunities
  - Self aware ASON/GMPLS services including fast provisioning and restoration
  - Transparent services
  - Enables highest bandwidth for lowest cost/bit
- Full integration into Alcatel-Lucent management solution

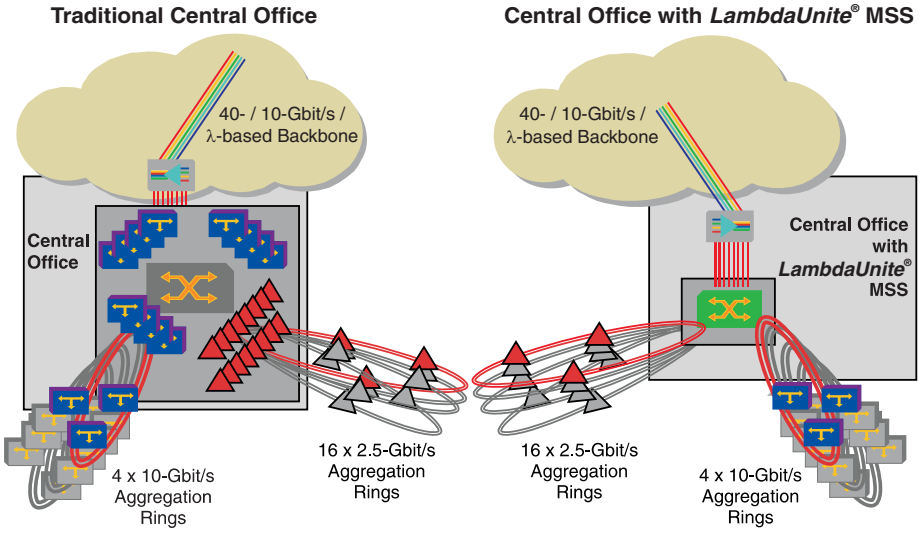
These features make the 1675 LambdaUnite MSS one of the most cost-effective, future-proof and flexible network elements available on the market today.

Comparison: central office

A comparison of a traditional central office and the vanguard central office with 1675 LambdaUnite MSS impressively shows its advantages:

- significantly reduced floor space requirements
- lowering relative equipment cost
- reducing power requirements
- reducing cabling effort
- reduced personnel training costs.

The following figure shows as an example a traditional central office consisting of 8 backbone feeder 10-Gbit/s ADMs, 4 metro 10-Gbit/s ADMs, 16 metro 2.5-Gbit/s ADMs and one 4/4 Digital Cross Connect (DXC) with 160 Gigabit cross connection capacity on the left. On the right, all these network elements are replaced by one 1675 LambdaUnite MSS network element.



Configurations

Because of the modular design of 1675 LambdaUnite MSS, the system can be configured as:

- One or multiple Terminal Multiplexer (TM) system working at 40 Gbit/s, 10 Gbit/s or 2.5 Gbit/s line rate
- One or multiple Add/Drop Multiplexer (ADM) system working at 40 Gbit/s, 10 Gbit/s or 2.5 Gbit/s line rate in rings or linear chains
- A Cross Connect (XC) system with 10-Gbit/s, 2.5-Gbit/s, 622-Mbit/s, 155-Mbit/s SONET/SDH interfaces, 10-Gbit/s Ethernet WANPHY compatible interfaces, 10-Gbit/s Ethernet interfaces, or 1-Gbit/s Ethernet interfaces.

- An ONNS node, as part of an ASON/GMPLS domain, offering automatic connection setup and removal, automatic restoration, automatic topology discovery and dynamic network optimization in meshed topologies.
- Any combination of the applications mentioned above, playing the role of a hybrid network element and linking ONNS domains with traditional networks.

Management

Like most of the network elements of the Alcatel-Lucent Optical Networking Group (ONG) product portfolio, 1675 LambdaUnite MSS is managed by Alcatel-Lucent Optical Management System (OMS). These user-friendly management system provides information and management of 1675 LambdaUnite MSS network elements on a subnetwork-level and a network level. A local craft terminal, the *WaveStar*® Craft Interface Terminal (CIT), is available for on-site, but also for remote operations and maintenance activities.

Interworking

1675 LambdaUnite MSS is a member of the suite of next generation transport products which have the prefix “Lambda” in their name. The system can be deployed together with other Alcatel-Lucent transport products, for example *Metropolis*® ADM, *Metropolis*® DMX, *WaveStar*® TDM 10G, *WaveStar*® ADM-16/1, *WaveStar*® OLS 1.6T, and *LambdaXtreme*™ Transport. This makes 1675 LambdaUnite MSS one of the main building blocks of today’s and future transport networks.

If necessary, you can coordinate with Alcatel-Lucent what products are able to interwork with 1675 LambdaUnite MSS.

The Alcatel-Lucent optical networking products family

Overview

Alcatel-Lucent offers a comprehensive range of intelligent, service-aware products coping with operators' diversified network transformation scenarios in core/backbone and metro/edge networks.

Core DWDM Systems

Core Dense Wavelength Division Multiplexing (DWDM) systems are designed for long-haul and ultra long-haul optical networking applications.

Available products include the following:

- Alcatel-Lucent 1625 LambdaXtreme Transport (LX)
- Alcatel-Lucent 1626 Light Manager (LM)
- Alcatel-Lucent 1830 Photonic Service Switch (PSS)
- Alcatel-Lucent *WaveStar*® OLS 1.6T

Management of Optical Networks

The management systems provide unified multiservice, multi-technology, multivendor, end-to-end management for optical networks.

Available products include the following:

- Alcatel-Lucent 1350 Optical Management System (OMS)
- Alcatel-Lucent 1340 Integrated Network Controller (INC)

Metro WDM Systems

Metro Wavelength Division Multiplexing (WDM) systems are designed for access, metro, and regional optical networking applications.

Available products include the following:

- Alcatel-Lucent 1692 Metrospan Edge (Metro CWDM System)
- Alcatel-Lucent 1694 Enhanced Optical Networking
- Alcatel-Lucent 1695 Wavelength Services Manager
- Alcatel-Lucent 1696 Metrospan (Metro WDM)
- Alcatel-Lucent 1830 Photonic Service Switch (PSS)

Multiservice SDH/SONET

SDH/SONET-based multiservice metro systems with integrated data-aware features provide multiprotocol aggregation and cross connect functionality.

Available products include the following:

- Alcatel-Lucent 1642 Edge Multiplexer
- Alcatel-Lucent 1642 Edge Multiplexer Compact (EMC)
- Alcatel-Lucent 1643 Access Multiplexer
- Alcatel-Lucent 1643 Access Multiplexer Small
- Alcatel-Lucent 1645 Access Multiplexer Compact (AMC)
- Alcatel-Lucent 1650 SMC STM-1/4 Multiservice Metro Node
- Alcatel-Lucent 1655 Access Multiplexer Universal
- Alcatel-Lucent 1660 SM STM-16/64 Optical Multi-Service Node for Metro Applications (OMSN)
- Alcatel-Lucent 1662 SMC STM-4/16 Compact Multiservice Node for Metro Networks (SMC)
- Alcatel-Lucent 1663 Add Drop Multiplexer-universal
- Alcatel-Lucent 1665 Data Multiplexer (DMX)
- Alcatel-Lucent 1665 Data Multiplexer Explore (DMXplore)
- Alcatel-Lucent 1665 Data Multiplexer Extend
- Alcatel-Lucent 1671 Service Connect (SC)
- Alcatel-Lucent 1677 SONET Link

Optical core switching

This area includes scalable optical systems for metro core and backbone applications, supporting add/drop multiplexer, broad- and wideband SDH/SONET and L2 switching functionalities from ring to meshed topologies, based on ASON/GMPLS dynamic control plane.

Available products include the following:

- Alcatel-Lucent 1675 Lambda Unite MultiService Switch (MSS)
- Alcatel-Lucent 1678 Metro Core Connect (MCC)

Optical CPE

Alcatel-Lucent optical customer premises equipment provides optical multiservice access to medium-large businesses.

Available products include the following:

- Alcatel-Lucent 1642 Edge Multiplexer
- Alcatel-Lucent 1642 Edge Multiplexer Compact (EMC)
- Alcatel-Lucent 1643 Access Multiplexer
- Alcatel-Lucent 1643 Access Multiplexer Small
- Alcatel-Lucent 1645 Access Multiplexer Compact (AMC)

- Alcatel-Lucent 1655 Access Multiplexer Universal
- Alcatel-Lucent 1665 Data Multiplexer Explore
- Alcatel-Lucent 1830 Photonic Service Switch (PSS)
- Alcatel-Lucent 1850 Transport Service Switch (TSS-3, TSS-5)

Optical Ethernet

Gigabit ethernet MAN products that feature extensive routing capabilities, flexible connectivity options and sophisticated management tools.

Available products include the following:

- Alcatel-Lucent 1642 Edge Multiplexer
- Alcatel-Lucent 1642 Edge Multiplexer Compact (EMC)
- Alcatel-Lucent 1643 Access Multiplexer
- Alcatel-Lucent 1643 Access Multiplexer Small
- Alcatel-Lucent 1645 Access Multiplexer Compact (AMC)
- Alcatel-Lucent 1650 SMC STM-1/4 Multiservice Metro Node
- Alcatel-Lucent 1655 Access Multiplexer Universal
- Alcatel-Lucent 1660 SM STM-16/64 Optical Multi-Service Node for Metro Applications (OMSN)
- Alcatel-Lucent 1662 SMC STM-4/16 Compact Multiservice Node for Metro Networks (SMC)
- Alcatel-Lucent 1663 Add Drop Multiplexer-universal
- Alcatel-Lucent 1665 Data Multiplexer (DMX)
- Alcatel-Lucent 1665 Data Multiplexer Explore (DMXplore)
- Alcatel-Lucent 1665 Data Multiplexer Extend
- Alcatel-Lucent 1671 Service Connect (SC)
- Alcatel-Lucent 1677 SONET Link
- Alcatel-Lucent 1678 Metro Core Connect (MCC)
- Alcatel-Lucent 1830 Photonic Service Switch (PSS)
- Alcatel-Lucent 1850 Transport Service Switch family (TSS)

Packet Transport

Multi-service packet transport products support any mix of traffic from 100% circuits to 100% packets.

Available products include the following:

- Alcatel-Lucent 1850 Transport Service Switch family (TSS)
- Alcatel-Lucent 9500 Microwave Packet Radio (MPR)

1675 LambdaUnite MSS profile

The 1675 LambdaUnite MSS system architecture is based on a central, fully non-blocking switching matrix (XC). Different switching matrix configurations with the following switching capacities are possible:

- 160 Gbit/s equals 3072 × 3072 STS-1 or 1024 × 1024 VC-4
- 320 Gbit/s equals 6144 × 6144 STS-1 or 2048 × 2048 VC-4
- 640 Gbit/s equals 12288 × 12288 STS-1 or 4096 × 4096 VC-4

For lower-order cross-connections an additional switching unit (LOXC) is available, with the following switching capacity:

- 15 Gbit/s equals 288 × 288 VC-3 (lower order), 6048 × 6048 VC-12 or 8064 × 8064 VT1.5.
- 40 Gbit/s equals 768 × 768 VC-3 (lower order), 16128 × 16128 VC-12 or 21504 × 21504 VT1.5.

Note that the LOXC pack is to be used in specific slots, refer to “Port location rules ” (p. 6-11).

1675 LambdaUnite MSS provides advantageous pay-as-you-grow opportunities, as the upgrade to a more powerful configuration requires simply the replacement of the switching units.

The system provides 32 universal slots, which can be flexibly configured according to your service requirements with optical interface units (synchronous and Ethernet). Besides these optical interface units, 1675 LambdaUnite MSS supports also 155-Mbit/s and 45- /51-Mbit/s electrical interface units that can be inserted into the upper row of the sub-rack.

The mix and the number of 10-Gbit/s, 2.5-Gbit/s 2-fiber/4-fiber rings and linear links is only limited by the maximum number of slots. This makes 1675 LambdaUnite MSS a highly flexible system and allows for a broad variety of different configurations.

The dimensions of the sub-rack are: 950 × 500 × 545 mm (37.4 × 19.7 × 21.5 in) (H × W × D). Therefore, two complete network elements fit in one rack. The sub-racks are in accordance with Rec. ETS 300 119-4 and *Telcordia*™ and can be mounted in ETSI racks (2200 mm (86.6 in) and 2600 mm (102.4 in) height) and *Telcordia*™ racks (2125 mm (83.7 in) height).

1675 LambdaUnite MSS sub-rack

The following figure illustrates the 1675 LambdaUnite MSS sub-rack in top-position in an ETSI rack:

2 Features

Overview

Purpose

This chapter briefly describes the features of 1675 LambdaUnite MultiService Switch (MSS).

For more information on the physical design features and the applicable standards, please refer to Chapter 6, “System planning and engineering” and to Chapter 10, “Technical specifications”.

Standards compliance

Alcatel-Lucent's SONET and SDH products comply with the relevant European Telecommunication Standardization Institute (ETSI), *Telcordia*™ Technologies, and International Telecommunications Union - Telecommunication standardization sector (ITU-T) standards. Important functions defined in SONET and SDH Standards such as the Data Communications Channel (DCC), the associated 7-layer OSI protocol stack, the SONET and SDH multiplexing structure and the Operations, Administration, Maintenance, and Provisioning (OAM&P) functions are implemented in Alcatel-Lucent's product families.

Alcatel-Lucent's intelligent control plane, implemented in 1675 LambdaUnite MSS as ONNS, is based on standards discussed in the ITU-T Automatically Switched Optical Network (ASON), the Internet Engineering Task Force (IETF) Generalized Multi Protocol Label Switching (GMPLS) Forum and the Optical Internet Forum (OIF).

Alcatel-Lucent is heavily involved in various study groups with ITU-T, *Telcordia*™ and ETSI work creating and maintaining the latest worldwide SONET and SDH standards. 1675 LambdaUnite MSS complies with all relevant and latest *Telcordia*™, ETSI and ITU-T standards and supports both, SONET and SDH protocols in a single hardware-software configuration.

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Physical interfaces

Overview

Purpose

This section provides information about all kinds of physical external interfaces of 1675 LambdaUnite MSS. For detailed technical data and optical parameters of the interfaces please refer to Chapter 10, “Technical specifications”.

1675 LambdaUnite MSS supports a variety of configurations as described in the previous chapter, due to its flexible architecture within the same subrack with a single common SW load. The choice of synchronous and data interfaces described below provides outstanding transmission flexibility and integration capabilities.

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Synchronous/electrical interfaces

SONET/SDH transmission interface overview

1675 LambdaUnite MSS supports the whole range of interfaces from 10 Gbit/s down to 45 Mbit/s. All optical interface units support SONET and SDH formatted signals.

The following synchronous interfaces are available in the present release:

- 10-Gbit/s long reach hot-pluggable optical module (80 km), 1550 nm
- 10-Gbit/s intermediate reach / short haul hot-pluggable optical module (40 km), 1550 nm
- 10-Gbit/s intra-office interface hot-pluggable optical module (600 m), 1310 nm
- 10-Gbit/s optical interface for direct *LambdaXtreme*™ Transport interworking, 128 wavelengths, tuneable
- 10-Gbit/s optical interface for direct *WaveStar*® OLS 1.6T interworking, 80 wavelengths
- 10-Gbit/s Extended Form-factor Pluggable (XFP) interface module (10 km), 1310 nm
- 10-Gbit/s Extended Form-factor Pluggable (XFP) interface module (40 km), 1550 nm
- 10-Gbit/s Extended Form-factor Pluggable (XFP) interface module (80 km), 1550 nm
- Extended Form-factor Pluggable (XFP) with OC192/STM-64 DWDM and pWDM compatible 1.5 micron optics (intermediate reach/short-haul applications), 16 colors
- 2.5-Gbit/s long reach optical Small Form Factor Pluggable (SFP) interface module (80 km), 1550 nm
- 2.5-Gbit/s long reach optical SFP interface module (40 km), 1310 nm
- 2.5-Gbit/s short reach / intra-office optical SFP interface module (2 km), 1310 nm
- 2.5-Gbit/s SFP interface, compatible for DWDM and pWDM long-haul applications, 16 colors
- 2.5-Gbit/s long reach optical interface (40 km), 1,5 µm, pWDM compatible, 32 wavelengths
- 2.5-Gbit/s long reach optical interface (80 km), 1,5 µm, CWDM compatible optics, 8 colors
- 2.5-Gbit/s short reach optical interface (40 km), 1,5 µm, CWDM compatible optics, 8 colors
- 2.5-Gbit/s transparent optical transmission unit, for use with optical SFP interface modules
- 622-Mbit/s long reach / long haul optical SFP interface module (40 km), 1310 nm
- 622-Mbit/s intermediate reach / short haul optical SFP interface module (15 km), 1310 nm
- 622-Mbit/s intermediate reach / short haul optical interface (15 km), 1310 nm
- 155-Mbit/s long reach / long haul optical SFP interface module (40 km), 1310 nm

- 155-Mbit/s intermediate reach / short haul optical SFP interface module (15 km), 1310 nm
- 155-Mbit/s intermediate reach / short haul optical interface (15 km), 1310 nm
- 155-Mbit/s intra-office interface for electrical STM-1 signals
- 51-Mbit/s intra-office interface for electrical EC1 signals
- 45-Mbit/s intra-office interface for electrical DS3 signals

Data interfaces

Gigabit Ethernet interface

1675 LambdaUnite MSS supports optical 1-Gbit/s (1000BASE) Ethernet interfaces, as part of the *TransLAN*™ Ethernet SDH Transport Solution.

Three optical 1-Gbit/s Ethernet interface types are supported on the GE10PL1/1A8:

- SX, the short reach interface,
- LX, the long reach interface and
- ZX long reach interface (1550nm long haul, single mode fiber, with a target distance of 70km)

The GE10PL1/1A8 supports up to 8 SFP's with these interfaces.

Alternatively a GE10PL1/1A8 supports one XFP, three optical 10-Gbit/s Ethernet interface types:

- 10GbE 10km 1310nm
- 10GbE 40km 1550nm
- 10GbE 80km 1550nm

The SX and LX interfaces are still supported by the GE1/SX4 and GE1/LX4 resp., with four ports each.

These interfaces are in accordance with IEEE 802.3-2000 Clause 38. To optimize communication the Ethernet interface supports flow control and auto-negotiation, as defined in Section 37 of IEEE 802.3. This feature, among others, enables IEEE-802.3 compliant devices with different technologies to communicate their enhanced mode of operation in order to inter-operate and to take maximum advantage of their abilities.

The GE1 interfaces provide enhanced flexibility for Gigabit Ethernet packet routing, for example virtual concatenation, multipoint MAC bridge, VLAN trunking and Spanning Tree Protocol (STP) with Generic VLAN Registration Protocol (GVRP). For further information please refer to “Ethernet features” (p. 2-18), to “Gigabit Ethernet short reach circuit pack” (p. 10-21) and to “Gigabit Ethernet long reach circuit pack” (p. 10-23).

Each GE1 circuit packs offer four bidirectional 1000BASE Ethernet LAN ports with LC connectors. If 1675 LambdaUnite MSS is mounted in a rack with doors you must use fiber connectors with angled boots.

10-Gbit/s Ethernet WANPHY interface

The 10-Gbit/s synchronous intermediate reach / short haul interface (40 km) operates compliant to the 10-Gbit/s Wide Area Network Physical (WANPHY) Ethernet protocol, accepting some minor limitations. For further information please refer to “Optical transmission units OP10” (p. 4-17).

10-Gbit/s Ethernet LR LANPHY interface

A 10 Gigabit Ethernet interface supporting 10GBASE-LR interface (1310 nm long haul, multimode or single mode fiber) according IEEE Draft P802.3ae/D3.0 (2001 Edition) Clause 49 and 52. Full duplex only is supported.For further information please refer to "Gigabit Ethernet transmission unit GE10PL1/1A8 " (p. 4-24).

10-Gbit/s Ethernet ER LANPHY interface

A 10 Gigabit Ethernet interface supporting 10GBASE-ER interface (1550 nm long haul, single mode fiber) according IEEE Draft P802.3ae/D3.0 (2001 Edition) Clause 49 and 52. Full duplex only is supported. For further information please refer to "Optical transmission units OP10" (p. 4-17).

10-Gbit/s Ethernet 80 LANPHY interface

A 10 Gigabit Ethernet interface supporting 10GBASE-ER interface (1550 nm long haul, single mode fiber) according IEEE Draft P802.3ae/D3.0 (2001 Edition) Clause 49 and 52. Full duplex only is supported. For further information please refer to "Optical transmission units OP10" (p. 4-17).

Timing interfaces

Synchronization interfaces

1675 LambdaUnite MSS provides two physical timing inputs and two timing outputs. For SONET applications, DS1 (B8ZS) *Telcordia*™ timing signals (SF or ESF) are supported. In SDH networks, ITU-T compliant 2,048 kHz and 2 Mbit/s (framed or unframed) timing signals can be used as inputs and outputs, see also "Timing features" (p. 2-60).

User byte and orderwire interfaces

User byte and orderwire interfaces

1675 LambdaUnite MSS provides six physical overhead access interface ports, using the E1, E2 and F1 bytes on the 10-Gbit/s- and on the 155-Mbit/s interfaces. Four ports are configurable to operate in G.703 or in V.11 mode, and two ports only support V.11 mode. In V.11 mode the interface supports frame clock and bit clock. The interfaces operate in contradirectional mode (timing provided by transport system).

Operations interfaces

Operations interfaces

1675 LambdaUnite MSS is equipped with the following operations interfaces:

- Station alarm interface which drives three rack top lamps (indicating critical/prompt, major/deferred and minor/informal alarms)
- LEDs on each controlled circuit pack (red fault LED, green status LED)
- One LED on the double density parent board for each plug-in module (red fault LED)
- User panel with several LEDs to indicate alarms and status, an alarm cut-off (ACO) button, an LED test button, and one LAN interface (LAN 1) to *WaveStar*® Craft Interface Terminal (CIT) or Optical Management System (OMS).
- Eight miscellaneous discrete inputs and eight miscellaneous discrete outputs (MDI/MDO) for control and supervision purposes
- Two additional LAN connectors (LAN 2 and 3) on the rear side for management systems (e.g. OMS or *WaveStar*® CIT), and one (LAN 4) reserved *LambdaXtreme*™ Transport interworking.

Power interfaces and grounding

Power supply

Two redundant power supply inputs are available per shelf; the supply voltage is -48 V DC to -60 V DC nominal. The system powering meets the ETSI requirements ETS 300132-2, *Telcordia*™ Technologies General Requirements GR-1089-CORE and GR-499-CORE. Operation range is -40 V DC to -72 V DC.

For detailed information about the power consumption please refer to "System power consumption" (p. 6-4) and to "Weight and power consumption" (p. 10-40).

System grounding

System grounding can be done according to

- ETSI requirements in ETS 300253 (mesh ground with the battery return connected to ground),
- *Telcordia*™ GR-1089-CORE.

Transmission features

Overview

Purpose

This section gives an overview of the transmission related features of the 1675 LambdaUnite MultiService Switch (MSS). For more detailed information on the implementation of the switch function in the NE please refer to Chapter 4, “Product description”.

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Cross-connection features

Cross-connection rates

1675 LambdaUnite MSS supports unidirectional and bidirectional cross-connections for VT1.5/VC-12, VC-3 (lower-order), STS-1/VC-3 (higher-order), STS-3c/VC-4, STS-12c/VC-4-4c, STS-48c/VC-4-16c and STS-192c/VC-4-64c payloads. The assignment of unidirectional cross-connection does not occupy or restrict cross-connection capacity or cross-connection types in the reverse direction.

Cross-connection capacity

The following switching units are available with the present release of 1675 LambdaUnite MSS:

- the XC160 with a cross-connection capacity of 160 Gbit/s in total (3072 × 3072 STS-1 / 1024 × 1024 VC-4)
- the XC320 with a cross-connection capacity of 320 Gbit/s in total (6144 × 6144 STS-1 / 2048 × 2048 VC-4)
- the XC640 with a cross-connection capacity of 640 Gbit/s in total (12288 × 12288 STS-1 / 4096 × 4096 VC-4)
- the LOXC for lower-order cross-connections with a capacity of 15 Gbit/s in total (288 × 288 VC-3 (lower order), 6048 × 6048 VC-12 or 8064 × 8064 VT1.5), to be used in addition to the main XC switching units.
- the LOXC (two slot wide) for lower-order cross-connections with a capacity of 40 Gbit/s in total (768 × 768 VC-3 (lower order), 16128 × 16128 VC-12 or 21504 × 21504 VT1.5), to be used in addition to the main XC switching units.Only in combination with XC640.
- the LOXC (three slot wide) for lower-order cross-connections with a capacity of 40 Gbit/s in total (768 × 768 VC-3 (lower order), 16128 × 16128 VC-12 or 21504 × 21504 VT1.5), to be used in addition to the main XC switching units.Only in combination with XC320 and XC640.

Lower-order cross-connections (LOXC)

The main purpose of the LOXC function is to cross connect LO tributaries on the following levels:

- VT1.5 (SONET),
- VC-12 (SDH),
- (LO) VC-3 (SDH).

The LO tributaries are created while a lower-order cross-connection is successfully created (implicit method) or by substructuring command (explicit method).

The following HO signals can be substructured:

- STS-1 (SONET) carrying VT1.5 signals,
- VC-4 (SDH) carrying LO VC-3 or VC-12 signals (or a mix of it).

Note that any type of contiguously concatenated signal is not supported (despite VC-4 could be interpreted as STS-3C).

Substructuring of disconnected HO signals (VC4 [of a SDH port] or STS1 [of a SONET port])

The system supports to create/delete a lower-order signal (default) substructure for disconnected HO tributaries. It also supports to adapt the default substructure to a mix of VC-12 and VC-3 if needed (SDH only, the transmission rate for each third of the substructure can be configured separately).

For SDH, it is additionally possible to adapt each third of the substructure individually to be a set of VC-12 (21 times) or a single VC-3, also individually per direction.

After lower-order cross-connections are established with the predefined LO tributaries the transmission rate for the related third cannot be changed (in this direction).

For SDH, independent whether the substructure has been created by the implicit or explicit method, the substructure will automatically adapt to the required transmission rate if still possible.

The resulting lower-order tributaries can be non-intrusively monitored and the higher-order path termination can be provisioned.

Connectivity types

The LOXC function supports following types of cross-connections:

- uni-/bidirectional cross-connections (protected and unprotected),
- 1:2 broadcasting,
- AU3/AU4 conversion.

AU-3/AU-4 conversion cross-connections

AU-3/AU-4 conversion cross-connections allow the conversion of a higher-order STS-1 (SONET) to a lower-order VC-3 (SDH), and the mapping into a higher-order VC-4. From a cross-connection provisioning point of view, an AU-3/AU-4 conversion cross-connection is a cross-connection between a higher-order STS-1 and a lower-order VC-3 tributary.

The following types of conversion cross-connections are supported:

- SONET (STS-1) to SDH (AU-4) conversion
- DS3 signal to TU-3/VC-4 conversion

Important! Conversion cross-connections are only possible if a lower-order cross-connection unit of type *LOXC40G2S/1* or *LOXC40G3S/1* is used and the LOXC interface standard is set to SDH.

The source or destination of a conversion cross-connection may *not* reside on one of the following types of port units:

- OPT2G5
- GE1
- GE10PL1

Transparency behavior

Implementation details of the behavior of the path overhead bytes in the direction from SDH to SONET are shown in the following table:

SDH to SONET (LO-VC-3 to STS-1)		SONET to SDH (STS-1 to LO-VC-3	
Byte	**Behavior**	**Byte**	**Behavior**
J1:	Transparent	J1:	Transparent
B3:	Non-Transparent, reinserted	B3:	Transparent
C2:	Transparent	C2:	Transparent
G1:	Non-Transparent	G1:	Transparent
F2:	Transparent	F2:	Transparent
H4:	Transparent	H4:	Transparent
F3/Z3:	Transparent	F3/Z3:	Transparent
K3/Z4:	Transparent	K3/Z4:	Transparent
N1:	Transparent	N1:	Transparent

Bridged cross-connections (broadcast)

An existing cross-connection can be bridged by adding a unidirectional cross-connection from the existing input port to a second output port, resulting in a 1:2 broadcast. 1675 LambdaUnite MSS supports bridging for each of the supported cross-connection rates without impairing the existing signal. Conversely, either broadcast leg can be removed without impairing the remaining cross-connected signal.

Rolling cross-connections

The system supports facility rolling for all allowed cross-connection rates. Rolling means that for an existing cross-connection a new source can easily be selected, i.e. the cross-connection can be “rolled” to this new source without traffic interruption.

The rolling of lower-order cross-connections (VT1.5, VC-12, lower-order VC-3) is supported on the LOXC40G2S/1 and LOXC40G3S/1.

Converting cross-connections

The system supports to convert unprotected LO crossconnections into SNCP-/UPSR-protected crossconnections without need to remove them and vice versa.

When converting an SNCP-/UPSR-protected crossconnection into an unprotected crossconnection, both, worker leg or protection leg, can be dropped.

Fully non-blocking cross-connections

The system is strictly non-blocking for all supported cross-connection arrangements (point-to-point, multi-cast allowable port type connections, etc.) among all transmission interfaces within the cross-connection capacity of the system. Thus, within the system cross-connection capacity, a desired cross-connection can always be established, regardless of the state of other cross-connections. New cross-connections and/or disconnections do not cause any bit errors on existing cross-connections.

SONET pipe mode cross-connections

The system supports STS-3, STS-12, STS-48 and STS-192 unidirectional and bidirectional pipe-mode cross-connections. The STS-3 pipe mode cross-connection allows STS-3c or multiple STS-1 transport without extra provisioning. The STS-12 pipe-mode cross-connection allows STS-12c or multiple STS-3c / STS-1 transport or any mix without extra provisioning. The STS-48 pipe mode cross-connection allows STS-48c or multiple STS-12c / STS-3c / STS-1 transport or any mix without extra provisioning. The STS-192 pipe mode cross-connection allows STS-192c or multiple STS-48c / STS-12c / STS-3c / STS-1 transport or any mix without extra provisioning.

Pipe-mode processing can be configured at the port level. A pipe-mode cross-connection is created by provisioning a cross-connection with an input leg within a pipe-mode port. Path fault management and performance monitoring are performed independently for each of the path-level constituent signals within a pipe-mode port.

Inter-connection between SONET- and SDH- structured ports

The 1675 LambdaUnite MSS switching matrix supports an inter-connection between SONET and SDH structured ports: SONET signals can be cross-connected to the relative SDH signals and vice versa.

Inter-connection between SONET/SDH networks and ASON/GMPLS domains

Ports configured as Optical Network Navigation System (ONNS) edge ports (SONET/SDH structured ports to the outside) can be cross-connected with mere ONNS(I-NNI) ports. Thus 1675 LambdaUnite MSS allows the inter-connection between SONET/SDH networks and ASON/GMPLS domains.

Unequipped signal insertion

In case an STS/VC is not cross-connected, an unequipped signal is inserted in downstream direction.

Transmultiplexing

With the TransMUX feature, 1675 LambdaUnite MSS provides end-to-end connectivity across a transport network that consisting of legacy PDH equipment and newer SONET transport nodes. Using the Portless TransMUX Card (TMUX2G5/1), it is possible to remap channelized DS3 signals carrying DS1s into VT1.5 signals and vice versa.

Ethernet features

The Gigabit Ethernet interface provides an enhanced feature set for flexible Ethernet over SONET/SDH transport.

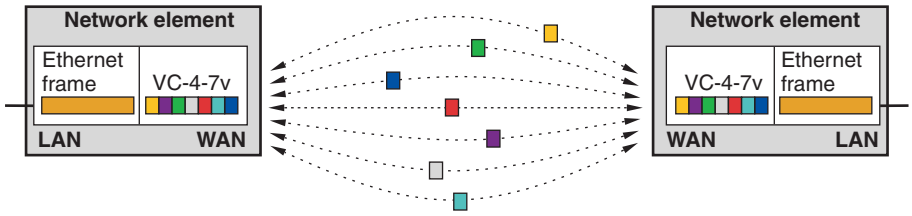
This section describes in brief some related features of 1675 LambdaUnite MSS:

- Virtual concatenation
- Link Capacity Adjustment Scheme (LCAS)
- Virtual LAN
- Repeater mode
- VLAN tagging
- Multipoint mode
- VLAN trunking
- Spanning Tree Protocol (STP)
- Rapid spanning tree protocol (rSTP)
- Generic VLAN Registration Protocol
- Link Pass Through (LPT)

Virtual concatenation

The GE1 interface supported by 1675 LambdaUnite MSS allows you to transport Gigabit Ethernet (GbE) signals over SONET/SDH networks by encapsulating Ethernet packets in virtually concatenated Synchronous Payload Envelopes (SPEs, SONET) or Virtual Containers (VCs, SDH).

The following figure shows the principle of virtual concatenation in a point-to-point Gigabit Ethernet (GbE) application example. Protection of the STS-1-Kv/VC-4-kv traffic is possible via UPSR/SNCP, via 1+1 line APS / 1+1 MSP and in ring topologies via BLSR/MS-SPRing protection schemes.



The H4 POH byte is used for the sequence and multi-frame indication specific for virtual concatenation.

Due to different propagation delay of the virtual containers a differential delay will occur between the individual virtual containers. This differential delay has to be compensated and the individual virtual containers have to be re-aligned for access to the contiguous payload area. The 1675 LambdaUnite MSS re-alignment process covers at least a differential delay of 32 ms.

Link Capacity Adjustment Scheme

Link Capacity Adjustment Scheme (LCAS) is an extension to virtual concatenation that allows dynamic changes in the number of STS-1/VC-4 channels per connection. In case channels are added or removed by management actions this will happen without loosing any customer traffic. LCAS allows a bandwidth service with scalable throughput in normal operation mode. In case of failure the connection will not be dropped completely only the affected STS-1s/VC-4s. The remaining channels will continue carrying the customer traffic. The implemented LCAS provides automatic decrease of bandwidth in case of link failure and reestablishment after link recovery.

The following unidirectional and bidirectional virtual concatenations are supported:

- STS-1-Kv, where K = 1 up to 21 in steps of 1
- VC-4-Kv, where K = 1 up to 7 in steps of 1.

The GE1 circuit pack allows to transport Gigabit Ethernet signals efficiently over SONET or SDH networks by encapsulating Ethernet packets in virtually concatenated VC-4 or STS-1s, using the LCAS. This protection-by-load-sharing feature allows for efficient use of protection bandwidth, that can be added/removed hitlessly for Ethernet applications.

The GE10PL1/1A8 Gigabit Ethernet unit supports the Link Capacity Adjustment Scheme (LCAS) acc. to the ITU-T Rec. G.7042/Y.1305 (02/2004) on all VCGs for all rates. LCAS can individually be enabled or disabled per VCG.

Virtual LAN

Virtual Local Area Networks (VLANs) can be used to establish broadcast domains within the network as routers do, but they cannot forward traffic from one VLAN to another. Routing is still required for inter-VLAN traffic. Optimal VLAN deployment is predicated on keeping as much traffic from traversing the router as possible.

VLAN supports the following advantages:

- Easy provisioning of VLANs
- Consistency of the VLAN membership information across the network
- Optimization of VLAN broadcast domains in order to save bandwidth
- Isolated service for different customers.

The operator configures VLANs on LAN ports, and GVRP takes care of configuring VLANs on Wide Area Network (WAN) ports in the most optimized way.

Repeater mode

The simplest form of Ethernet transport is to transparently forward all frames on the WAN that are transmitted by the end user via the LAN; this mode is called repeater mode (also referred to as promiscuous mode or no-tag mode). In this mode minimal provisioning is necessary.

VLAN tagging

Refer to "Tagging schemes" (p. 2-21).

Multipoint mode

1675 LambdaUnite MSS supports Ethernet multipoint applications for specific network topologies, for example if an end user has more than 2 sites that need to be connected. It is also possible to support multiple end users on the same Ethernet network, sharing the available bandwidth on the WAN ports over the SONET/SDH network.

The virtual switch implemented on the GE1 interface is a logical grouping of Ethernet ports and Virtual Concatenation Group (VCG) ports that share interconnect and a common set of properties. The virtual switch is automatically instantiated as soon as the VLAN tagging mode is set to IEEE802.1Q multipoint mode. All 4 LAN ports and all 4 WAN ports of the GE1 circuit pack are part of the single virtual switch.

Regarding multipoint Ethernet service a more general terminology is needed to cover the functions of LAN and WAN ports. The new application focused terms are:

- customer LAN ports (the default for LAN ports)
- network WAN ports (the default for WAN ports)
- network LAN ports
- customer WAN ports.

By default, network ports participate in STP and GVRP, and customer ports have a PVID and a Valid VLAN list assigned. LAN ports default to customer port role and WAN ports to network role. All default values can be overridden.

VLAN trunking

Trunking applications are those applications where traffic of multiple end users is handed-off via a single physical Ethernet interface to a router or switch for further processing. This scenario is also called "back-hauling", since all traffic is transported to a central location, e.g. a point-of-presence (PoP) of a service provider. Trunking applications can be classified into two topology types, trunking in the hub-node and distributed aggregation in the access network.

Tagging schemes

1675 LambdaUnite MSS systems support these tagging schemes:

- IEEE 802.1Q VLAN tagging
- Transparent tagging

IEEE 802.1Q VLAN tagging

All frames on the network links have a single VLAN tag. This tag is either the tag that was created by the end user equipment; or it is inserted on the ingress "customer" port (the default VLAN id) by the *TransLAN*® switch. On egress customer ports the earlier inserted VLAN tag is removed if a default VLAN id is provided on that port; it should be the same VLAN id as on the associated ingress ports. To ensure customer isolation, you must allocate VLANs to customers and to the customer ports, and ensure that VLANs don't overlap. The IEEE 802.1Q VLAN tagging scheme supports VLAN trunking, i.e. traffic from multiple different end users is multiplexed over one physical interface towards an IP router in an ISP POP (cf. "VLAN trunking" (p. 2-20)). End user identification and isolation is done via the VLAN tag.

Transparent tagging

The "Transparent tagging" scheme, also known as "Double tagging" or "VPN tagging", is a Alcatel-Lucent proprietary tagging scheme.

All frames that enter the network are prefixed with a customer identification (CID) tag. Each customer port on the network is assigned a CID. As all frames are prefixed, there is no difference between end user frames that were originally VLAN tagged or untagged, only the CID is used in Ethernet switching decisions. There is no need for an operator to coordinate the end user VLAN schemes, but CIDs must be assigned consistently per customer over the whole Ethernet network. VLAN trunking is not supported, due to the proprietary tagging scheme.

Spanning Tree Protocol

The Spanning Tree Protocol (STP) is a standard Ethernet method for eliminating loops and providing alternate routes for service protection. Standard STP depends to information sharing among Ethernet switches/bridges to reconfigure the spanning tree in the event of a failure. The STP algorithm calculates the best loop-free path throughout the network. STP defines a tree that spans all switches in the network; it e.g. uses the capacity available bandwidth on a link (path cost) to find the optimum tree. It forces redundant links into a standby (blocked) state. If a link fails or if a STP path cost changes the STP algorithm reconfigures the Spanning Tree topology and may reestablish previously blocked links. The STP also determines one switch that will be the root switch; all leaves in the Spanning Tree extend from the root switch.

Rapid spanning tree protocol

Rapid Spanning Tree Protocol (rSTP) reduces the time that the STP protocol needs to reconfigure after network failures. Instead of several tens of seconds, rSTP can reconfigure in less than a second. The actual reconfiguration time depends on several parameters, the two most prominent are the network size and complexity. IEEE802.1w describes the standard implementation for rSTP.

Generic VLAN Registration Protocol

Generic VLAN Registration Protocol (GVRP) is an additional protocol that simplifies VLAN assignment on network ports and ensures consistency among switches in a network. Further it prevents unnecessary broadcasting of Ethernet frames by forwarding VLAN frames only to those parts of the network that have customer ports with that VLAN ID.

The operator configures VLANs on customer ports, and GVRP will take care of configuring VLANs on network ports - in the most optimized way. Note that GVRP and Spanning Tree Protocol interact with each other. After a stable Spanning Tree is determined (at initialization or after a reconfiguration due to a failure) the GVRP protocol will recompute the best VLAN assignments on all network ports, given the new Spanning Tree topology.

The provisioned VLANs on customer ports are called static VLAN entries; the VLANs assigned by GVRP are called dynamic VLAN entries. The dynamic VLAN entries need not be stored in NE's database.

GVRP can be enabled (default) or disabled per virtual switch:

- In the enabled case up to 247 VLANs can be supported through GVRP; an alarm will be raised if more then 247 VLANs are provisioned on an Ethernet network. This limitation depends on the processor performance.
- If GVRP is disabled up to 4093 VLANs per Gigabit Ethernet circuit pack port are supported.

Further reading

For further information about the hardware implementation please refer to "Gigabit Ethernet short reach circuit pack" (p. 10-21) and "Gigabit Ethernet long reach circuit pack" (p. 10-23).

Link Pass Through (LPT)

The Gigabit Ethernet interfaces on Private Line card GE10PL1 support the Link Pass Through (LPT) mode.

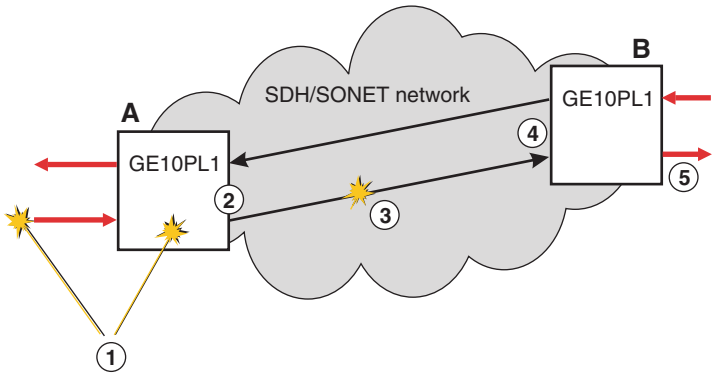
The LPT mode can be used to enable or improve network protection schemes on the equipment external to the *TransLAN*® systems.

The LPT mode can be enabled or disabled per GbE port.

Important! The LPT mode is only supported on GbE ports (LAN ports) that operate in a *strict one-to-one association* with a WAN port using *GFP encapsulation*.

Link Pass Through (LPT) is not supported by the Gigabit Ethernet transmission unit GE1.

Please refer to the following figure for clarification and further reference.



If an upstream GbE fiber failure (e.g. a fiber cut) or equipment failure (1) is detected at node A, then a Client Signal Fail (CSF) indication is inserted into the GFP-encapsulated signal (2). If such a CSF indication or a Server Signal Fail (SSF) condition (due to a failure on the transmission line (3) for example) is detected at node B (4), then this can be used to trigger the inhibition of the transmitter at the LAN egress port (5) as a consequent action.

Important! Please note that once the LPT mode is activated on a LAN port, any requests to have more than two ports on the associated virtual switch will be rejected. In order to increase the number of ports on the virtual switch, the LPT mode first has to be disabled.

CSF indication due to start of autonegotiation (1 GbE interfaces)

Please note that the subsequently described functionality is available for 1 GbE interfaces only, because autonegotiation is supported for 1 GbE interfaces only.

In addition to the failure conditions described above, an Ethernet link (LAN link) may also be down due to autonegotiation being (re-)started and not yet successfully completed.

Therefore, a (re-)start of autonegotiation leads to the insertion of a CSF indication until autonegotiation is successfully completed and the LAN link is up. If the autonegotiation ends in an autonegotiation mismatch condition (signaled by means of a `LAN Auto Negotiation Mismatch` (LANANM) alarm), then the insertion of the CSF indication will not be stopped until the autonegotiation mismatch condition is cleared.

CSF indication due to the presence of a remote fault indication (10 GbE interfaces)

While no autonegotiation is defined for 10 GbE interfaces, a mechanism called Link Fault Signaling (LFS) is defined to transport remote fault information between the link partners.

Therefore, for 10 GbE interfaces, a CSF indication will be inserted as long as a remote fault indication is received via LFS.

Supported frame sizes

This table provides an overview of the supported frame sizes per Gigabit Ethernet unit:

Gigabit Ethernet unit		Supported frame sizes
GbE	GE1/SX4 (KFA13)	up to 1536 bytes (*including* VLAN fields). This means: • up to 1536 bytes in repeater mode • up to 1532 bytes in single-tagging mode • up to 1528 bytes in double-tagging mode
	GE1/LX4 (KFA532)	
GbE/ 10GbE	GE10PL1/1A8 (KFA720)	up to 9242 bytes (“jumbo frames”)

Transparent SONET/SDH transport

With the transparent 2.5-Gbit/s interface units, the so called OPT2G5/PAR3, 1675 LambdaUnite MSS uncloses a broad range of applications, as described in "Clear channel topologies" (p. 3-10).

Transparency features

In the OPT2G5 the concept of virtual concatenation (refer to "Ethernet features" (p. 2-18)) is employed to transport client SONET/SDH signals (SPE/VC and transport overhead) transparently over SONET/SDH networks; this functionality is also known as G.modem.

In the present release 1675 LambdaUnite MSS provides 3 ports per OPT2G5 for OC-48/STM-16 synchronous signals, fed via SFPs. These client signals are split up and transported in 17 STS-3c/VC-4 containers over the server network, to be finally re-assembled and handed over as OC-48/STM-16 signals.

The main transparency features are:

- Full data transparency for signal payload and transport overhead
- Protection scheme independent
- Client signal timing transparency: Under certain limitations, the egressing signal can be used by subsequent equipment as a line timing source. The limitations relate to aspects like the amount of mapping/demapping stages and the number of pointer processor functions in between.

For further information please refer to "Transparent optical transmission units OPT2G5/PAR3" (p. 4-20), and to the *1675 LambdaUnite MSS User Provisioning Guide*.

Forward error correction

Forward error correction (FEC) makes it possible to improve the optical signal-to-noise ratio (OSNR), and thus to lower the bit error ratio, of an optical line signal by adding redundant information. This redundant information can then be used to correct bit errors that unavoidably occur when an optical line signal is transmitted over longer distances over an optical fiber.

Forward error correction types

There are two types of Forward Error Correction:

- In-band FEC (also referred to as “multibit FEC”)

  The redundant information is stored and transported in previously unused overhead bytes, the framing structure as well as the bit rate remain unchanged.
- Out-of-band FEC (also referred to as “strong FEC”)

  The redundant information is appended to the original signal resulting in an optical signal with a modified framing structure and extended bit rate. The bit rate is increased by the factor 255/239. The new signal format is referred to as “Optical Channel” at the corresponding bit rate.

1675 LambdaUnite MSS supports the out-of-band FEC type, because it provides a higher margin improvement (about 5 dB). This feature is available on the following transmission units:

- 10-Gbit/s *LambdaXtreme*™ Transport interworking interface
- 10-Gbit/s *WaveStar*® OLS 1.6T interworking interface.

Ring protection

1675 LambdaUnite MSS supports both, SONET and SDH ring protection features:

- SONET: Bidirectional Line Switched Ring (BLSR)
- SDH: Multiplex Section Shared Protection Ring (MS-SPRing)

BLSR

The following BLSR protection schemes can be configured:

- 2-fiber BLSR on OC-192 and on OC-48 interfaces
- 4-fiber BLSR on OC-192 interfaces and on OC-48 interfaces, both with asymmetric ring support.

The protection scheme complies with the ANSI T1.105.01 Standard.

MS-SPRing

The following MS-SPRing protection schemes can be configured:

- 2-fiber MS-SPRing on STM-64 and on STM-16 interfaces
- 4-fiber MS-SPRing on STM-64 interfaces and on STM-16 interfaces, both with asymmetric ring support.
- 4-fiber MS-SPRing with TransOceanic Protocol “TOP” on STM-64 and on STM-16 interfaces
- 4-fiber MS-SPRing with TransOceanic Protocol with EXtra traffic “TOP+EX” on STM-64 interfaces

The protection scheme complies with ITU-T Rec. G.841.

The 4-fiber MS-SPRing “TOP+EX” supports extra traffic on protection timeslots and provides its re-establishment in case these timeslots are not utilized for protection, please refer also to “Preemptible protection access” (p. 2-32).

APS Autolock

An *APS Autolock* mechanism is implemented to stabilize K-byte protocols in MS-SPRings in case of toggling switching events. If there are more than 10 switch state changes in a certain period of time, the NE performs a forced switch. The time period is in the range of few seconds depending on the local node ID to avoid that multiple nodes will perform the forced switch simultaneously. This forced switch will be cleared automatically when the ring is stable again. In that case, “stable” means no switch requests within 1 second.

BLSR/MS-SPRing principle

BLSR/MS-SPRing is a self-healing ring configuration in which traffic is bidirectional between each pair of adjacent nodes and is protected by redundant bandwidth on the bidirectional lines that inter-connect the nodes in the ring. Because traffic flow is bidirectional between the nodes, traffic can be added at one node and dropped at the next without traveling around the entire ring. This leaves the spans between other nodes available for additional traffic. Therefore, with many traffic patterns a bidirectional ring can carry much more traffic than the same facilities could carry if configured for a unidirectional ring.

Self-healing rings

1675 LambdaUnite MSS BLSR/MS-SPRings are self healing, that means transport is automatically restored after node or fiber failures. This is realized by using only half capacity for protected traffic (working), reserving the other half of the capacity for back up purpose (protection).

The following table gives an overview of the bidirectional transmission capacities of the various BLSR/MS-SPRing types:

BLSR	MS-SPRing	transmission capacity
4-fiber 10-Gbit/s		192 STS-1 equivalents protected
2-fiber 10-Gbit/s		96 STS-1 equivalents protected
4-fiber 2.5-Gbit/s		48 STS-1 equivalents protected
2-fiber 2.5-Gbit/s		24 STS-1 equivalents protected
	4-fiber 10-Gbit/s	64 VC-4 equivalents protected
	4-fiber 10-Gbit/s “TOP”	64 VC-4 equivalents protected
	4-fiber 10-Gbit/s “TOP+EX”	64 VC-4 equivalents protected + 64 VC-4 equivalents unprotected
	2-fiber 10-Gbit/s	32 VC-4 equivalents protected
	4-fiber 2.5-Gbit/s	16 VC-4 equivalents protected
	4-fiber 2.5-Gbit/s “TOP”	16 VC-4 equivalents protected
	2-fiber 2.5-Gbit/s	8 VC-4 equivalents protected

In the event of a fiber or node failure, service is restored by switching traffic from the working capacity of the failed line to the protection capacity in the opposite direction around the ring. (See “2-fiber BLSR/MS-SPRing traffic flow” (p. 2-29) and “Loopback protection switch in a 2-fiber BLSR/MS-SPRing” (p. 2-30).)

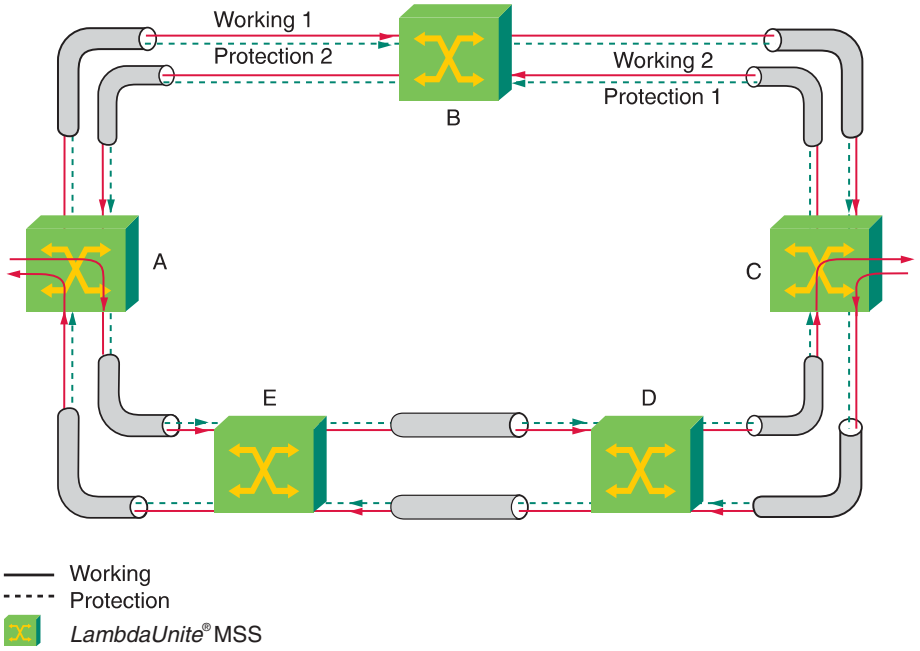
To have a high network resilience, a 4-fiber MS-SPRing/BLSR requires four different port units (single density or double density). A 2-fiber MS-SPRing/BLSR requires two different port units. Ports of several rings can be located on the same port unit.

Protection switching

When a line-level event triggers a protection switch, the affected nodes switch traffic on the protection capacity and transport it to its destination by looping it back the other way around the ring. (See "Loopback protection switch in a 2-fiber BLSR/MS-SPRing" (p. 2-30).) Service is reestablished on the protection capacity in less than 50 milliseconds after detection of the failure (for signal fail conditions in rings without existing protection switches or extra traffic).

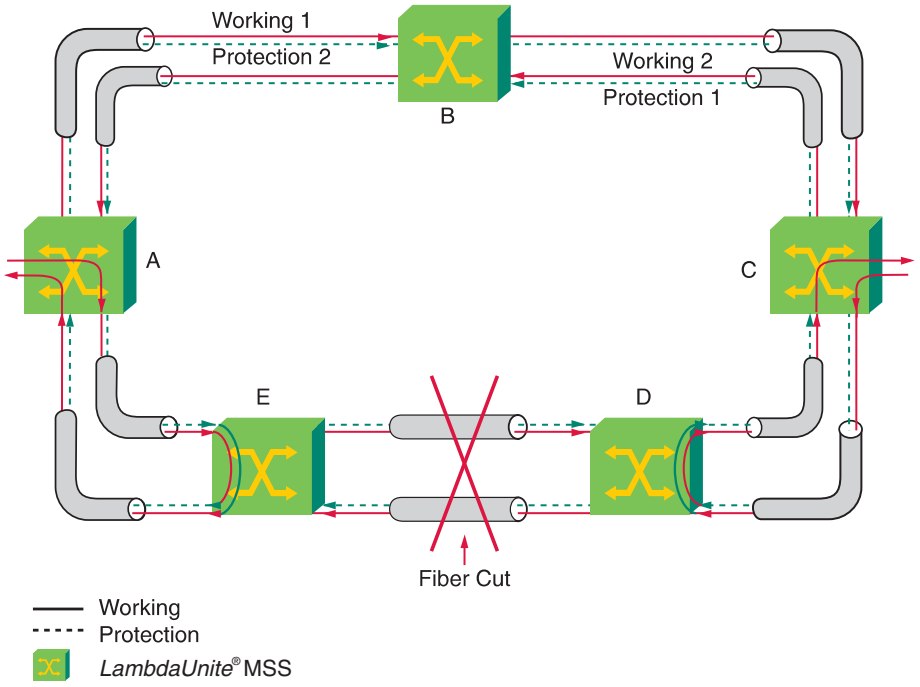
2-fiber BLSR/MS-SPRing traffic flow

The following figure shows normal (non-protection-switched) traffic flow in a 1675 LambdaUnite MSS 2-fiber BLSR/MS-SPRing.



Loopback protection switch in a 2-fiber BLSR/MS-SPRing

The following figure illustrates a 2-fiber BLSR/MS-SPRing protection switch that results from a fiber cut.



Protection traffic flow

In case of loopback protection switch in a 2-fiber BLSR/MS-SPRing, the traffic going from Node A to Node C, that normally passed through Node E and Node D on “working 2 ”capacity, is switched onto the “protection 2” capacity of the line leaving Node E in the opposite direction. The traffic loops back around the ring via Node B, C, and D (where the loopback switch is active) to Node C. Similarly, traffic going from Node C to Node A that normally passed through Node D and Node E on “working 1” capacity is switched on to the “protection 1” capacity of the line leaving Node D in the opposite direction.

The same approach is used for a node failure. For example, if Node D were to fail, Nodes C and E would perform loopback protection switches to provide an alternate route for ring traffic.

Asymmetric ring provisioning

In standard (4 Fiber) BLSR/MS-SPRing, depending on traffic, some ring segments (especially overlapping ring segments) might be only partially filled. These ring segments have overcapacity for working traffic as standard BLSR/MS-SPRing schemes have to have equal capacity between service and protection line across the ring.

With the asymmetric ring functionality these only partially filled ring segments (spans) can be used for traffic.

Asymmetric ring protection schemes

The asymmetric ring protection schemes are supported for:

- 4-fiber MS-SPRing on STM-64 and on STM-16 interfaces
- 4-fiber BLSR on OC-192 and on OC-48 interfaces.

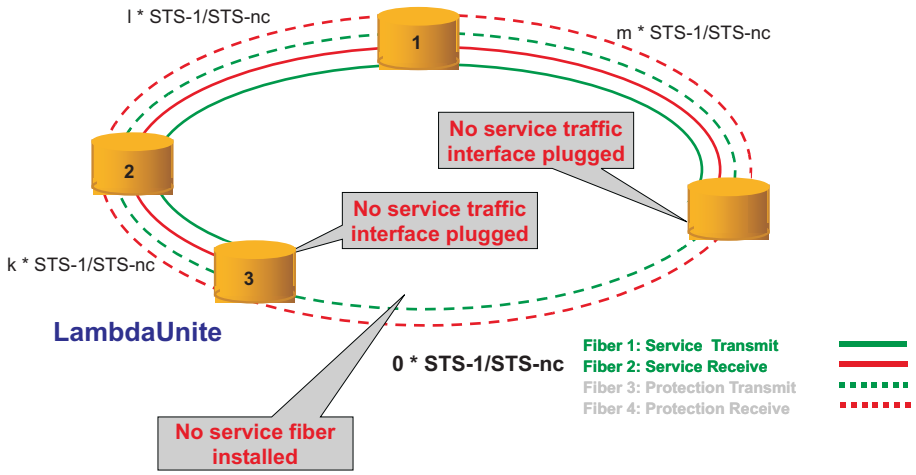
Asymmetric ring

An asymmetric 4-fiber ring is a subnetwork which consists of a set of nodes, where:

- each node is an ADM that interfaces with two spans;
- each span interconnects two nodes;
- each span consists of one or two lines (i.e. one or two bidirectional pairs of fibers);

There is at least one span consisting of one protection line (asymmetric property) and at least one span with two lines (worker and protection);

- the set of nodes is interconnected by the spans into a closed loop (a closed ring).
- all spans operate at the same rate.



Transoceanic protocol (TOP)

Overview

A special feature of 1675 LambdaUnite MSS for very long-haul 4-fiber MS-SPRing applications is the TransOceanic Protocol (TOP).

This protocol supports also re-establishment of extra traffic ("TOP+EX", see "Preemptible protection access" (p. 2-32)).

It shortens the protection path in rings, avoiding loops over very long distance spans. Thus it greatly reduces the impact of propagation delay on the signal quality, and it saves fiber resources.

Transoceanic protocol (TOP)

1675 LambdaUnite MSS supports 4-fiber MS-SPRing transoceanic protocol protection schemes on the 10-Gbit/s interfaces. The protection scheme complies with ITU-T Rec. G.841.

This section defines the support of undersea cable applications in an SDH network using a 4 fiber MS-SPRring configuration. These applications frequently encounter distances between nodes that are greater than the overall circumference designed for the MS-SPRing (for example, > 1200km). This application is the transoceanic protocol without extra traffic (TOP-WO) and the transoceanic protocol with restoration of extra traffic (TOP+EX). Extra traffic is allowed during normal conditions and is preempted when a protection switch is requested. The TOP+EX preempts the extra traffic initially, but re-establishes extra traffic assigned to protection bandwidth that is not required for restoration of the failed services. The extra traffic is re-established as part of the protection switching processing.

Preemptible protection access

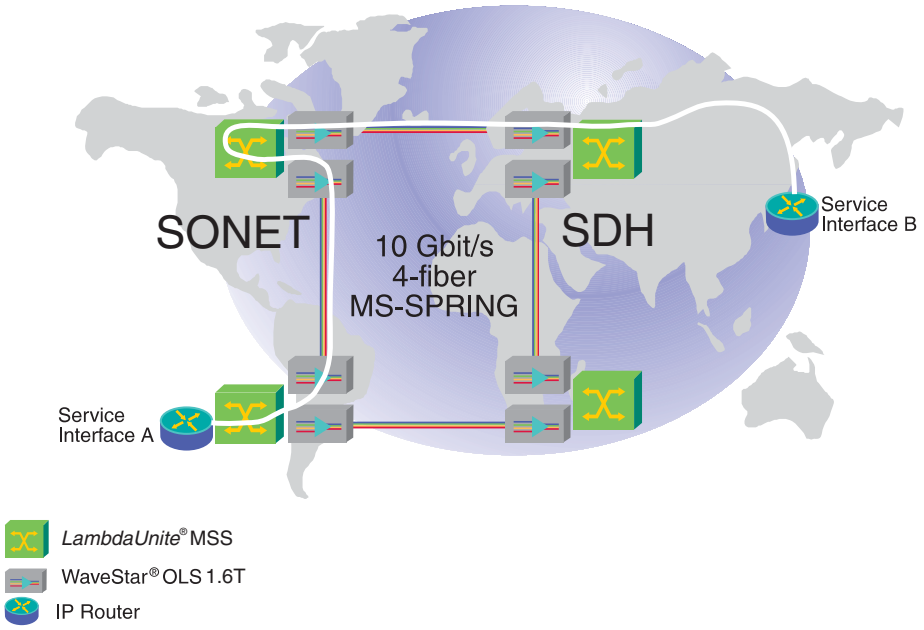
Preemptible Protection Access (PPA) or extra traffic is a feature using the protection capacity in order to carry some extra, low priority traffic. 1675 LambdaUnite MSS supports extra traffic on 4-fiber MS-SPRing with "TOP+EX" as an economical way to obtain more capacity for traffic that does not need to be protected.

In case of a failure the protected traffic is restored and all the extra traffic is momentarily dropped. There are two cases to distinguish:

- if the timeslots formerly used by extra traffic are now occupied by restored traffic, then the extra traffic is lost
- if the timeslots formerly used by extra traffic are not occupied by restored traffic, then the extra traffic is immediately re-established.

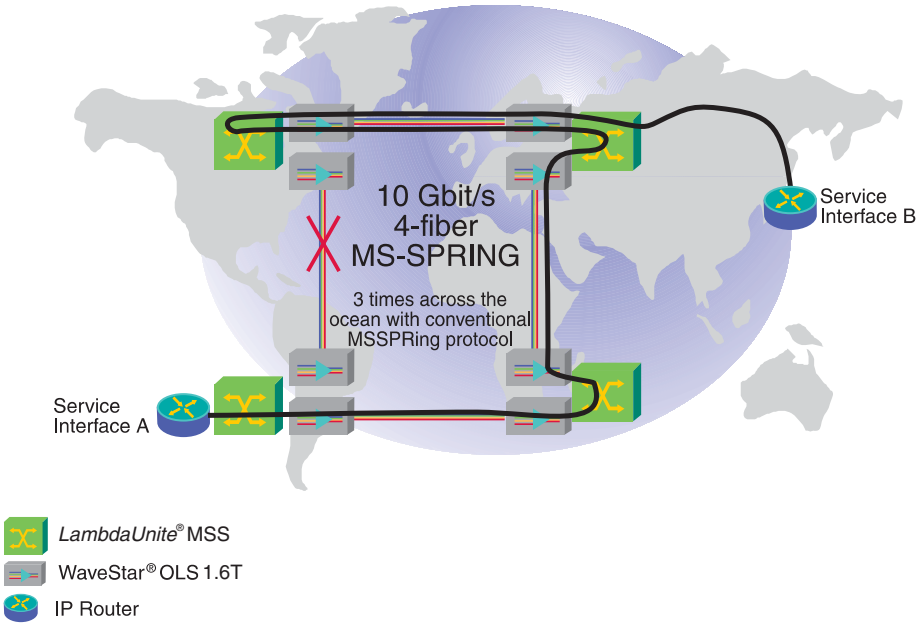
Transoceanic principle

The following figure provides a schematic view of 1675 LambdaUnite MSS in a 4 fiber MS-SPRing very long distance configuration. The MS-SPRing is composed of four 1675 LambdaUnite MSS elements. Under normal conditions (MS-SPRing idle) the traffic is routed from service interface A over *two* very long distance spans to service interface B.



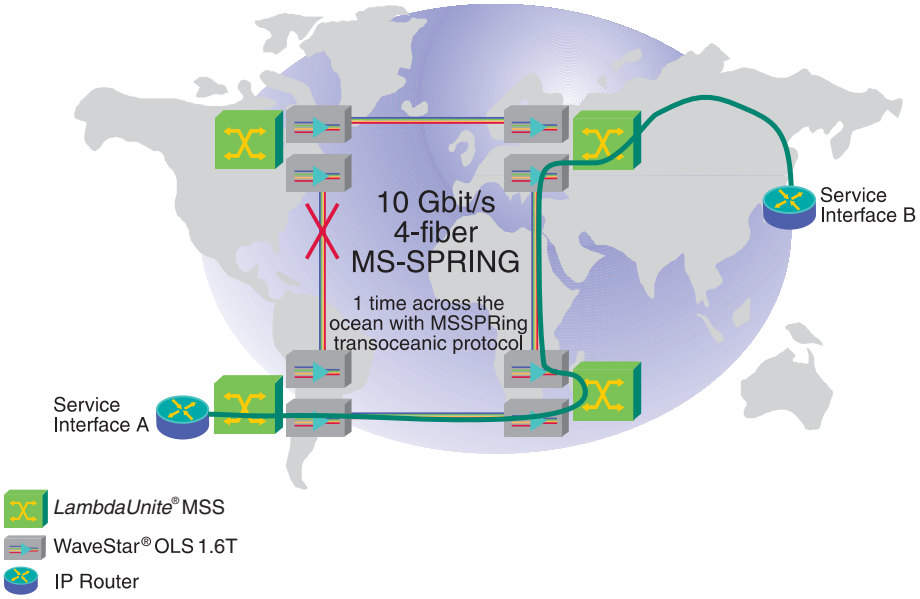
Plain MS-SPRing switching case

The figure below shows the traffic flow in the MS-SPRing protection condition (switching case) *without* transoceanic protocol. In case of a complete fiber cut as indicated by the red cross, the traffic is carried *three times across the ocean*.



MS-SPRing with transoceanic protocol, switching case

In the protection condition (switching case) *with* transoceanic protocol, the traffic *passes the ocean only once*, running through *two* very long distance spans only, just like under normal conditions, as shown in the following figure. 1675 LambdaUnite MSS routs the traffic directly to the service interface B, avoiding the loop over the ocean.



In this way 1675 LambdaUnite MSS in the MS-SPRing with transoceanic protocol shortens the protection path strikingly, improving significantly the signal quality and increasing the performance of fiber resources.

Transoceanic protocol without re-establishment of extra traffic (TOP-WO)

The objectives for the TOP-WO application is to create a shortened loop for the purpose of restoring normal traffic affected by a ring failure. In this application, extra traffic is pre-empted for the duration of the switch request. In the case of a span switch, extra traffic not using the span may remain. In the case of a ring switch, all extra traffic on the ring is pre-empted.

Transoceanic protocol with re-establishment of extra traffic (TOP+EX)

One of the objectives of the transoceanic protocols is to re-establish extra traffic that is assigned to capacity not required for the restoration of traffic in the protected spans. In this application, any service traffic that is to be restored must have a complete path on the ring, i.e. an entry point, an exit point and through connections at any intermediate nodes. If a channel in the span being protected by a ring switch is not part of a complete path on the ring, it will not be restored. Similarly, an extra traffic cross-connection must be part of a complete path on the ring to be selected for reestablishment.

A cross-connection is considered to be part of a complete connection path on the ring if the collective cross-connection/squelch information from the ring nodes contains an add cross-connection, a drop cross-connection, and through cross-connections at nodes between the add and drop nodes for the channel(s) in question in the same sequence reflected by the ring map.

A complete connection path is referred to as a circuit and the collective information is referred to as ring circuit map. The basic data needed for TOP+EX operations is the same as that needed for a circuit audit feature. However, unlike the circuit audit feature, the determination of a complete connection path is done by every node on the ring for every timeslot in each direction.

Back-to-back cross-connection of TOP+EX protected paths

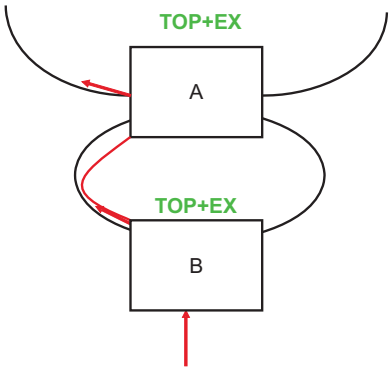
1675 LambdaUnite MSS in supports back-to-back cross-connections of TOP+EX protected path within the same 1675 LambdaUnite MSS shelf. The protection schemes / combinations described below are supported.

Back-to-back cross-connection of paths within two TOP+EX protected group

The following interface connections are supported:

1. Connections between service paths of TOP+EX protection groups.
2. Connections between pre-emptible path time / extra traffic paths of TOP+EX protection groups

TOP+EX to TOP+EX

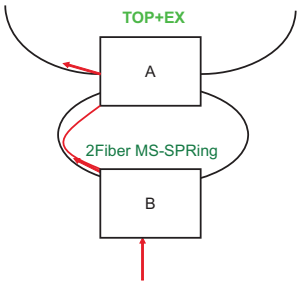


Back-to-back cross-connection of TOP+EX protected path on subsea line interface and 2Fiber MS-SPRing protected path on terrestrial backhaul interfaces

The following interface connections are supported:

1. Connections between service paths of TOP+EX protection group and service path of 2Fiber MS-SPRing protection group
2. Connections between pre-emptible parth time / extra traffic paths of TOP+EX protection group and pre-emptible part time / extra traffic paths of 2Fiber MS-SPring protection group.

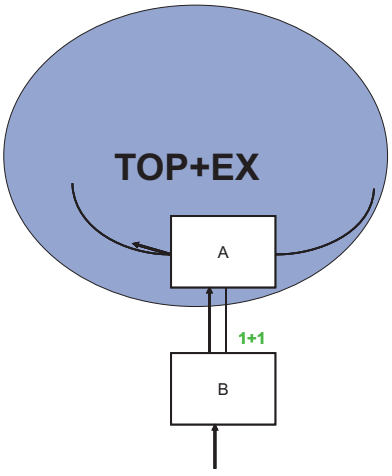
TOP+EX to 2Fiber MS-SPRing



Back-to-back cross-connection of TOP+EX protected path on subsea line interface and 1:1 MSP protected path on terrestrial backhaul interfaces

The following interface connections are supported:

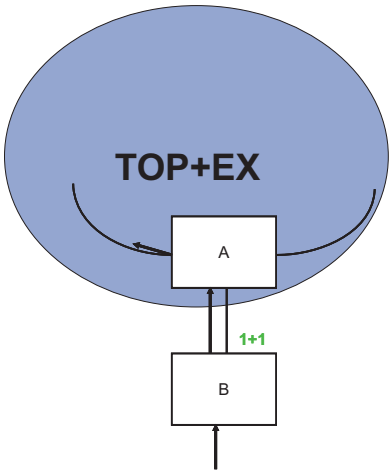
1. Connections between service paths of TOP+EX protection group and service path of 1:1 MSP protection group.
2. Connections between pre-emptible parth time / extra traffic paths of TOP+EX protection group and pre-emptible part time / extra traffic paths of 1:1 MSP protection group



Back-to-back cross-connection of TOP+EX protected path on subsea line interface and 1+1 MSP (all supported modes) protected path on terrestrial backhaul interfaces

The following interface connections are supported:

1. Connections between service paths of TOP+EX protection group and service path of 1+1 MSP protection group.
2. Connections between pre-emptible parth time / extra traffic paths of TOP+EX protection group and service paths of 1+1 MSP protection group



Lower-order cross connections involving MS-SPRing TOP+EX ports

The provisioning of lower-order cross-connections to, from, and between the ports of a 4 fibre MS-SPRing TOP+EX group is not fully supported and might result in problems. For instance a CTL reboot has been observed when a LO cross-connection has been deleted using a LOXC/1 pack and involving a TOP+EX port . So it is currently not recommended to use LO cross-connections with ports of a 4 fibre MS-SPring TOP+EX group. However, the system does not deny the attempt to provision such LO cross-connections.

DRI/DNI

1675 LambdaUnite MultiService Switch (MSS) supports both, SONET and SDH dual node ring interworking features:

- SONET Dual Ring Interworking (DRI) for BLSR and UPSR
- SDH Dual Node Interworking (DNI) for MS-SPRing and SNCP

SONET Dual ring interworking (DRI) for BLSR

1675 LambdaUnite MSS supports Dual Ring Interworking (DRI) for the purpose to protect between two BLSR protected rings. The DRI feature is compliant with ANSI T1.105.01 and *Telcordia*™ GR-1230-CORE, GR-1400-CORE standards. It provides a service selector for each STS-N tributary provisioned for DRI.

The service selector selects the better of two received path-level signals in accordance with a given hierarchy of conditions. These conditions include STS path signal fail and PDI-P (payload defect indicator - path level). This applies only to drop and continue, does not include dual transmit. Multiple DRIs (up to the maximum system capacity) are supported.

SONET Dual ring interworking (DRI) for UPSR

1675 LambdaUnite MSS supports Dual Ring Interworking (DRI) for the purpose to protect between two UPSR protected paths. The DRI feature is compliant with ANSI T1.105.01 and *Telcordia*™ GR-1230-CORE, GR-1400-CORE standards. It provides a service selector for each STS-N tributary provisioned for DRI.

The service selector selects the better of two received path-level signals in accordance with a given hierarchy of conditions. These conditions include STS path signal fail and PDI-P (payload defect indicator - path level). This applies only to drop and continue, does not include dual transmit. Multiple DRIs (up to the maximum system capacity) are supported.

SDH Dual node ring interworking (DNI) for MS-SPRing

1675 LambdaUnite MSS supports SDH Dual Node Interworking (DNI) for the purpose to protect between two MS-SPRING protected rings. The DNI feature is compliant with ITU-T G.842 standard. It provides a service selector for each VC-N tributary provisioned for DNI.

The service selector selects the better of two received path-level signals in accordance with a given hierarchy of conditions. These conditions include VC Path Signal Fail. Multiple DNIs (up to the maximum system capacity) are supported.

SDH Dual node ring interworking (DNI) for SNCP

The system supports SDH dual node interworking (DNI) for the purpose to protect between two SNC/I/N protected rings. The DNI feature is compliant with ITU-T Rec. G.842 standard. It provides a service selector for each VC-N tributary provisioned for DNI.

The service selector selects the better of two received path-level signals in accordance with a given hierarchy of conditions. These conditions include VC Path Signal Fail. Multiple DNIs (up to the maximum system capacity) are supported.

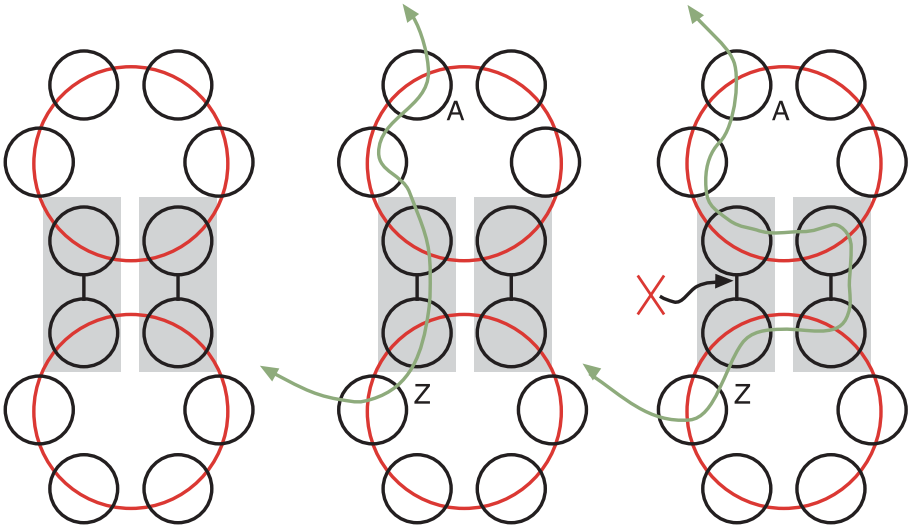
Dual ring interworking protection principle

The self-healing mechanisms of the two rings remain independent and together they protect against simultaneous single failures on both rings (not affecting the inter-connections). The DRI/DNI configuration additionally protects against failures in either of the inter-connections between the rings, whether the failure is in a facility or an inter-connection node.

DRI/DNI is a configuration that provides path-level protection for selected OC-n/STM-N circuits that are being carried through two rings. Protection for the route between the two rings is provided by inter-connecting the rings at two places, as shown in the figure below. Each circuit that is provisioned with DRI/DNI protection is dual-homed, meaning it is duplicated and subsequently terminated at two different nodes on a ring. The two inter-connecting nodes in each ring do not need to be adjacent.

DRI/DNI traffic flow

The following figures show a DRI/DNI configuration transporting traffic between nodes A and Z.



Description of the figures:

1. Two rings are interconnected by two nodes.
2. A path is set up from node A to node Z.
3. A failure, depicted by X, triggers a DRI/DNI switch at the top ring primary node, which automatically selects traffic from the secondary node.

Protection switching with 1675 LambdaUnite MSS

The previous figure illustrates a failure of the inter-connection to a primary node at the point labeled “X” in figure 3. The failure results in a DRI/DNI switch at the primary node in the top ring. A DRI/DNI protection switch in a 1675 LambdaUnite MSS occurs in ≤ 50 milliseconds (not counting the detection time) plus a provisionable hold-off time.

Primary and secondary nodes

In the BLSR/MS-SPRing, a bidirectional DRI/DNI-protected circuit to and from the terminating node is added and dropped at both a primary node and a secondary node, both of which inter-connect with the other ring. The primary and secondary nodes are defined and provisioned on a per-circuit basis.

Drop and continue

1675 LambdaUnite MSS supports the drop and continue method of DRI/DNI, in which the primary node is between the terminating node and the secondary node, and the primary node is the node that performs the drop-and-continue and path-selection functions.

The primary node drops the circuit in the direction of the other network and also continues (bridges) the circuit to the secondary node. The secondary node drops the circuit in the direction of the other network and adds the circuit from the other network in the direction of the terminating node.

The primary node either adds the circuit received on its tributary interface from the other network, or else passes through the duplicate signal received on the line from the secondary node, depending on standards-compliant path selection criteria.

Types of connections

The two types of connections shown in "Example: DRI/DNI via OC-3/STM-1 tributaries" (p. 2-43) are

- a direct intra-office connection between the primary nodes, Node 1 and Node 2, at the first central office (CO 1).
- an optically extended, direct secondary connection between the secondary nodes (Node 3 at the second central office (CO 2) and Node 4 of the *WaveStar*® ADM16/1 2.5-Gbit/s ring). This type of connection is achieved through the 155-Mbit/s interfaces at the inter-connected nodes and can go through other equipment.

Both types of connections can be used in either primary or secondary nodes.

Interworking

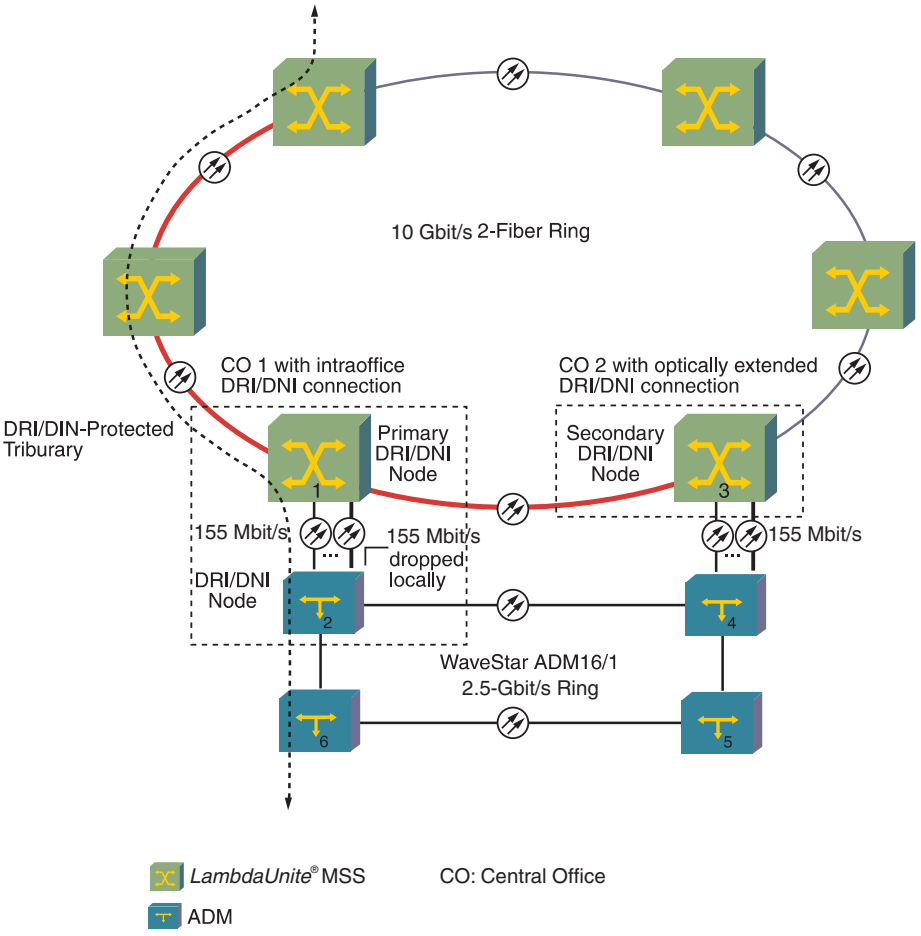
All 1675 LambdaUnite MSS optical synchronous interfaces (40 Gbit/s, 10 Gbit/s, 2.5 Gbit/s, 622 Mbit/s and 155 Mbit/s) support dual node ring interworking. A 1675 LambdaUnite MSS 10-Gbit/s ring can interwork with a 2-fiber BLSR/MS-SPRing, including rings using for example

- *WaveStar*® BandWidth Manager
- *Metropolis*® ADM (Universal shelf)
- *Metropolis*® DMX Access Multiplexer
- *WaveStar*® ADM16/1

Additionally, there can be intermediate network elements in the inter-connection routes between the two rings.

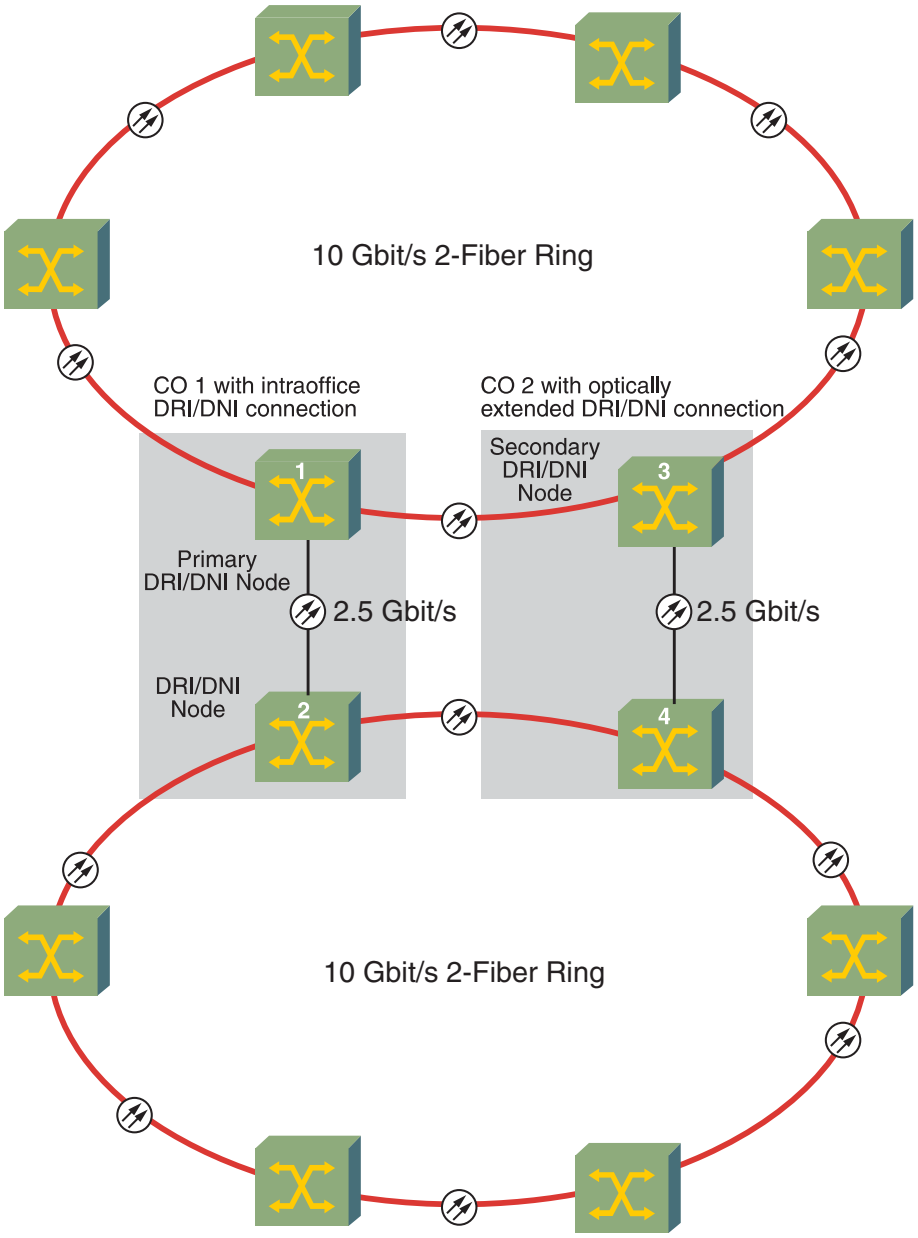
Example: DRI/DNI via OC-3/STM-1 tributaries

The following figure illustrates a DRI/DNI configuration that uses OC-3/STM-1 interfaces between a 1675 LambdaUnite MSS 10-Gbit/s ring and a *WaveStar*® ADM16/1 2.5-Gbit/s ring.



Example: DRI/DNI via 2.5-Gbit/s tributaries

The following figure illustrates a DRI/DNI configuration that uses 2.5-Gbit/s interfaces between two 1675 LambdaUnite MSS 10-Gbit/s rings.



Line protection

1675 LambdaUnite MSS supports both, SONET and SDH linear protection features on all optical 10-Gbit/s, 2.5-Gbit/s, 622-Mbit/s and 155-Mbit/s ports:

- SONET: linear Automatic Protection Switching (line APS)
- SDH: Multiplex Section Protection (MSP).

Linear APS / MSP principle

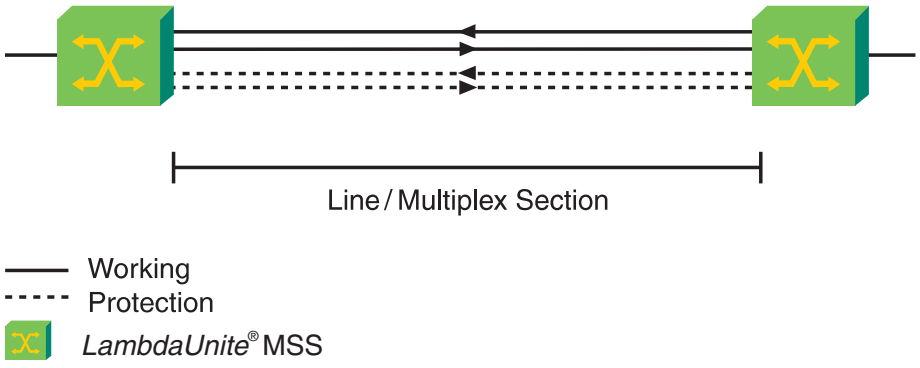
The principle of a linear APS is based on the duplication of the signals to be transmitted and the selection of the best signal available at the receiving port. The two (identical) signals are routed over two different lines, one of which is defined as the working line, and the other as protection line. The same applies to the opposite direction (bidirectional linear APS). The system only switches to the standby line if the main line is faulty.

It is possible to add/drop linear APS protected traffic from/to all 10-Gbit/s, 2.5-Gbit/s, 622-Mbit/s and 155-Mbit/s ports in the NE. Linear APS protection switching can be configured with *WaveStar*® CIT or OMS.

Linear APS / MSP schemes

Linear APS protection schemes can be configured with 1675 LambdaUnite MSS network elements for all 10-Gbit/s, 2.5-Gbit/s, 622-Mbit/s and 155-Mbit/s interfaces. The SONET 1+1 Linear APS scheme complies with the ANSI T 1.105.01 APS standard. The SDH multiplex section protection (MSP) scheme complies with the ITU-T Rec. G.841 including annex B (optimized protocol).

The following figure shows an 1+1 linear APS protection example: one physical main (working) connection between multiplexers is protected by one physical stand-by (protection) connection.



The system supports multiple linear APS protections at the same time up to the full transmission/slot capacity. There is no restriction due to other configuration or performance limitations.

The linear APS feature can be provisioned also directly on existing cross connections.

Linear APS / MSP can be configured in the following modes:

Protocol	SONET	SDH
1+1 uni-directional revertive	on OC-192…OC-3 ports	on STM-64…STM-1 ports
1+1 uni-directional non-revertive	on OC-192…OC-3 ports	on STM-64…STM-1 ports
1+1 bi-directional revertive	on OC-192…OC-3 ports	on STM-64…STM-1 ports
1+1 bi-directional non-revertive	on OC-192…OC-3 ports	on STM-64…STM-1 ports
1+1 optimized (bi-directional non-revertive)	not supported	on STM-64…STM-1 ports
1:1 bi-directional revertive (with extra traffic)	not supported	on STM-64…STM-1 ports

1:1 MSP provides so called extra traffic, using the protection ports in order to carry some additional, low priority traffic; this extra traffic is dropped in the switching case.

Nesting of APS/MSP and UPSR/SNCP

Nesting of MSP and SNCP means that it is allowed to source an SNCP selector from the worker bandwidth of an MSP.

The system allows to provision higher-order cross connections with path-protected cross-connection topology if the logical input tributaries and the output tributary are of the following types:

- Inputs

  any two tributaries from which you can provision a higher-order cross connection, in any port which is *unprotected* or which is the working port of an MSP/APS port protection group

The two input tributaries can be part of worker ports of two different MSP/APS groups, both may be part of the same worker port of one MSP/APS group, only one of them may be part of a worker port of an MSP/APS group, or none of them may be part of a worker port of an MSP/APS group.

- Output:

  any tributary to which you can provision a higher-order cross-connection in any port

Additional the following has to be considered:

- When nested with an MSP which complies to a 50 ms service restoration requirement it is recommended to provision the path protection group with a hold-off time of 100 ms, to avoid double switches (MSP/APS and SNCP/UPSR). This hold-off time is required to avoid a double transmission hit which could be caused by the nesting of two protection mechanisms. This is especially true for the switch back in case of revertive schemes.
- Extra traffic in 1:1 MSP from the protection port cannot be connected to SNCP.

Path protection

1675 LambdaUnite MSS supports both, SONET and SDH path protection features on all available cross-connection rates:

- SONET: Unidirectional Path-Switched Ring (UPSR)
- SDH: Subnetwork Connection Protection (SNCP)

UPSR/SNCP benefits

This feature allows you to provide additional end-to-end survivability for selected paths in a subnetwork. It can also be provisioned directly on existing cross-connections.

UPSR/SNCP principle

The principle of a UPSR/SNCP is based on the duplication of the signals to be transmitted and the selection of the best signal available at the path termination. The two (identical) signals are routed over two different path segments (uni-directional paths), one of which is defined as the main path and the other as standby path. The same applies to the opposite direction (bidirectional UPSR/SNCP). The system only switches to the standby path if the main path is faulty.

UPSR/SNCP with 1675 LambdaUnite MSS

UPSR/SNCP protection switching can be configured with *WaveStar*® CIT, or Optical Management System (OMS) in two modes: revertive or non-revertive. When revertive switching is configured, a Wait-To-Restore time (WTR) can be defined. Additionally a hold-off timer can be configured individually for each path selector to defer to other protection features in case of redundant protection.

UPSR/SNCP can be configured for all types of cross-connections (see "Cross-connection features" (p. 2-13)). It is possible to add/drop UPSR/SNCP protected traffic from/to all ports in the NE. There are no restrictions regarding the types or mix of supported interfaces. Also traffic from 1-Gigabit Ethernet interfaces may be protected. UPSR/SNCP can be configured up to the total capacity of the system on the lowest path (cross-connection) granularity. The protection schemes comply with the *Telcordia*™ GR-1400-CORE, respectively ETS 300417 and ITU-T Rec. G.783.

UPSR

1675 LambdaUnite MSS supports UPSR protection, also within logical ring applications.

Path-protected pipe mode cross-connections

1675 LambdaUnite MSS supports the operation of path selectors (UPSR) for all SONET pipe-mode cross-connection rates. Protection switching is performed and reported independently for each of the path-level constituent signals within a path-protected pipe-mode cross-connection. This feature does not apply for ONNS ports.

SNCP

1675 LambdaUnite MSS supports two types of SNCP:

- Inherently monitored subnetwork connection protection (SNC/I)

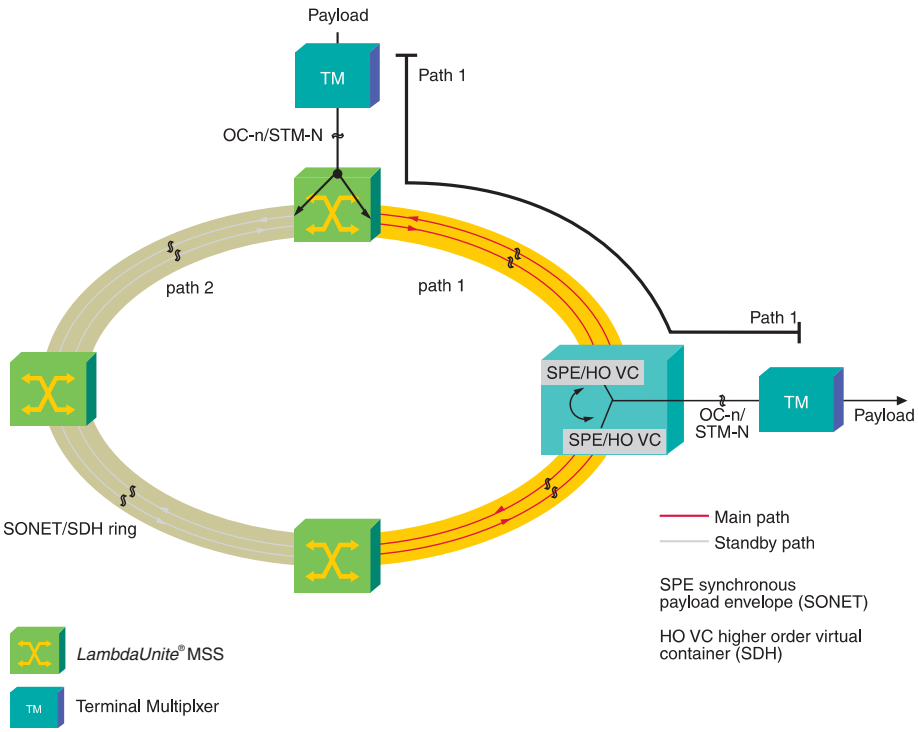
  SNC/I protection generally protects against failures in the server layer. This means AU-4 or STS-1 defects are detected and the switch is triggered by the Server Signal Fail (SSF) signal.
- Non-intrusively monitored subnetwork connection protection (SNC/N)

  SNC/N protection, generally, protects against failures in the server layer and against failures and degradation in the client layer. This means the non-intrusive monitor function detects Signal Fail (SF) and Signal Degrade (SD) events on the incoming signal and triggers the switch accordingly.

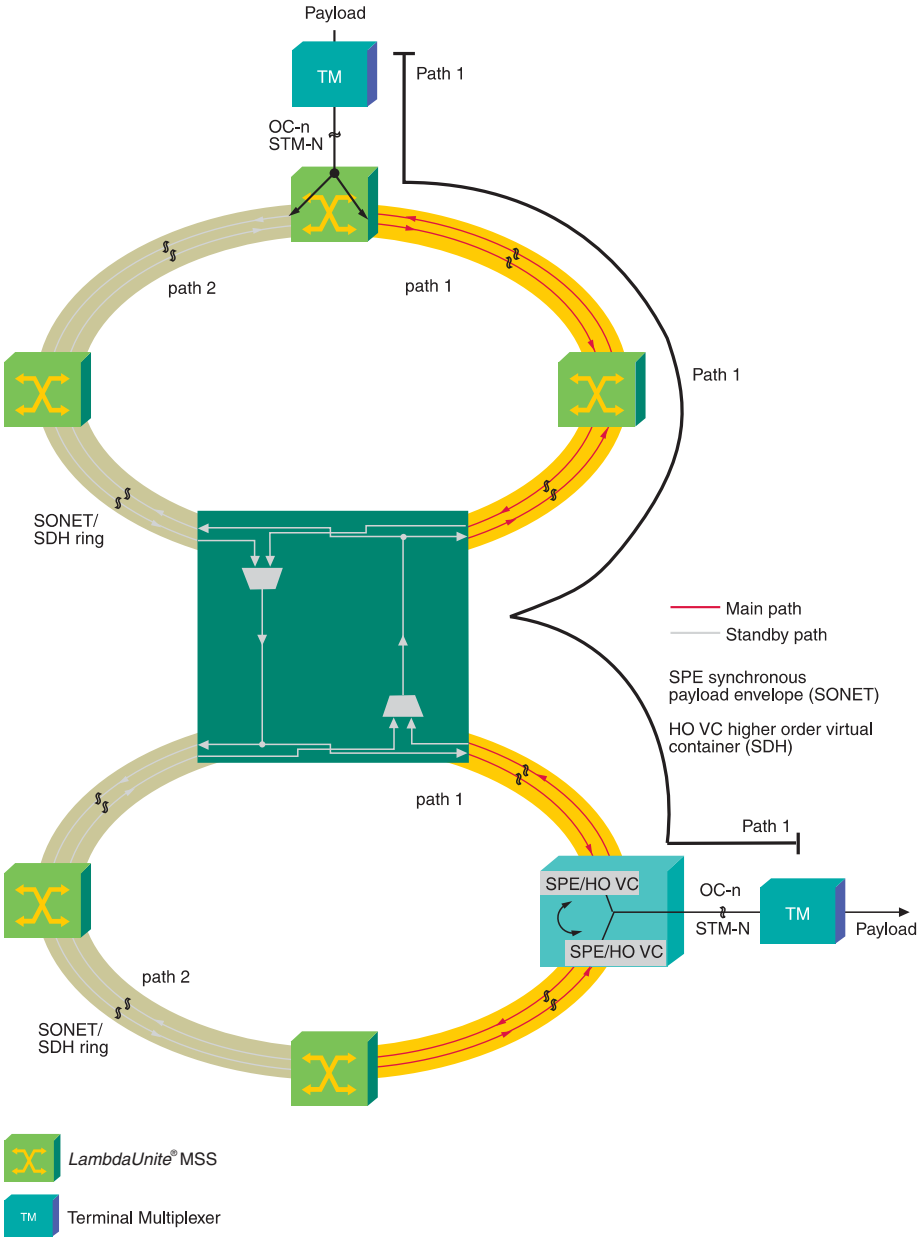
UPSR/SNCP configuration

The *WaveStar*® CIT cross-connection wizard supports the creation of UPSR/SNCP protected paths in single rings and in connected rings (ring-to-ring configuration, that means, one NE connects to two rings). Note that in the ring-to-ring configuration the full UPSR/SNCP is available within each ring. The connection between the rings, this means the connection within the network element, is unprotected, because in this example there is just a single-homed ring connection, no dual node ring interworking.

The following figure shows a single ring UPSR/SNCP application. Path 1 is the working (main) path, path 2 is the protection (standby) path in this example. The path termination is always outside 1675 LambdaUnite MSS. For simplification, the UPSR/SNCP switch is only shown for a unidirectional connection.



The following figure shows a ring-to-ring UPSR/SNCP configuration. Here, the UPSR/SNCP also consists of a broadcast in transmit direction. The signal then moves through the first ring via path 1 (working) and path 2 (protection). The ring is connected to another ring via one single NE. For simplification, the UPSR/SNCP switch is only shown for a unidirectional connection.



Important! Prerequisite for establishing path-protected cross-connections (UPSR/SNCP) is that both ports of the logical input tributaries are provisioned in fixed-mode. In other words, one or both inputs of a path-protected cross-connections must not be in adaptive-mode.

Nesting of APS/MSP and UPSR/SNCP

Nesting of MSP and SNCP means that it is allowed to source an SNCP selector from the worker bandwidth of an MSP.

The system allows to provision higher-order cross-connections with path-protected cross-connection topology if the logical input tributaries and the output tributary are of the following types:

- Inputs

  any two tributaries from which you can provision a higher-order cross-connection, in any port which is *not* in a port protection group or which is the working port of an MSP/APS port protection group

  The two input tributaries can be part of worker ports of two different MSP/APS groups, both may be part of the same worker port of one MSP/APS group, only one of them may be part of a worker port of an MSP/APS group, or none of them may be part of a worker port of an MSP/APS group.
- Output:

  any tributary to which you can provision a higher-order cross-connection in any port

Additional the following has to be considered:

- When nested with a lower layer protection scheme which complies to a 50 ms service restoration requirement it is recommended to provision the path protection group with a hold-off time of 100 ms, to avoid double switches (MSP/APS and SNCP/UPSR). This hold-off time is required to avoid a double transmission hit which could be caused by the nesting of two protection mechanisms. This is especially true for the switch back in case of revertive schemes.
- Extra traffic in 1:1 MSP from the protection port cannot be connected to SNCP.

Manual switch

The following manual switching actions are possible with *WaveStar*® CIT or OMS,:

- Manual to working: switches the traffic to the main path if it is not faulty
- Manual to protection: switches the traffic to the standby path if it is not faulty
- Forced to working: causes switchover to the main (working) path (even if this path is faulty)
- Forced to protection: causes forced switchover to the standby (protection) path (even if this path is faulty)
- Clear: clears any active manual switch request; clear will also release the wait-to-restore timer when provided for revertive switching.

Nesting of lower-order SNCP/UPSR with 2-fiber MS-SPRing/BLSR

Nesting of lower-order SNCP/UPSR with 2-fiber MS-SPRing/BLSR means that it is allowed to source a lower-order SNCP/UPSR selector from the worker bandwidth of a 2-fiber MS-SPRing/BLSR.

Important! Lower-order path-protected cross-connections can only be made from higher-order tributaries belonging to the working bandwidth of 2-fiber MS-SPRing/BLSR ports. Lower-order path-protected cross-connections *cannot* be made from higher-order tributaries, which belong to a 4-fiber MS-SPRing/BLSR port, or to the protection bandwidth of a 2-fiber MS-SPRing/BLSR port.

The system allows to provision lower-order cross-connections with path-protected cross-connection topology if the logical input tributaries and the output tributary are of the following types:

- Inputs

  any two tributaries from which you can provision a lower-order cross-connection, in any port which is *not* in a port protection group or which is the working port of a 2-fiber MS-SPRing/BLSR protection group

  The two input tributaries can be part of worker ports of two different 2-fiber MS-SPRing/BLSR protection groups, both may be part of the same worker port of one 2-fiber MS-SPRing/BLSR protection group, only one of them may be part of a worker port of a 2-fiber MS-SPRing/BLSR protection group, or none of them may be part of a worker port of a 2-fiber MS-SPRing/BLSR protection group.
- Output:

  any tributary to which you can provision a lower-order cross-connection in any port

Protection scheme independence

Due to the 1675 LambdaUnite MSS architecture protection schemes of different layers do not interact from a resource or provisioning perspective. Especially 1675 LambdaUnite MSS supports the back-to-back configuration of protection schemes. In a back-to-back configuration the selector of the first protection scheme is followed by the bridge function of the second protection scheme.

Equipment features

Overview

Purpose

This section provides information about 1675 LambdaUnite MSS equipment features concerning hardware protection, optical interface modules, and inventory and failure reports.

Contents

Equipment protection	2-55
Optical interface modules	2-56
Equipment reports	2-58

Equipment protection

1+1 redundancy

To enhance the reliability of 1675 LambdaUnite MSS, for the following plug-in units non-revertive 1+1 equipment protection is supported:

- the switching units (XC160, XC320, XC640, LOXC/1, LOXC40G2S/1, and LOXC40G3S/1)
- the Controller units (CTL) in ONNS applications; in traditional, non ONNS applications the redundancy is optional
- the electrical transmission units (EP155 and EP51, using the designated ECI paddle boards).
- the Portless TransMUX Card (TMUX2G5/1)

Besides automatic switching, manual and forced switching is supported to minimize operations interruption e.g. in maintenance scenarios (exchange of units).

Note: Please observe the following aspects:

- For the Controller units (CTLs) the forced switch option is not available.
- For ONNS applications, two Controllers of type CTL/3S are required.

Power supply

The power feed is maintained duplicated throughout the system, applying “load sharing” in normal operation conditions.

Optical interface modules

1675 LambdaUnite MSS supports optical port units consisting of a parent board which can be equipped with field-replaceable optical interface modules.

An optical interface module is a replaceable unit with a receiver and transmitter function providing the optical port. 1675 LambdaUnite MSS optical interface modules are “hot pluggable” (field-replaceable), i.e. the interface modules can be inserted or removed while the parent board is in operation, without affecting the service of other interface modules on the same parent board.

Advantages of the optical interface modules

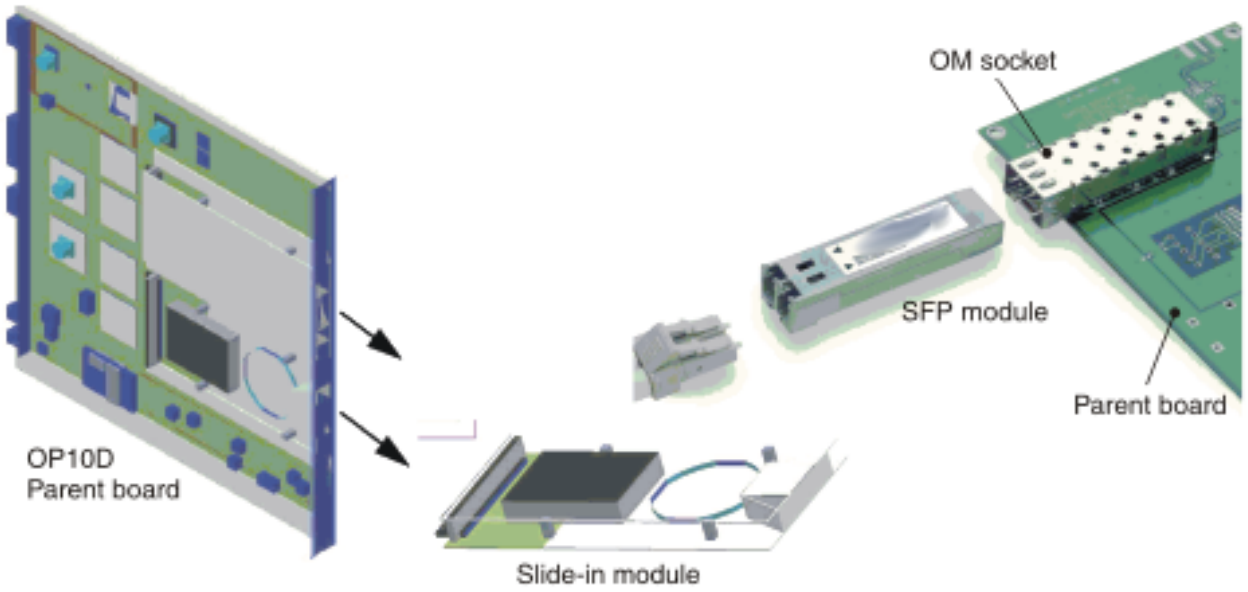
The 1675 LambdaUnite MSS optical interface modules provide excellent pay-as-you-grow opportunities for smaller or start-up applications, as only the up-to-date required number of ports must be purchased. An additional advantage of this flexible interface lies in ease and cost reduction when it comes to maintenance and repair activities.

The number of modules inserted into the parent board can be varied flexibly between zero and the maximum number of sockets; the possibly unused sockets can be left empty.

Types of optical interface modules

For 1675 LambdaUnite MSS systems, the following types of optical interface modules can be used:

- For the 10-Gbit/s optical interface modules, these types of optical interface modules are available:
  - Slide-in modules with a Alcatel-Lucent proprietary design
  - 10 Gigabit Extended Form-factor Pluggable (XFP) modules
- For the 155-Mbit/s, 622-Mbit/s and 2.5-Gbit/s optical interface modules, standardized Small Form-factor Pluggable (SFP) modules are used which comply to the SFP MSA standard of the SFF Committee.



Please observe the configuration rules described in “Port location rules ” (p. 6-11).

The 1675 LambdaUnite MSS SFPs are marked by the manufacturer, and they are checked upon insertion, in order to protect from accidental insertion of non 1675 LambdaUnite MSS specific SFPs. Only for the 1675 LambdaUnite MSS specific SFPs Alcatel-Lucent can guarantee the full functionality and warranty.

Equipment reports

Equipment inventory

1675 LambdaUnite MSS automatically maintains an inventory of the following information of each installed circuit pack:

- Serial number
- ECI code
- CLEI code
- Functional name
- Apparatus code
- Series number
- Functional qualifier
- Software release (of the NE)

You can obtain this information by an inventory request command.

Equipment failure reports

Failure reports are generated for equipment faults and can be forwarded via the *WaveStar*® CIT and OMS interfaces.

Synchronization and timing

Overview

Purpose

This section provides information about synchronization features, timing protection and timing interfaces of 1675 LambdaUnite MSS.

Contents

Timing features	2-60
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Timing interface features	2-62

Timing features

Synchronization modes

Several synchronization configurations can be used. The 1675 LambdaUnite MSS can be provisioned for:

- Locked mode, internal Station Equipment Clock (SIC/SEC) locked to either:
  - One of two external synchronization inputs (each of them accepts DS1 (B8ZS) signals (SF or ESF) or 2,048 kHz, 2 Mbit/s (framed or unframed), or
  - One of up to six OC-n / STM-N input signals (choice of input is provisionable, maximum one per transmission unit).

  In locked mode, if all configured references fail, the internal clock will switch to the hold-over state.
- Free-running.

Thus in the timing reference list up to eight timing references can be configured. The timing reference for the external timing output can be provisioned independently from the timing reference for the system clock.

Synchronization provisioning

It is possible to provision manual timing reference switching, to set priorities for timing sources, to choose timing sources that are added to the sources list, etc. using the *WaveStar*® CIT or OMS.

Timing protection

Timing unit protection

In 1675 LambdaUnite MSS the timing functionality is physically located on the switching unit. Thus, 1+1 non-revertive protection of the timing functionality is provided (see "Equipment protection" (p. 2-55)).

Timing reference selection

Automatic timing reference switching is supported by 1675 LambdaUnite MSS on signal failure of the active timing reference. The timing reference selection is according to *Telcordia*™ GR-253 for SONET timing and ETSI 300 417.1.1 / ITU-T Rec. G.781 for SDH timing. If all provisioned timing references fail or become unacceptable, the system will automatically switch over to the hold-over mode.

Timing interface features

External timing outputs

1675 LambdaUnite MSS provides external timing output signals derived from the system clock or from the incoming line signals. These output ports support DS1 (B8ZS) signals (SF or ESF) or 2,048 kHz or 2 Mbit/s (framed or unframed) signals as per ITU-T Rec. G.812 and G.703.

An external timing output will automatically be squelched as soon as its associated Quality Level (QL) drops below a provisionable threshold. Squelching is implemented by turning the timing output signal off.

Synchronization Status Message (SSM)

A Synchronization Status Message (SSM) can be used to indicate the signal quality level throughout a network. This will guarantee that all network elements will always be synchronized to the highest quality clock available.

On the 1675 LambdaUnite MSS system, the SSM algorithm is implemented according to ETS 300 417-6 and GR-253-CORE. SSM is supported on all incoming and outgoing optical and electrical interfaces.

The user can assign a certain SSM value (overriding the received SSM, if any) to any synchronization reference signal that can be made available to the SSM selection algorithm.

It is possible to force each individual outgoing SSM value (overriding the SSM computed by the algorithm) to the value DNU/DUS (Do Not Use for Synchronization).

Additionally 1675 LambdaUnite MSS supports insertion of an SSM value into an outgoing 2-Mbit/s framed signal (external timing output) and evaluation of the SSM of an incoming 2-Mbit/s framed signal (external timing input). This feature complies with Bellcore TR-NWT-000499 and with the ITU-T Rec. G.704 respectively.

Operations, administration, maintenance and provisioning

Overview

Purpose

The following section provides information about interfaces for operations, administration, maintenance, and provisioning (OAM&P) activities and tools, and about the monitoring and diagnostics features of 1675 LambdaUnite MSS.

Contents

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User-Network Interface (UNI)	2-75
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Interfaces

WaveStar® CIT and *Navis*® OMS

Operations, administration, maintenance, and provisioning (OAM&P) activities are performed using either the *WaveStar*® Craft Interface Terminal (CIT) or *Navis*® OMS. The *WaveStar*® CIT and the , *Navis*® OMS client are customer-supplied PC running the *WaveStar*® Graphical User Interface (GUI) software. You can plug it into the 1675 LambdaUnite MSS user panel or use it at a remote location to access 1675 LambdaUnite MSS by means of a LAN or of Data Communications Channel (DCC). You can use the *WaveStar*® CIT and the *Navis*® OMS to run a fully featured GUI. The GUI provides access to the entire 1675 LambdaUnite MSS functionality and contains extensive menus and context-sensitive help.

Full TL1 command/message set

1675 LambdaUnite MSS supports the full TL1 command and message set. The *WaveStar*® CIT and *Navis*® OMS convert user inputs at the GUI into the corresponding TL1 commands and convert TL1 responses and messages into the GUI displays.

TL1 cut-through interface

The 1675 LambdaUnite MSS system provides a TL1 cut-through interface via *WaveStar*® CIT and OMS. Thus, you can interact with the NE using the TL1 language directly. OMS provides TL1 cut-through as a function within the GUI and also supports a special TL1 login. The TL1 cut-through is useful because it enables you to build custom macros of multiple TL1 commands coupled with a broadcast capability to send the TL1 commands to multiple NEs. Furthermore, TL1 cut-through is necessary for some infrequently used commands that are not supported by the OMS GUI.

Security

1675 LambdaUnite MSS uses logins, passwords, authentication, and access levels to protect against unauthorized access. It also keeps the security log.

Local and remote software downloads

With 1675 LambdaUnite MSS software can be downloaded from the *WaveStar*® CIT or from the *Navis*® OMS. Software downloading does not affect transmission or operations. Activating the newly downloaded software may affect operations but does not affect transmission.

LAN interface

1675 LambdaUnite MSS also communicates with remote logins, operations systems and management systems by means of the standard 7-layer OSI protocol and via TCP/IP over a LAN.

TCP/IP access

1675 LambdaUnite MSS provides TCP service for end-to-end communication with the management system via an IP network. Each LAN port of the NE can be provisioned with its own IP address and default router.

DCC interfaces

The 1675 LambdaUnite MSS system supports operations via the standard 7-layer OSI protocol over Data Communications Channel (DCC). Up to 180 DCC terminations of section DCC (DCC_R) and line DCC (DCC_M) channels can be configured on 155-Mbit/s, 622-Mbit/s, 2.5-Gbit/s, 10-Gbit/s ports. DCC channel protection switching is supported in conjunction with line APS / MSP protection switching of the respective optical port (“slaving”).

Transparent DCC

The 1675 LambdaUnite MSS system supports transparent DCC connections, so called DCC cross connections, in addition to the standard DCCs. These transparent DCC connections pass through the DCC information between two ports (without D-byte termination).

NE level

Detailed information and system control is obtained by using the *WaveStar*® CIT (Craft Interface Terminal) which supports provisioning, maintenance, configuration on a local basis. A similar facility is remotely (via a Q-LAN connection or via the DCC channels) available on the *WaveStar*® CIT, or on the *Navis*®OMS, that provide a centralized maintenance view and supports maintenance activities from a central location.

Orderwire and User Channel

Orderwire and user channel interfaces are physically implemented on the Control Interface Panel. E1, E2 and F1 byte access are supported on 10-Gbit/s- and 155-Mbit/s interfaces.

Optical Network Navigation System (ONNS)

ONNS features

The key drivers for the Optical Network Navigation System (ONNS) are:

- Fast, automatic service fulfillment
- Simplification of manual operations
- Elimination of connection design failure by using the network as the topology database.

As a part of the intelligent network platform, ONNS comprises a set of capabilities that automates SONET/SDH connection set-up, fast restoration, and the automatic discovery of the topology in SONET/SDH networks. Compared to a classical SONET/SDH network, the ONNS can set-up or remove connections faster, using automated circuit design. Furthermore it provides automatic restoration in meshed topologies, automatic neighbor and topology discovery, and dynamic network optimization, e.g. link defragmentation to recover stranded bandwidth.

The Optical Network Navigation System (ONNS) implements the requirements and follows the architecture of ITU-T recommendation G.8080/Y1304 (Architecture of the Automatically Switched Optical Network (ASON)), which encompasses automatic neighbor discovery (G.7714/Y.1705, G.7714.1/Y.1705.1), routing (G.7715/Y.1706) and distributed connection management (G.7713/Y.7404). It supports the OIF UNI in the role of a transport NE (TNE or UNI-N) in order to create switched connections. Further information regarding Generalized Multi Protocol Label Switching (GMPLS) can be obtained from the relevant IETF documents.

1675 LambdaUnite MSS can be used as classical SONET/SDH network element, as ONNS network element, or the user can define port per port which standard to support. With this hybrid application 1675 LambdaUnite MSS allows to integrate ONNS domains into existing classical SONET/SDH networks, hence 1675 LambdaUnite MSS takes the role of a flexible link between the two standards, offering unique growth opportunities.

ONNS functional overview

ONNS shifts network functions like path routing and protection from the management systems to the network elements, gaining time, resources and flexibility, especially in complex structures like meshed network topologies.

The Optical Network Navigation System (ONNS) application basically subsumes the following functions:

- Automated path setup and tear down

You insert in one network element only start- and end-point of a connection within the ONNS network. The system then performs automatically the connection set-up, a management system is not necessary. With a similar automatic function the path can be torn down via the ONNS.

- User defined network element exclusions

  You can choose to exclude certain network elements from the path. ONNS will not take them into consideration for setting up the path.

- With ONNS you can choose between a number of protections types as shown in "ONNS protection types " (p. 2-68).

  In 1+1 protection schemes it is possible to execute a manual switch to a specified path of the 1+1 connection. This features supports the "ONNS link based manual re-routing for link maintenance".

- Automatic topology and link state discovery

  The ONNS network elements exchange topology information and link state information continuously. For this purpose the Link State Advertisements (LSA) are running over the Signaling Communications Network (SCN). With the resulting database every ONNS network element is able to perform the ONNS functions.

- Data Replication for ONNS

  The system supports a fast data replication interface for ONNS related data, which provides a back up during CTL and XC equipment protection switches and supports fast restoration and provisioning activities.

- ONNS port provisioning browser

  By using the ONNS port provisioning browser, you can provision port parameters without leaving the ONNS View of the *WaveStar*® CIT. Otherwise, you would need to leave the ONNS View, enter the System View, provision the port parameters, and then re-enter the ONNS View.

- Restoration Event Trigger

  The system allows the operator to send a restoration trigger to the source node. The trigger will initiate a restoration for connections which have a valid input signal. In cases, where the restoration attempt fails (for example, because of insufficient network resources) the trigger will first send the connection into the restoration queue, but then abort the restoration (provided there is no real failure in between).

- ONNS support for E-NNI interface (refer to "External Network-Network Interface (E-NNI)" (p. 2-71))

For further details and operations information concerning the ONNS feature please refer to the chapter "ONNS tasks" in the *1675 LambdaUnite MSS User Provisioning Guide*.

ONNS protection types

The protection types available with ONNS are shown in the following table:

Protection type	Specific characteristic
Unprotected	An unprotected connection is a single path. If it fails, no attempt is made to protect or re-route it. The unprotected connection is intended for non-critical traffic or for traffic protected within the client layer, using diversely routed unprotected paths.
Auto Reroute, non-revertive	For a non-revertive auto-reroute connection, the existence of two disjoint paths is verified, but only one path is set up, not two. If and when the originally setup path fails, a maximal disjoint alternate path is calculated which meets all the original constraints. Once this path is setup, the original path is released to allow the network to reuse the resources.
Auto Reroute, revertive	For a revertive auto-reroute connection, the original path is not released and traffic will revert back to the original path once the failure has been fixed.
Auto Reroute, manual revertive	This type of service is similar to the **Auto Reroute, revertive** type of service with the difference that traffic can be *manually* reverted back to the original path.
1+1 (revertive or non-revertive)	The path from source to destination is set up as a 1+1 protected connection (revertive or non-revertive). A 1+1 protected connection contains two separate paths from source to destination, both set up and ready for use. If one fails, the other is selected.
1+1 end-to-end network protection	A 1+1 protected connection contains two separate paths from source to destination, both set up and ready for use. If one fails, the other is selected. Mission critical traffic can be given 1+1 protection.
1+1 permanent	A 1+1 permanent protected connection is a combination of a 1+1 end-to-end network protection (SNCP/UPSR, either revertive or non-revertive) with an auto-reroute path restoration (always non-revertive). If one of the two legs of the 1+1 end-to-end network protection fails, then this failed leg will be restored. Thus, the service is still available in case of the failure of one leg. However, the service is "degraded" until the restoration of the failed leg is completed.
	In principle, two types of 1+1 permanent protected connections can be distinguished: • **P1PLUS1NRNR**: Both the 1+1 SNCP/UPSR end-to-end network protection as well as the auto-reroute path restoration are non-revertive. The first "NR" in "NRNR" corresponds to the operation mode of the auto-reroute path restoration, the second "NR" in "NRNR" corresponds to the operation mode of the 1+1 SNCP/UPSR end-to-end network protection. • **P1PLUS1NRRV**: The auto-reroute path restoration is non-revertive ("NR") while the 1+1 SNCP/UPSR end-to-end network protection is revertive ("RV").
M:N shared protection	An M:N shared protection consists of N worker routes which are protected by M protection routes of equal bandwidth. The supported M:N protection ratios are 1:1, 1:2 and 1:3. Thus, one protection route is available to protect up to three worker routes.
Y-connection	A Y-connection contains two paths which start at the same source node (on the same port and timeslot), but terminate at different destination nodes. This ONNS construct can be used as a building block for a cross-domain 1+1 pair of paths (a logical ring), where the first half of both paths – in the ONNS domain – are set up by requesting a Y-connection, and the second half of both paths – outside of the ONNS domain – are set up by traditional means.

ONNS prerequisites

To enable the ONNS functionality specific 1675 LambdaUnite MSS configuration is required:

Hardware prerequisites

Enabling the Optical Network Navigating System (ONNS) in a 1675 LambdaUnite MSS network element requires a specific hardware configuration:

- Controller of type CTL/3S
  - hosts the ONNS ORP software (topology/connection routing)
  - integrates SCN stack
  - forwarding of NNI, LSA messages to other NEs
- Cross-connect and timing unit of type XC160, XC320/B, or XC640
  - hosts the ONNS core software (connection set-up/tear down/restoration)
  - initialization and (operational) state handling for ONNS
  - defect information processing and evaluation for connection switching/restoration

  Important! It is recommended to use *two* suitable Controllers (duplex control) and *two* suitable cross-connect and timing units (XCs). Thus, both the Controllers as well as the cross-connect and timing units are automatically 1+1 equipment protected.

Signaling communications network

The SCN provides a robust network that interconnects a number of NEs, for exchange of ONNS and UNI control messages. The SCN is optimized for support of high-speed forwarding of failure recovery messages (failure notification and path setup). Redundancy in the SCN is typically provided through a meshed network at least, where two node-diverse routes exist between any two ONNS NEs.

A DCC/LAN data network, that is referred to as the SCN (Signaling Communications Network), is established per ONNS domain. The SCN is separated from the management network. The SCN is the network that ONNS uses to exchange its control messages via a protocol stack with TCP/UDP, IP, MPLS, PPP, Link Control Protocol (LCP) for Point-to-Point Protocol (PPP), DCC, Ethernet-LAN and exchange of UNI messages via a protocol stack with IP, Ethernet-LAN. The service that the SCN provides to the ONNS application is the ability to establish and use TCP and/or UDP connections for the exchange of signaling messages and routing information. Each node in the SCN has a *unique IP address* for exchanging ONNS control messages and additionally each node with connected UNI clients has a additional unique IP address for exchanging UNI control messages. All IP addresses are distributed by the ONNS Topology Discovery function.

Important! For further details and operations information concerning the ONNS feature please refer to the chapter “ONNS tasks” in the *1675 LambdaUnite MSS User Provisioning Guide*.

Pipe ONNS connections

Pipe ONNS connections (adaptive rate ONNS connections) are ONNS connections for a specific bandwidth that support auto-adaptation of their SONET signal structure into substructures. ONNS connections that do not support auto-adaptation are called “fixed ONNS connections”.

Since auto-adaptation is supported for SONET networks only, it cannot be used in mixed SDH/SONET networks. No SDH I-NNI ports must exist in a network. This would lead to problems during protection switching and restoration.

Pipe ONNS connections are restricted to ONNS protection types *unprotected*, *1+1*and *Auto Reroute, non-revertive*. Depending on the protection type, an adaptive rate connection may have one or more pipes.

A *pipe* is defined as a single, end-to-end transmission path from an ingress node to an egress node between two connect points with tributary mode *adaptive*. Ingress and egress can be in the same node or in different nodes with zero or more intermediate nodes.

The ONNS connection type (pipe or fixed) is explicitly specified during connection setup.

1675 LambdaUnite MSS supports the following adaptive signaling rates:

- STS1-pipe
- STS3-pipe
- STS12-pipe
- STS48-pipe
- STS192-pipe

External Network-Network Interface (E-NNI)

General

An E-NNI port is a Network-Network Interface port connected to another E-NNI port in a different domain of the switched network. E-NNI ports run a standardized signaling and routing protocol for the purpose of interworking between ONNS control domains. This especially includes multi-vendor interworking as well as end-to-end routing in spite of different operators and different SLAs within the individual domains.

1675 LambdaUnite MSS systems support an External Network-Network Interface (E-NNI) based on the following standards:

- OIF E-NNI 1.0:
  - OIF-E-NNI-Sig-01.0: Intra-Carrier E-NNI Signaling Specification
  - OIF-E-NNI-OSPF-01.0 External Network-Network Interface (E-NNI) OSPF-based Routing - 1.0 (Intra-Carrier) Implementation Agreement

Transport services

The transport services offered over the E-NNI include:

- Connection setup
- Connection release (tear-down)
- Connection status query

Protection or restoration mechanisms

E-NNI inter-domain links can be unprotected or 1+1 MSP/APS protected.

However, please note that a network connection wide end-to-end protection is *not* supported.

Network connection setup

A new network connection can be setup by means of a network connection setup request which is initiated by a management system and sent to the source node of the new network connection.

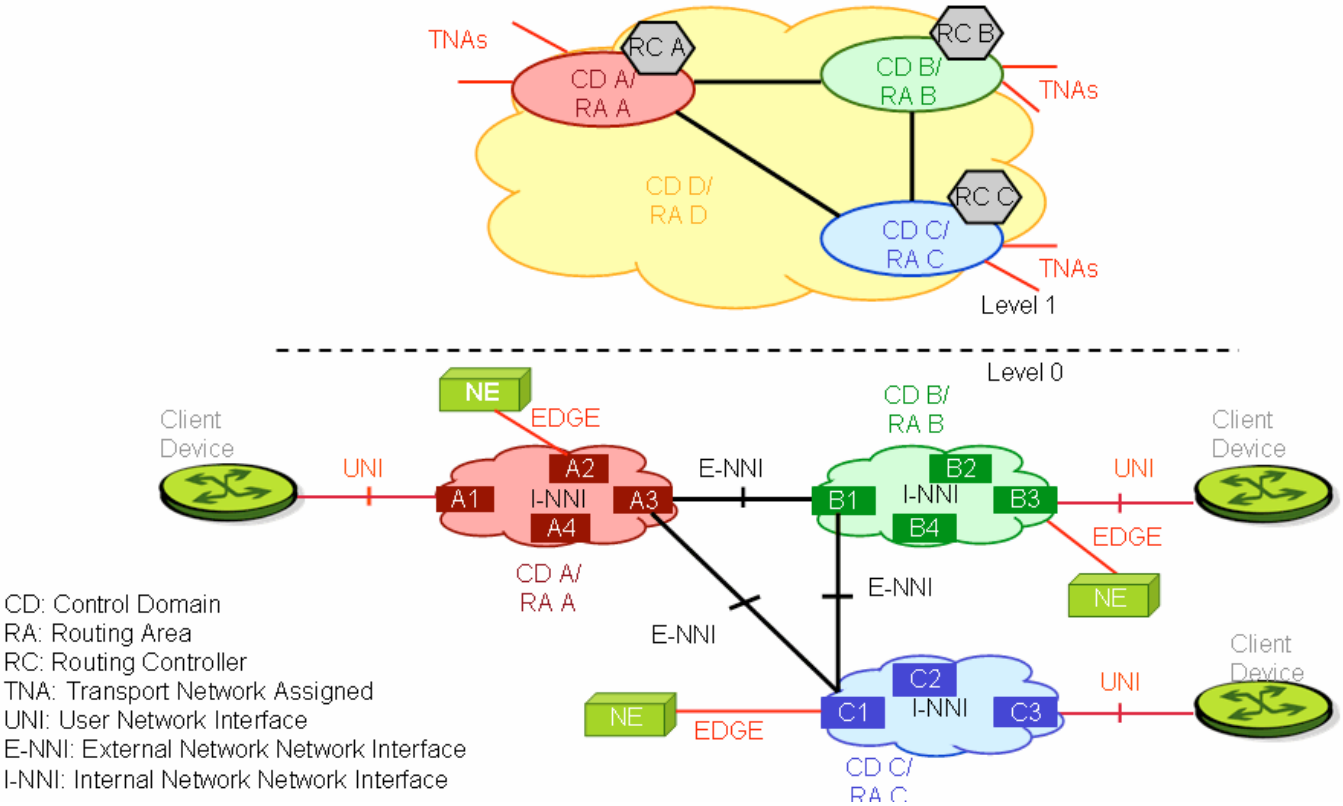
The source node (or “initiator node”) is addressed by the source TNA address. A network connection can be setup between Edge ports with provisioned TNA addresses.

If the end point of a network connection is in a different ONNS control domain then the path for the network connection will be calculated using level 1 routing.

E-NNI routing

1675 LambdaUnite MSS systems support E-NNI routing based on the OIF-E-NNI-OSPF-01.0 External Network-Network Interface (E-NNI) OSPF-based Routing - 1.0 (Intra-Carrier) Implementation Agreement.

The following graphic serves as a reference for some of the terms and definitions related to E-NNI routing:



Legend:

RA: Routing area	Defines a set of transport resources in the network for which the routing applies. A routing area is defined by a set of subnetworks, the links that interconnect them, and the links exiting that routing area. A routing area may contain smaller routing areas interconnected by links.
RC: Routing controller	The routing controller provides the routing service interface and is responsible for coordination and dissemination of routing information. A single node in the level 0 area can be configured as routing controller, which advertises all intra-area information of the level 0 area into the level 1, thereby summarizing the complete level 0 area as a single node (“abstract node”).

TNA: Transport Network Assigned	End points, which are not local to a specific ONNS control domain and which are addressable (or reachable) across control domains need a unique identifier. The Transport Network Assigned (TNA) address, together with a logical link identifier and a label is used for this purpose. A TNA address can be associated with a UNI or Edge port.
Routing hierarchy	A routing hierarchy describes the relationship between a routing area and a containing routing area or contained routing areas. routing areas at the same depth within the routing hierarchy are considered to be at the same routing level. The lowest level in a routing hierarchy, in which all the physical nodes (NEs) and links are visible, is denoted as “level 0”. A routing area, which contains a set of level 0 routing areas is referred to as level 1 routing area. The topology of the level 1 routing area may provide an abstract view, i.e. resources (nodes and links) may not represent physical resources.

E-NNI control channel

For the exchange of E-NNI signaling messages between two adjacent E-NNI nodes (adjacent but in different ONNS control domains), a signaling control channel named “E-NNI control channel” must be defined. This logical channel may be realized by one or more physical communication channels (in-band DCC or out-of-band LAN). As the E-NNI control channel must support the transport of IP packets, it is often also referred to as IP control channel (IPCC).

Configurable neighbor relations for E-NNI ports

The E-NNI control channel as well as neighbor information for signaling and routing are to be provisioned manually:

- For the communication between routing controllers, routing controller adjacencies must be configured manually.
- For the communication between signaling controllers, manual routes towards the subnet of the neighbor domains must be created.

E-NNI configuration parameters

The following interface ports can be configured to be E-NNI ports:

- SDH:
  - STM-1
  - STM-4

  - STM-16
  - STM-64
- SONET:
  - OC-3
  - OC-12
  - OC-48
  - OC-192

Important! *Consistent provisioning of E-NNI configuration parameters is essential in order for E-NNI routing and signaling to work properly!*

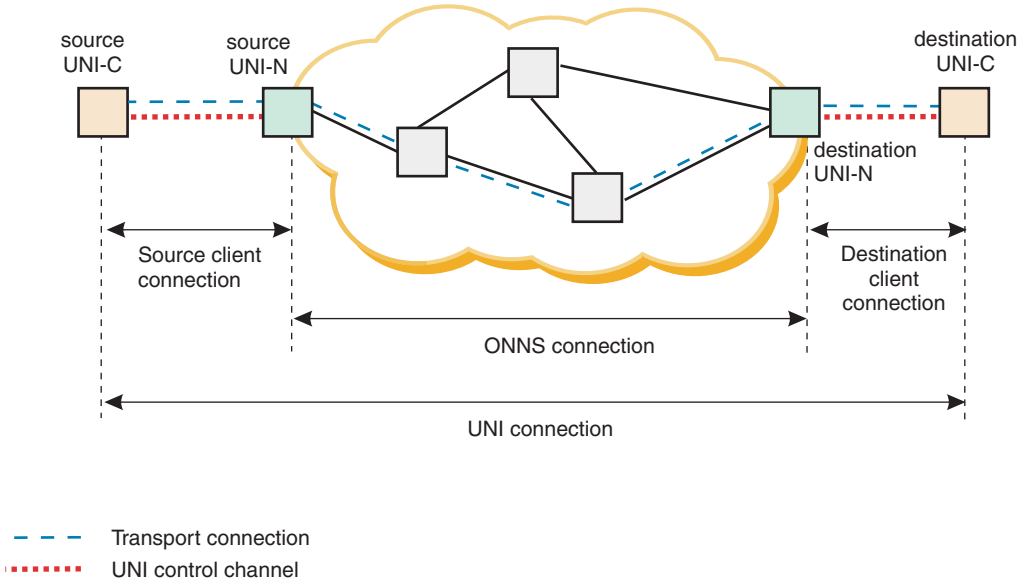
User-Network Interface (UNI)

Introduction

1675 LambdaUnite MSS systems support a User-Network Interface (UNI) based on the following standards:

- OIF UNI 1.0 Release 2:
  - OIF-UNI1-R2-Common - User Network Interface (UNI) 1.0 Signaling Specification Release 2: Common Part
  - OIF-UNI1-R2-RSVP - RSVP Extensions for UNI 1.0 Signaling, Release 2

The User-Network Interface (UNI) is an interface which makes it possible for a source client (source UNI-C) to request certain transport services to a destination client (destination UNI-C). These transport services are requested via the Optical Network Navigation System (ONNS) by the UNI network counterpart, the UNI-N.



Legend:

UNI-C	User-Network Interface - client side
UNI-N	User-Network Interface - network side
ONNS connection	An end-to-end connection between two UNI-N ports within the ONNS domain created by ONNS.
UNI connection	An end-to-end connection between 2 UNI-Cs consists of a source and destination client connection and an ONNS connection.

Important! 1675 LambdaUnite MSS systems can take on the UNI-N role (Transport NE, TNE).

Transport services

The transport services offered over the UNI include:

- Connection setup
- Connection release (tear-down)
- Connection status query

UNI connection setup

The creation of a UNI connection can be triggered by a connection create request from the source UNI-C. In this connection create request, the source and destination TNA addresses of the UNI connection must be specified as well as the attributes that describe the service requirements for the connection (signal rate, disjointness, etc.).

UNI connection endpoints are identified by Transport Network Assigned (TNA) adresses An individual data link within a group of links sharing the same TNA address is identified by a logical port identifier at each end.

UNI control channel

For each pair of UNI-C/UNI-N connected to each other in an ONNS domain for the exchange of UNI signaling messages exactly one unique logical channel named “UNI control channel” must be defined. This logical channel may be realized by one or more physical communication channels (in-band DCC or out-of-band LAN). As the UNI control channel must support the transport of IP packets, it is often also referred to as IP control channel (IPCC).

No automatic neighbor discovery for UNI ports

There is no automatic neighbor discovery between UNI-C and UNI-N.

The UNI control channel as well as transport link address information needs to be provisioned manually.

Monitoring and diagnostics features

Performance monitoring

1675 LambdaUnite MSS monitors performance parameters for 24-hour and 15-minute intervals on the synchronous and Ethernet transmission interfaces, so monitoring can be full-time for each signal. For further information please refer to "Performance monitoring" (p. 5-40).

Threshold reports

Additional to the common alarm status and normal/abnormal condition reports 1675 LambdaUnite MSS supports threshold reports (TRs). A TR is generated when a performance monitoring parameter threshold is exceeded, that can be set individually by the user for 24-hour and 15-minute intervals. For further information please refer to "Performance monitoring" (p. 5-40).

Port monitoring modes

Each physical interface can be in one of three different modes: automatic (AUTO), monitored (MON) or non-monitored (NMON). In NMON mode all alarms that originate in the physical section termination function are suppressed, while in the MON mode they are reported. In the AUTO mode alarms are suppressed until an incoming signal is detected, then the mode of the port switches automatically to MON.

Transmission maintenance signals

Regenerator section, multiplex section, and higher-order path maintenance signals are supported as per ITU-T Rec. G.783. The system can generate and retrieve path trace messages on STS-1 respectively higher-order VC-3 and VC-4 level as well as section trace messages respectively STM-N RSOH messages.

Path termination point monitoring modes

Each Path Termination Point can be in one of two different modes, monitored (MON) or non-monitored (NMON). In NMON mode all alarms that originate in the termination point are suppressed, while in the MON mode they are reported .

Provisioned state record

1675 LambdaUnite MSS automatically maintains a record of the provisioned state of each transmit and receive port on each circuit pack.

Loopbacks

1675 LambdaUnite MSS supports facility and cross-connection loopbacks for testing and maintenance purposes. These loopbacks are available for each supported signal type. Facility loopbacks are established electrically on port-level on a port unit, cross-connection loopbacks in the switching matrix. The available types are:

- Near-side loopback (in-loop)
- Far-side loopback (out-loop)
- Cross-connection loopback

The loopbacks can be configured via *WaveStar*® CIT or OMS.

Note that on ONNS I-NNI ports loopbacks are not allowed and therefore blocked by the system.

Local and remote inventory

The 1675 LambdaUnite MSS system provides automatic version recognition of the entire hardware and software installed in the system. This greatly simplifies troubleshooting, dispatch decisions, and inventory audits. A list of detailed information, see "Equipment inventory" (p. 2-58), is accessible via *WaveStar*® CIT or via *Navis*® OMS.

Self diagnostics (in-service)

The system runs audits and diagnostics to monitor its health. These self-diagnostics do not have any effect on the performance of the system.

Auto-recovery after input power interrupt

The system will restore itself automatically after an interruption of the power.

Recovery from configuration failures

If the system detects that its configuration database is empty or corrupted it will remain in the current configuration without impacting the traffic, it will raise an alarm, and the configuration database can then be updated via the management system.

3 Network topologies

Overview

Purpose

This chapter describes the key applications of 1675 LambdaUnite MultiService Switch (MSS). It gives an overview of the various network applications and identifies the key functions associated with these applications.

Network tiers

Optical networks can be structured into three tiers in order to simplify their understanding, modelling and implementation:

- Backbone (tier 3)
- Metro core/regional (tier 2)
- Access (tier1)

Due to the flexibility of 1675 LambdaUnite MSS it is able to cover many different applications especially in the backbone and metro core/regional tier. The following sections will identify some of the main applications and configurations for which 1675 LambdaUnite MSS is optimized.

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Backbone applications

Overview

Purpose

This section provides, after a brief introduction to the backbone topology, information about backbone applications for 1675 LambdaUnite MSS.

Characterization of tier 3 topologies

The backbone network tier typically shows the following features:

- Ring and meshed network topology
- Long and very long distance (several thousand kilometers)
- High capacity per fiber (multiple terabit/s)
- Efficient protection schemes (e.g. 4-fiber BLSR/MS-SPRing)
- Traffic patterns of big pipes (2.5 Gbit/s and beyond)
- Edge grooming (45-Mbit/s up to 10-Gbit/s services)

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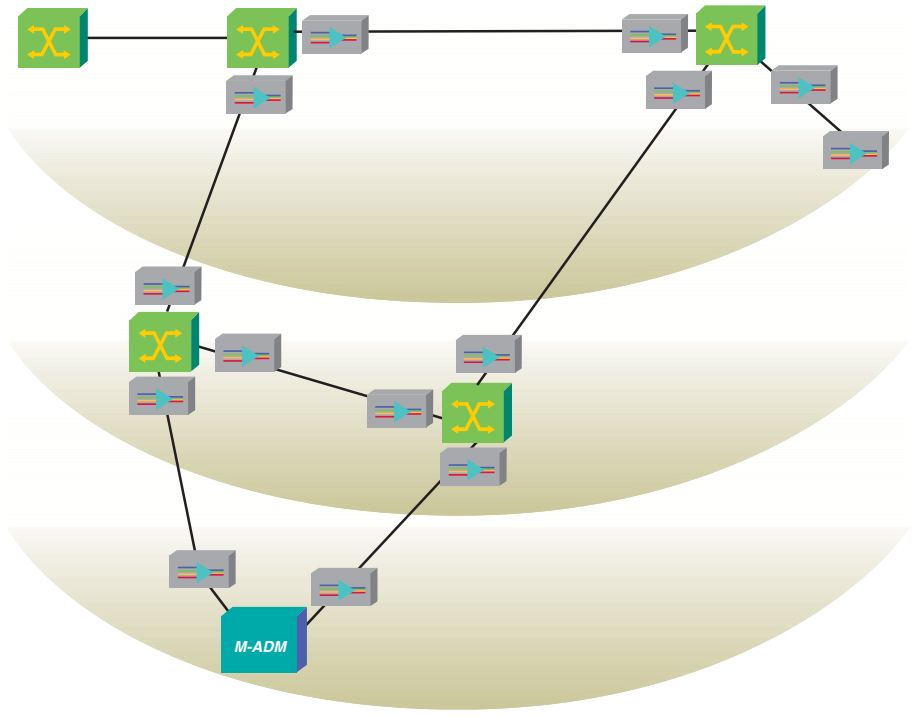
Classical backbones

This application shows the fit of 1675 LambdaUnite MSS in a backbone transport network, which uses a combination of DWDM and SONET/SDH technology in a meshed topology.

Classical backbone example

The given application is characterized by the use of DWDM equipment for cost optimized long distance point-to-point transport to link the Points of Presence (PoP) in the network. Network Protection Equipment (NPE) based on SONET/SDH is used in the PoPs to protect and redirect traffic, as well as to monitor transport quality and isolate faults. The service capacities handled in this network range from STS-1 (50 Mbit/s) or VC-4 (155 Mbit/s) on the low end to concatenated service signals with speeds up to 10 Gbit/s. For optimal reliability stacked BLSR/MS-SPRing rings are provisioned through the NPE in different PoPs. The selection of the network elements which form a ring is determined by the topology and the traffic pattern of the network.



***LambdaUnite*® MSS**

***WaveStar*® OLS 1.6T / *LambdaXtreme*™ Transport**

***Metropolis*® ADM (Universal shelf)**

1675 LambdaUnite MSS fits very well in this application as the capacity and density of the system allows for easy and cost efficient scaling when new lambdas are lit in the DWDM equipment. Together with the *WaveStar*® BandWidth Manager and the *WaveStar*® TDM 10G solutions, Alcatel-Lucent offers an NPE solution set that meets all scalability needs. It is fully compliant to SONET/SDH cross-connection, protection and monitoring standards matching the expectations of operators. Additionally, in the case Alcatel-Lucent's DWDM equipment is used, 1675 LambdaUnite MSS offers direct optics into the DWDM equipment allowing for substantial savings with respect to cost and footprint.

As the network evolves high-performance mesh service restoration schemes based on an intelligent network element control plane, the Optical Network Navigation System (ONNS) will appear as a way to provision and protect services in the application above. 1675 LambdaUnite MSS is already supporting this functionality.

As transparent high capacity services are becoming more important, 1675 LambdaUnite MSS supports transparent DCC, and in future it will support further overhead transparent services. This way transparent high capacity services and all lower speed transport services can be served from a single layer high capacity network. Especially for applications with a substantial portion of the services being in the lower speed range, this is a powerful and flexible solution.

Transoceanic applications

This application shows the fit of 1675 LambdaUnite MSS in a transoceanic transport network.

Transoceanic network

A transoceanic network is categorized by very long distance point-to-point DWDM transport links (several thousand kilometer length through the ocean). In addition to these links, shorter DWDM transport links in the terrestrial portion of the network are used to backhaul the traffic from the landing point on the shore of the ocean into the business centers located somewhere deeper in the country. Normally only a small portion of the lambdas available in the undersea and the backhaul links are utilized initially. More lambdas are lit as demand increases.

Network Protection Equipment (NPE) based on SONET/SDH is used in the Points of Presence (PoPs) at the end points of the DWDM links to protect and redirect traffic, as well as to monitor transport quality and isolate faults. The service capacity ranges from VC-4 (155 Mbit/s) to concatenated service signals with speeds up to 10 Gbit/s. The rate per lambda is 10 Gbit/s and 40 Gbit/s.

1675 LambdaUnite MSS in transoceanic topologies

1675 LambdaUnite MSS fits ideally in the transoceanic application mainly because of the following reasons: The system supports the special transoceanic version of the MS-SPRing protocol, which is a mandatory requirement for the given application. Even preemptible protection access is supported. 1675 LambdaUnite MSS allows for easy and cost efficient scaling when new lambdas are lit in the transoceanic and/or backhaul link. It’s capability to support both SONET and SDH out of single node makes it a perfect vehicle for transatlantic links.

Metro core/regional applications

Overview

Purpose

This section provides some information about the metro core/regional network tier and about regional/metro applications of 1675 LambdaUnite MSS.

Characterization of tier 2 topologies

The metro core/regional network tier typically shows the following features:

- Dominated by ring network topology
- Mid range distance (up to ~ 200 km)
- Partially high capacity per fiber (terabit/s)
- Efficient protection schemes (e.g. 2-fiber/4-fiber BLSR/MS-SPRing)
- Mixed traffic patterns (from 45-Mbit/s up to 10-Gbit/s services)
- Grooming (1.5-Mbit/s up to 10-Gbit/s services)
- Synchronous and data interfaces

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Ring topologies

For many metro core applications around the world a ring based network topology with the associated ring protection schemes is used. This application example shows the benefits of using a 1675 LambdaUnite MSS system in these applications.

Ring based metro core/regional application

In this ring based application 1675 LambdaUnite MSS plays the role of a very flexible and expandable multi-ring terminal. This and the data capabilities of the system make it a representative of the new optical switches or Optical Edge Devices (OEDs), which are brought to market and are greatly replacing classical ADM products. The multi-ring capability of the OED applies both to the aggregation of access rings as well as grooming between neighboring metro core/regional rings, which come together at central PoPs.

As described in the network picture below, the multi-ring terminals are aggregating traffic from the access rings, huge data nodes and business parks. Grooming functionality (down to STS1/HO-VC-3 or VC-4 level) together with flexible time slot assignment or interchange and fully non-blocking switching capability enables high utilization in the high rate metro ring.

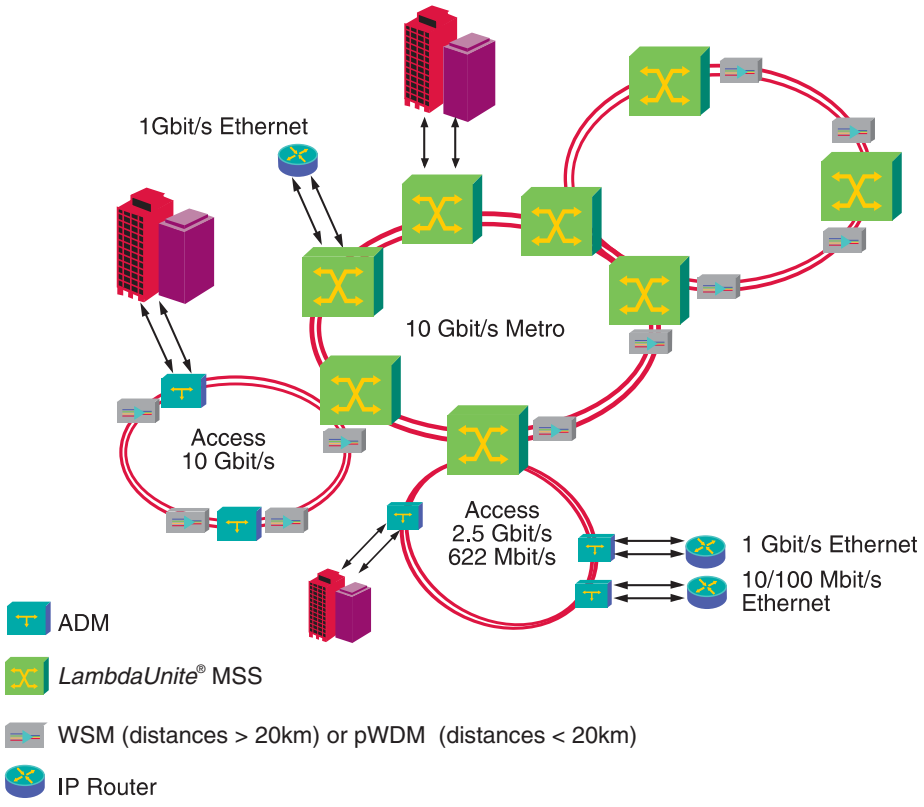
Taking into account the size of the metro ring (typically 5 to 10 nodes), 155-Mbit/s (later release), 622-Mbit/s (later release), 2.5-Gbit/s and 10-Gbit/s tributary interfaces are supported to address an appropriate bandwidth ratio between access and metro rings. The support of Gigabit Ethernet with flexible bandwidth assignment to the GbE service port is a key functionality to fit the operator’s needs for flexible data transport solutions as part of a single network.

Due to the wide range of protection features supported by 1675 LambdaUnite MSS, protection schemes can be chosen dependent on traffic pattern (hubbed versus more neighboring traffic) and the protection schemes supported by the nodes to interwork with on the same ring. A combination with DWDM or “passive” WDM is supported for fiber constrained environments.

An important additional value proposition to use 1675 LambdaUnite MSS in these applications lies in the value that the system brings to the table as a backbone feeder node (see previous application descriptions). This way the very same 1675 LambdaUnite MSS system that interconnects Metro rings can also serve as the grooming and feeding device into the backbone network: All from a single node offering a very cost and space efficient solution.

Ring based metro core/regional example

In the following figure a ring based metro core/regional example is shown where 1675 LambdaUnite MSS acts as flexible multi-ring terminal, employing furthermore Wavelength Division Multiplexing, please refer to "Interworking with Wavelength Division Multiplexing" (p. 3-31).



Clear channel topologies

In clear channel topologies the client signal is transparently transported over the carrier network. Especially in the SONET/SDH world this service boosts the flexibility of service providers. Actually, there is a strong market interest in clear channel transport services for all types of client signals. With its transparent optical SONET/SDH interfaces 1675 LambdaUnite MSS extends its application range, fulfilling these market requests.

Typical non-transparent service model

SONET/SDH transport mechanisms require the termination of section/RS and line/MS layer information. The in-band OA&M channels associated with the section/RS and line/MS layers are terminated at the boundaries between two operator domains. Thus, important network management information such as data communication channels, parity bits etc. are terminated at the network boundaries.

In certain application topologies, however, the transparent transport of client SONET/SDH signals across a native SONET/SDH network can provide enormous advantages. Some examples for such applications are

- Transmission protections like BLSR/MS-SPRing
- Data Communications Networks
- ONNS domains.

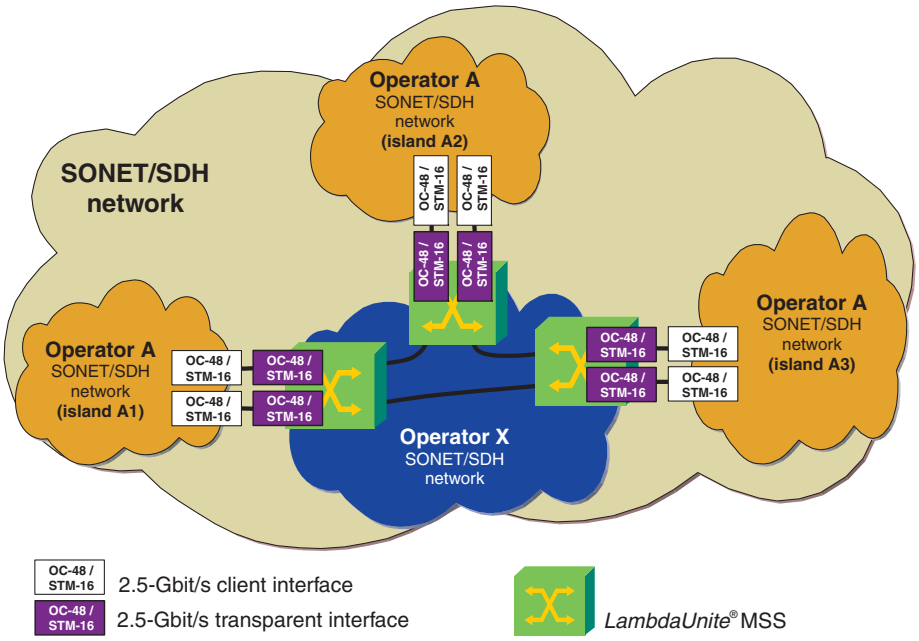
Transparent carrier’s carrier application

One of the scenarios where a new service model is required is the “carrier’s carrier” application, as shown in the figure below. Supposed that an operator or carrier (A) wants to extend the reach of his networking infrastructure over an area which is serviced by another SONET/SDH operator (X), for example by leasing transport capacity, he may ideally want each of the leased connections to behave as a virtual fiber.

With the transparent interfaces of 1675 LambdaUnite MSS operator X can support, as shown in the following example, a BLSR/MS-SPRing application of carrier A, with nodes in the different location islands A1, A2 and A3.

Carrier's carrier service example

The following figure shows the scheme of a carrier's carrier topology example, where operator X provides transparent connections to operator A. These so called clear channels are implemented by the 1675 LambdaUnite MSS 2.5-Gbit/s transparent interfaces, here depicted in purple.



Transparent enterprise VPN (SONET/SDH) application

There are many institutions (large enterprises, government institutions, railway or energy companies) who also deploy private SONET/SDH networks with their own networking infrastructure. These enterprises often wish to extend the reach of their enterprise SONET/SDH networks by leasing circuits from the local operators. At the same time they want to become interconnected in a seamless fashion, allowing to operate and manage the contracted channels as “virtual fibers” in their network infrastructure.

This application can be modelled in the same way as the previous example, depicted in the preceding figure, substituting operator A by an enterprise. Note that the requirements for an enterprise OC-n/STM-N service may be significantly different from the requirements for a carrier’s carrier OC-n/STM-N service. In particular, the requirements for timing transparency may be important for the carrier’s carrier service, but less so for the enterprise service. 1675 LambdaUnite MSS is able to cover both applications with its SONET/SDH transparency feature.

Meshed topologies

As new metro core networks are built, mesh based topologies are starting to appear in fiber-rich metro environments. The benefits that these new topologies bring along are only possible with systems designed like 1675 LambdaUnite MSS. The following application description explain these benefits enabled by 1675 LambdaUnite MSS.

Meshed metro core/regional topology

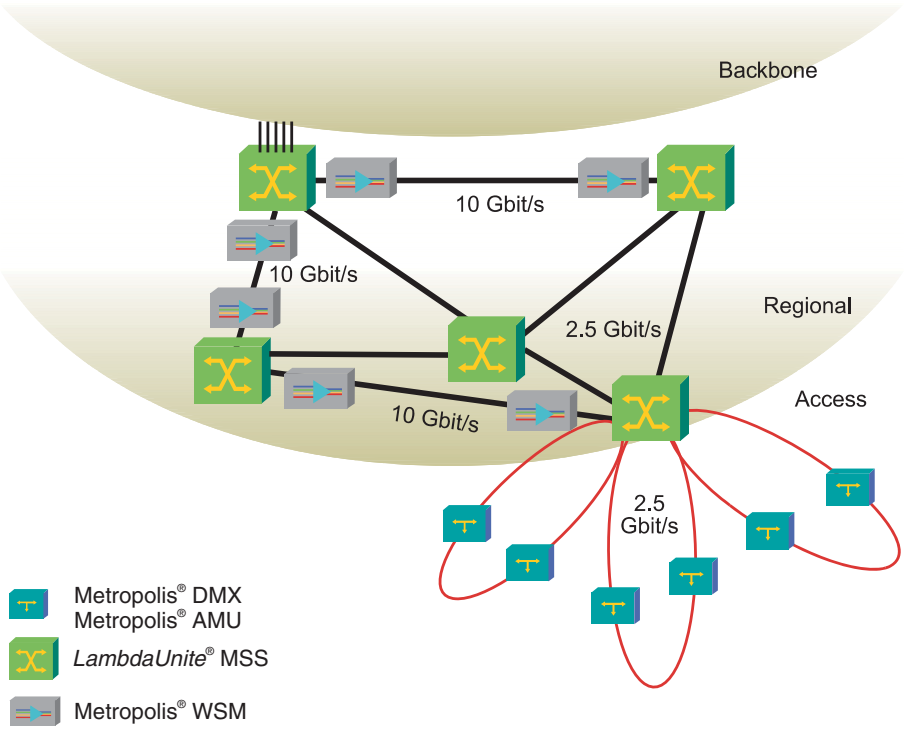
Due to the flexible architecture of 1675 LambdaUnite MSS the creation and expansion of a meshed metro core topology is very easy. This can be combined with the ring closure capability towards the access network (the access network is still dominated by ring topologies due to its hubbed traffic pattern). Taking these capabilities, the system plays the role of a flexible Optical Edge Device (OED) supporting a wide range of services and topologies.

As described in the picture below, the OEDs are aggregating the traffic from the access nodes and are forwarding it into the metro core meshed network. In this network type Optical Network Navigation System (ONNS) provides you with the highest flexibility of protection schemes and service activation methods. If ONNS is not used, the protection schemes used in the metro core network can be either BLSR/MS-SPRing or UPSR/SNCP, which supports meshed topologies more easily.

Service interfaces of the OEDs in the metro core network cover a wide range from 45 Mbit/s to 10 Gbit/s as well as Gigabit Ethernet. The fact that 1675 LambdaUnite MSS can act as a flexible backbone feeder node makes it possible to combine the metro core and backbone feeder node function into a single node, allowing for cost and floor space optimization.

Meshed metro core/regional example

As illustrated in the figure below, the 1675 LambdaUnite MSS network elements, for example performing multiple access ring closure, can be interconnected at different line rates to realize a meshed topology.



Traffic hubbing

As mentioned before in this chapter, one of the key values of the flexible 1675 LambdaUnite MSS architecture lies in its ability to efficiently hub traffic from lower tiers of the network and feed this traffic into the next higher tier. This section illustrates the specifics and the value of this hubbing function from the access layer into the metro core/regional layer as well as from the metro core/regional layer into the backbone layer.

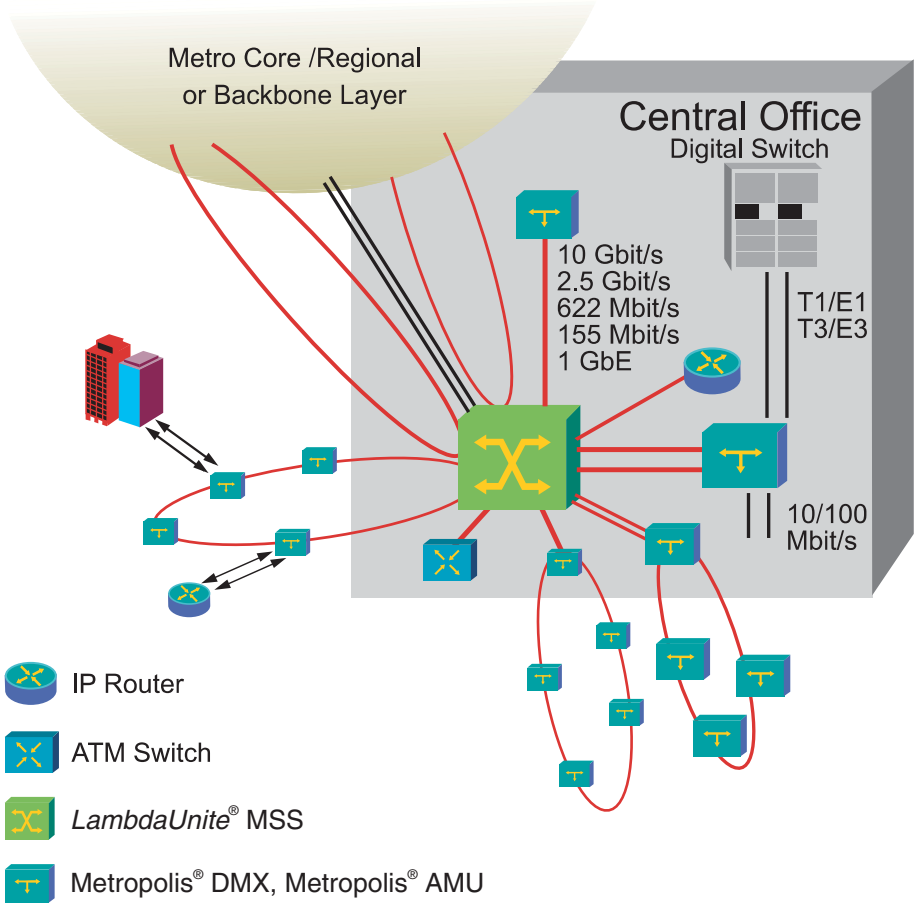
Metro core/regional hubbing example

In the 10-Gbit/s metro regional hub application 1675 LambdaUnite MSS acts as multi-ring terminal, hubbing (see also “Remote hubbing ” (p. 3-24)) the traffic from several lower rate access rings (see also “Closing rings” (p. 3-25)) and providing the interconnection to one or more 10-Gbit/s metro/backbone networks. 1675 LambdaUnite MSS will be typically located in a central office where it provides numerous local interconnections to several routers of the different IS-providers, voice switches, backbone multiplexers, and DWDM equipment of other network operators.

Grooming functionality, together with flexible time slot assignment/interchange and fully non-blocking switching capability, enables high utilization in the 10-Gbit/s metro/backbone rings. 1675 LambdaUnite MSS can be efficiently coupled with another SONET/SDH multiplexer mounted in the same bay, in order to provide access for electrical signals as well as grooming of some remaining lower-order traffic.

Depending on the capacity needs, configurations of 2-fiber and 4-fiber rings are supported, also in combinations with DWDM or “passive” WDM; see also “Growing demand for extra capacity” (p. 3-31).

In the following figure the 1675 LambdaUnite MSS is performing a hub function for various rings and point-to-point connections.



Access/metro applications

Overview

Purpose

This section provides information about access/metro tier characteristics and access/metro applications for 1675 LambdaUnite MultiService Switch (MSS).

Characterization of tier 1 topologies

The access/metro network tier typically shows the following features:

- Point-to-point and ring network topology
- Short distance (up to ≈ 40 km)
- Low capacity per fiber (2.5 Gbit/s and lower)
- Mixed traffic patterns (from 2 Mbit/s up to 2.5 Gbit/s services)
- Edge concentration
- Circuit and data interfaces

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Tier 1 applications

Although 1675 LambdaUnite MultiService Switch (MSS) with its 160-Gbit/s, 320-Gbit/s, or 640-Gbit/s switching matrix is designed rather for network tier 3 and tier 2 applications, it can be employed in topologies that initially perform access/metro functions, providing remarkable and cost-efficient growth capabilities.

Application details

Overview

Purpose

This chapter gives an overview of 1675 LambdaUnite MSS application details in basic topologies.

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Ethernet applications

Data services based on IP are becoming more and more important. With Ethernet being the native LAN interface for IP traffic, offering Ethernet interface based WAN transport services becomes an important element for competitive service offerings.

This section explains the Ethernet services and underlying applications supported by 1675 LambdaUnite MSS:

- Ethernet service types
- Inter-PoP (Point-of-Presence) services
- Corporate LAN interconnections

Ethernet service types

An Ethernet end-to-end transport service is the service that a service provider or operator delivers to an end-user, in which multiple access points of that customer are interconnected via physical Ethernet interfaces. The end-user Ethernet frames are transported transparently to the proper destination. A second type of Ethernet transport service is not really end-to-end, but is a back-hauling service whereby the end-user traffic is collected via a physical Ethernet access interface and handed-off at a central location to a service node (most likely an IP edge router). In this case Ethernet provides a transport function for services at the IP layer.

These service types are grouped in three applications based on Ethernet network topology:

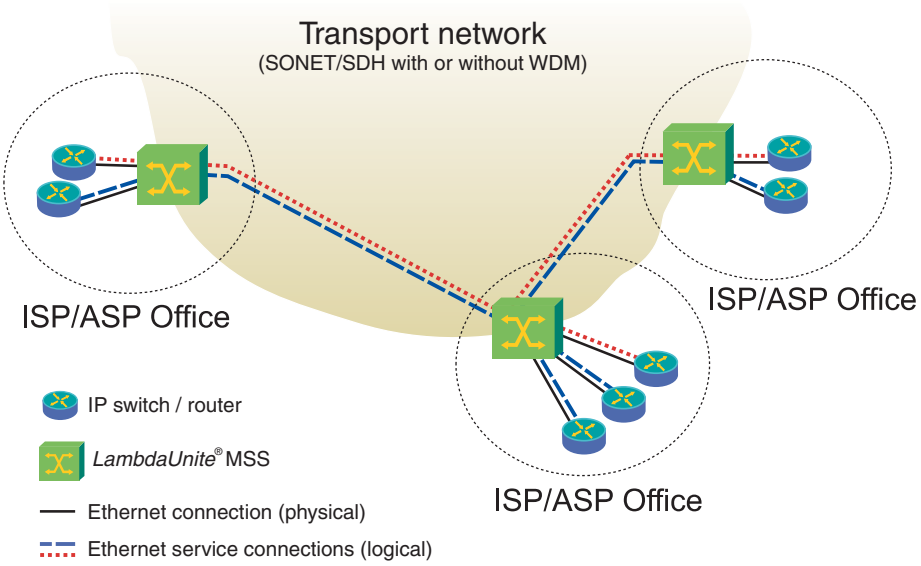
- point-to-point applications, see the first example of “Corporate LAN interconnections”
- multi-point applications, see the first example of “Inter-PoP services”
- trunking applications, see the second example of “Corporate LAN interconnections”.

For further information please refer to “Ethernet features” (p. 2-18).

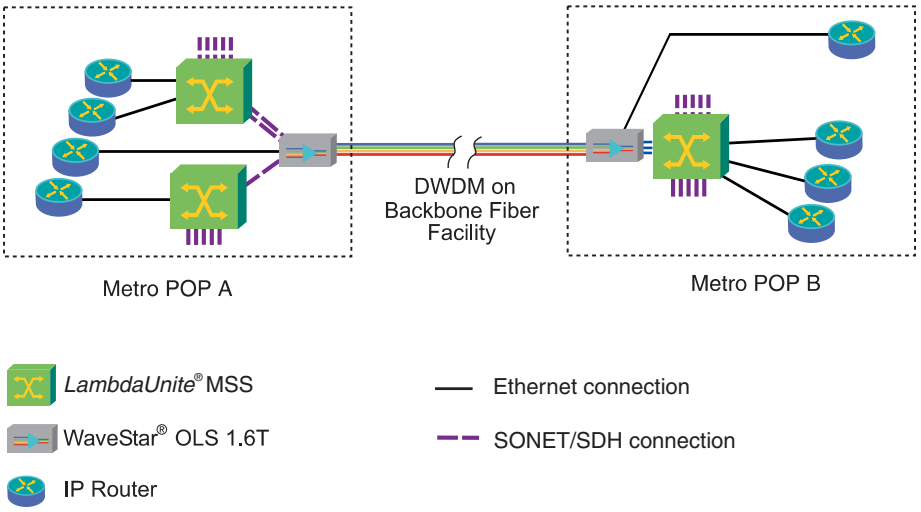
Inter-PoP services

Internet Service Providers (ISPs) and Application Service Providers (ASPs) need high but flexible bandwidth connections between their IP routers and their bandwidth wholesaler. An efficient solution for these connections are direct paths between the main routing locations (inter-PoP services) in the form of dedicated SONET/SDH and/or WDM signals, simply employing Ethernet interfaces in SONET/SDH add-drop-multiplexers.

This Hybrid Transport based on SONET/SDH with 1675 LambdaUnite MSS systems can provide high speed and simply leased line Ethernet connections between ISP/ASP offices over long distances, see the multi-point application example given in the following figure.



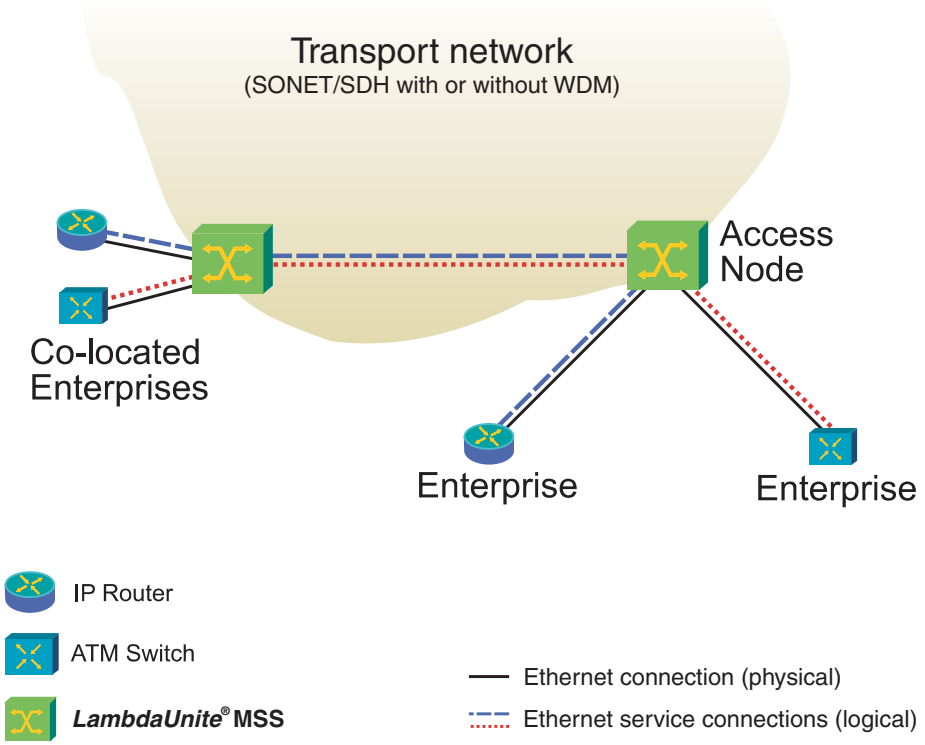
A specific option for high bandwidth between distant Metro POPs is to transport Gigabit Ethernet (GbE) traffic with Hybrid Transport over 1675 LambdaUnite MSS and *WaveStar*® OLS 1.6T, based on SONET/SDH and Dense Wavelength Division Multiplexing (DWDM) solutions, as shown in the example given in the following figure.



Corporate LAN interconnections

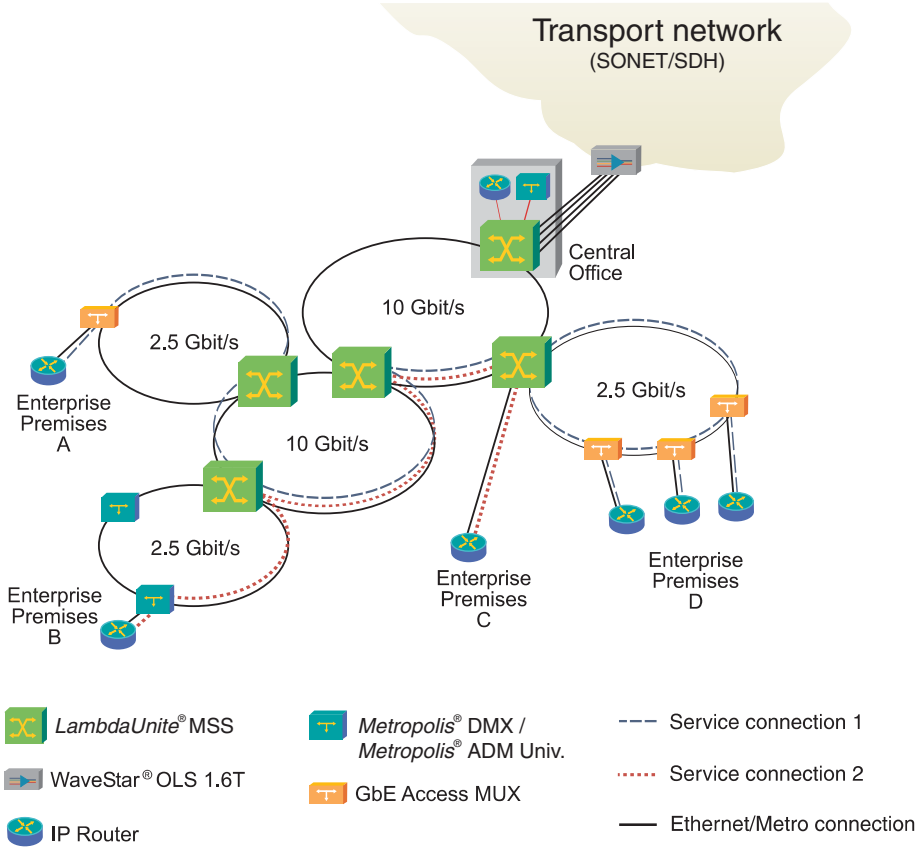
With the growing need to communicate across long distances, many enterprises find themselves faced with a severe problem: although they have Ethernet available in their Local Area Networks (LAN) in each of their geographically separated offices, a standard and cost-efficient way to connect them is missing.

Hybrid Transport with an Ethernet connection via 1675 LambdaUnite MSS systems over the public transport network (often referred to as Wide Area Network – WAN) provides a solution to this problem, connecting the different enterprise locations like in a single LAN. A schematic example for such a point-to-point corporate LAN interconnection with two 1675 LambdaUnite MSS systems is shown in the following figure.



The Alcatel-Lucent portfolio allows service providers to offer LAN interconnection services to their customers with throughput rates of up to 1 Gbit/s or 10 Gbit/s WANPHY. These high bandwidth service connections require a high capacity metro network, as shown in the following figure. In this example two corporate LAN interconnections are depicted, between enterprise premises A and D, and between B and C, employing 1675

LambdaUnite MSS as multi-ring terminals and in the central office, connecting the metro network over a *WaveStar*® OLS 1.6T system to the optical backbone transport network. In this figure also VLAN trunking is shown, for example in enterprise premises D.



Broadband transport

Broadband Services

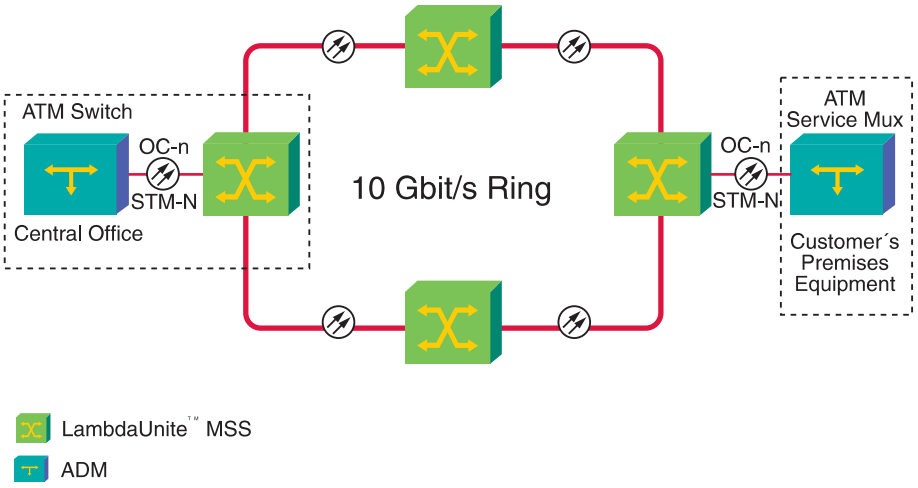
Broadband services that can be handled with 1675 LambdaUnite MultiService Switch (MSS) include:

- LAN interconnection
- Video distribution from a video server
- Medical imaging
- ATM traffic

These services can be conveniently switched by 1675 LambdaUnite MSS, for example as concatenated payloads (STS-3c/VC-4, STS-12c/VC-4-4c, STS-48c/VC-4-16c or STS-192c/VC-4-64c) - except LAN - over all available line interfaces; for LAN interconnection the special Gigabit Ethernet interfaces can be used.

ATM transport example

As an example, the figure below shows 1675 LambdaUnite MSS transporting ATM traffic between a central office and a customer's premises.



Remote hubbing

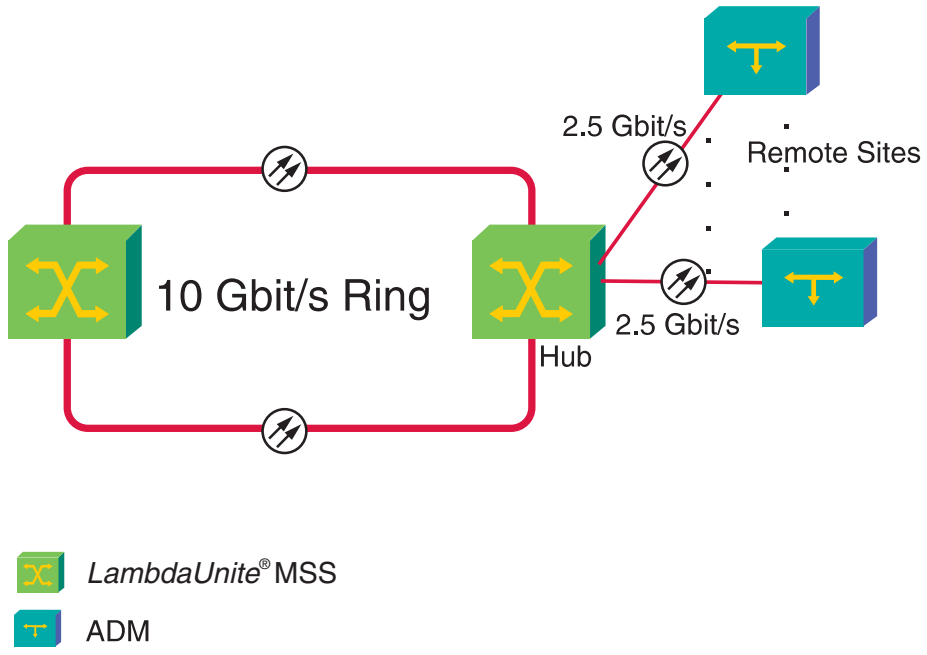
What is remote hubbing?

A network element is a hub when it is a collecting point for low rate lines. If the low rate lines are from remote sites, then the network element is performing remote hubbing.

1675 LambdaUnite MSS can perform remote hubbing for linear and ring networks. It can lower transport costs by consolidating lower rate traffic (typically 155 Mbit/s, 622 Mbit/s or 2.5 Gbit/s) and placing it on higher rate rings (typically 10 Gbit/s or 40 Gbit/s).

Remote hubbing linear networks

An example is shown in the following figure where a 1675 LambdaUnite MSS 10-Gbit/s ring serves a cluster of 2.5-Gbit/s multiplexers located at remote sites.



Remote hubbing ring networks

In some situations the traffic volume of a route does not justify the expense of a full ring. It may be practical to evolve a linear network to a ring network gradually, moving first to a folded ring (please refer to “Folded ring” (p. 3-25)).

However, you can still gain the benefit of a ring architecture on the route by using two interfaces per ring in one 1675 LambdaUnite MSS network element to close and link the rings. In this way 1675 LambdaUnite MSS acts as a hub for traffic from the lower rate ring that is to be carried on a 10-Gbit/s ring, see also “Ring topologies” (p. 3-25).

Ring topologies

Folded ring

A folded ring is a ring that uses a linear cable route between its end nodes. All traffic passes through the same geographical locations, perhaps even in the same cable sheaths between nodes, instead of through diverse locations. This is useful for networks in which not all locations are ready to be connected.

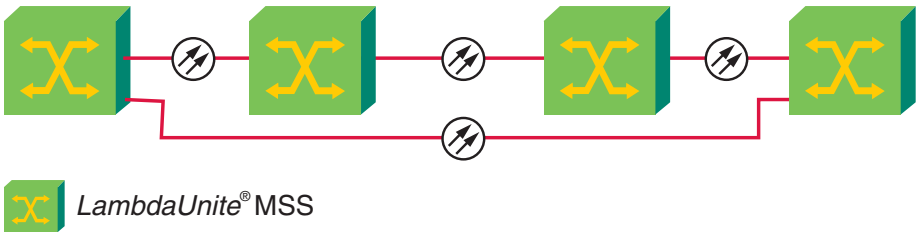
In many cases, a network starts out as a linear add/drop chain because of short-term service needs between some of the nodes. Later, it evolves into a ring when there is a need for service and fiber facilities to other nodes in the network. It is easier to evolve the linear add/drop network into a full ring configuration if a folded ring is used in the nodes that have this short-term service need. Folded rings have upgrade, operational, and self-healing advantages over other topologies for this type of evolution.

Reliability

In a folded ring configuration the traffic can be protected against node failures, but not against a fiber cut if all the fibers are in the same cable sheath. However, a folded ring configuration does enhance the reliability of a linear route until there is enough traffic to warrant expanding to full rings.

Folded ring example

In the folded ring configuration, shown in the following figure, a linear add/drop chain has been upgraded to a folded ring configuration by connecting the end nodes together.



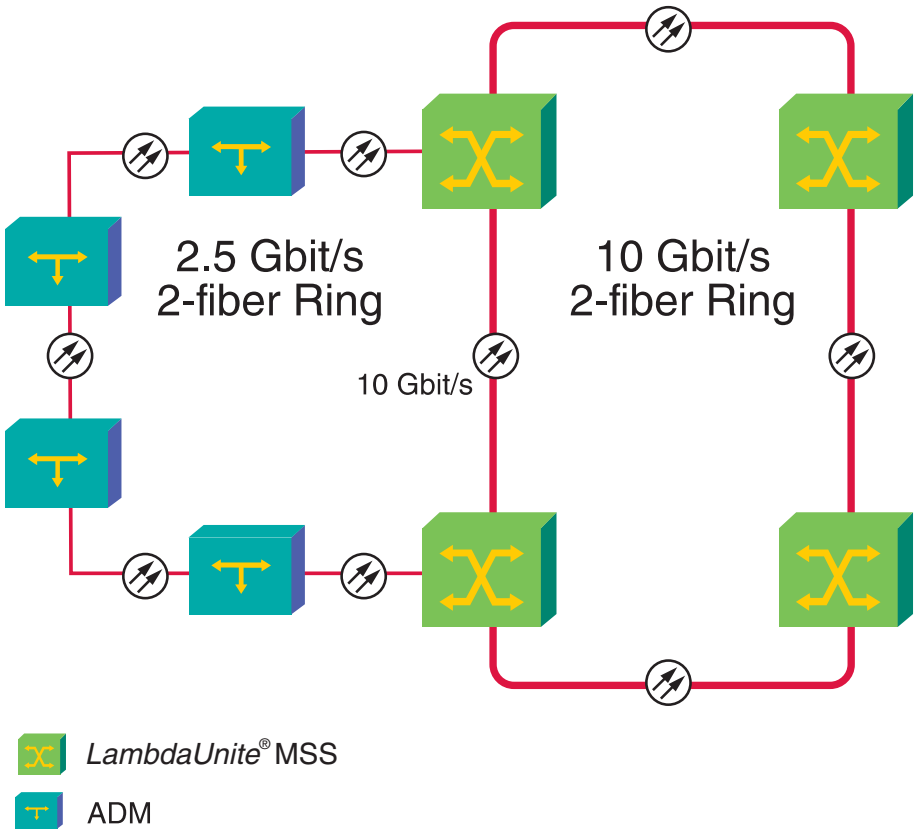
Closing rings

If a linear network is geographically close enough to a backbone system, then the linear network can be upgraded to a ring network by connecting both ends to the backbone. Traffic from the newly-formed ring can be transported by the backbone system, thereby closing the ring. This is referred to as closing or completing the ring, or ring transport.

A 1675 LambdaUnite MSS 10-Gbit/s ring carrying backbone traffic can be used to close up to 64 2.5-Gbit/s rings. The example below shows how 1675 LambdaUnite MSS 2.5-Gbit/s interfaces can provide transport for a 2.5-Gbit/s ring. The 1675 LambdaUnite MSS 10-Gbit/s ring provides 48 OC-1s or 16 STM-1s of bandwidth to close one 2.5-Gbit/s ring.

Dual homing ring closure

In the following figure, a 1675 LambdaUnite MSS 10-Gbit/s ring is used to close an 2.5-Gbit/s ring. The topology example shown here is also known as dual-homing ring closure.



In a *dual*-homed ring configuration, one ring connects to the other by *two* 1675 LambdaUnite MSS nodes, one 2.5-Gbit/s interface connection each, like in this example.

In a *single*-homed ring configuration, one ring connects to the other by a *single* 1675 LambdaUnite MSS node, with two 2.5-Gbit/s interface connections.

Note that MS-SPRing can be implemented in the given example only in the 10-Gbit/s ring.

Multiple ring closure

1675 LambdaUnite MSS is an ideal ring closure network element because its architecture, although extremely compact, allows the insertion of up to 32 10-Gbit/s interfaces, or up to 128 2.5-Gbit/s ports in one single shelf. Therefore up to sixteen 10-Gbit/s rings or up to 64 2.5-Gbit/s rings can be closed by one 1675 LambdaUnite MSS. For all rings BLSR/MS-SPRing protection can be configured.

Low rate grooming and protection

Closing rings that carry traffic structured below the STS-1 or HO-VC-3 level, it may occur to perform

- grooming
- path protection

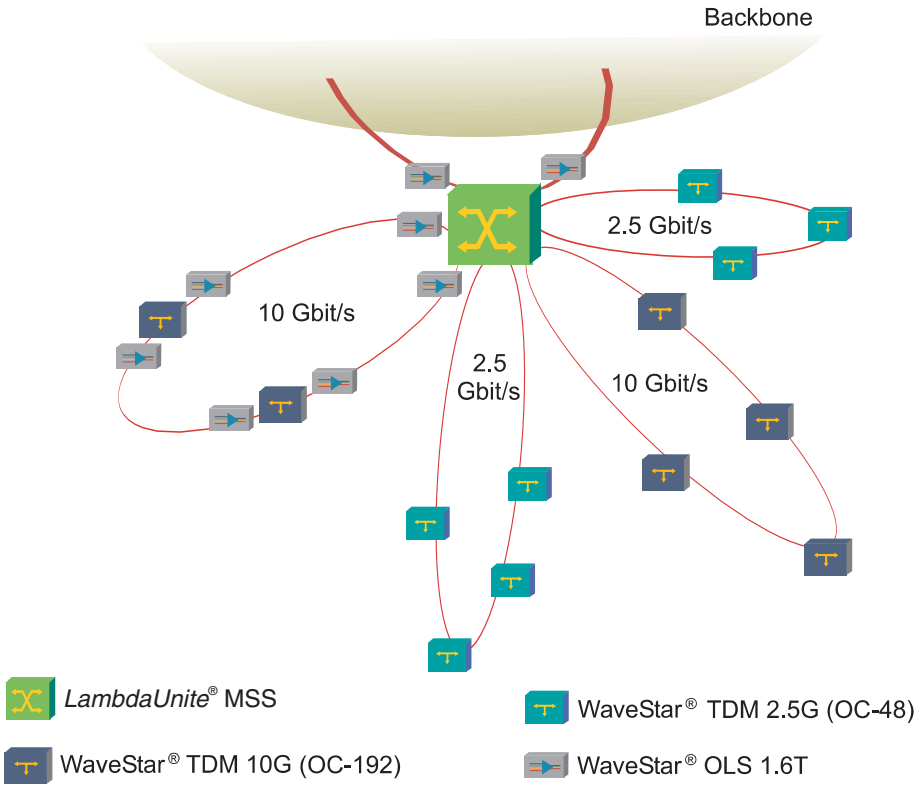
on traffic rates below the STS-1 or HO-VC-3 level. This can be done by connecting an additional lower rate ADM to 1675 LambdaUnite MSS, as shown in "Metro core/regional hubbing example" (p. 3-15). In this example 1675 LambdaUnite MSS is used as a hub in the central office with two *Metropolis*®DMX or *Metropolis*® ADM universal connected to it as grooming devices.

Interworking with *WaveStar*® TDM 10G/2.5G and *Metropolis*® ADM universal

To provide grooming and feeding in the metro/core and access layer the 1675 LambdaUnite MSS system can be connected to various Add-Drop-Multiplexers (ADMs). Due to its flexibility 1675 LambdaUnite MSS supports contemporary interworking with SONET- and SDH- ADMs, provisioning the respective interface port according to the particular standard. This section describes interworking examples with some ideally fitting SONET- and SDH- ADMs: *WaveStar*® TDM 10G (OC-192) and *WaveStar*® TDM 2.5G (OC-48), respectively *WaveStar*® TDM 10G (STM-64) and *Metropolis*® ADM universal.

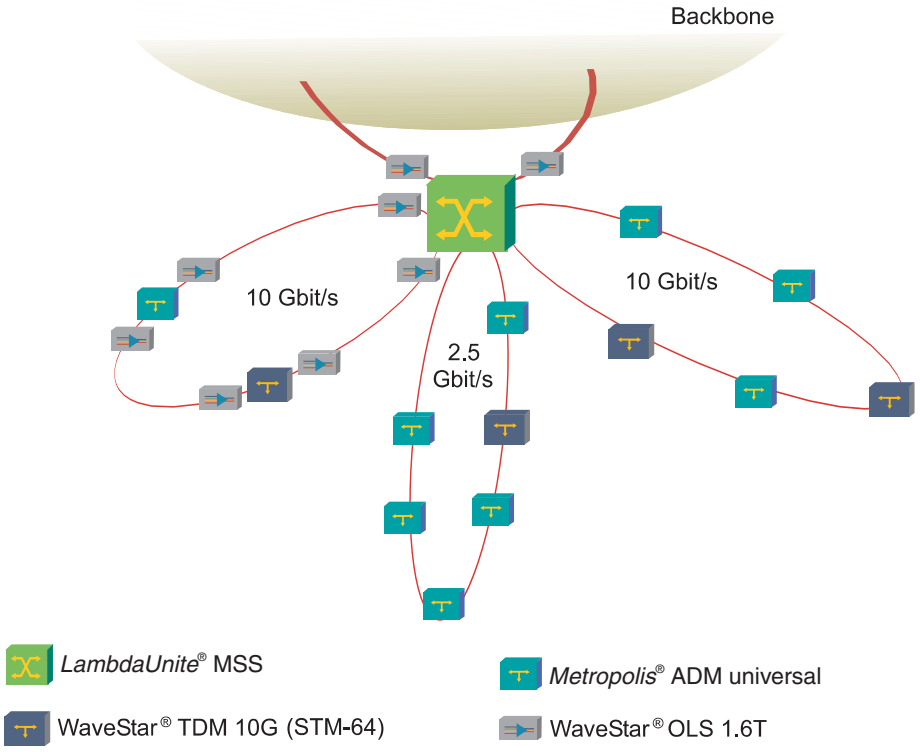
Interworking with *WaveStar*® TDM 10G (OC-192) and *WaveStar*® TDM 2.5G (OC-48)

The following figure shows an application example of 1675 LambdaUnite MSS acting as a multi-ring terminal, connected to several ring topologies with *WaveStar*® TDM 10G (OC-192) systems, partly employing DWDM interfaces, and with *WaveStar*® TDM 2.5G (OC-48) systems.



Interworking with *WaveStar*® TDM 10G (STM-64) and *Metropolis*® ADM universal

The figure below shows an application example of 1675 LambdaUnite MSS acting as a multi-ring terminal, connected to several ring topologies with *WaveStar*® TDM 10G (STM-64) systems, partly employing DWDM interfaces, and with *Metropolis*® ADM universal systems.



Interworking with *WaveStar*® BandWidth Manager

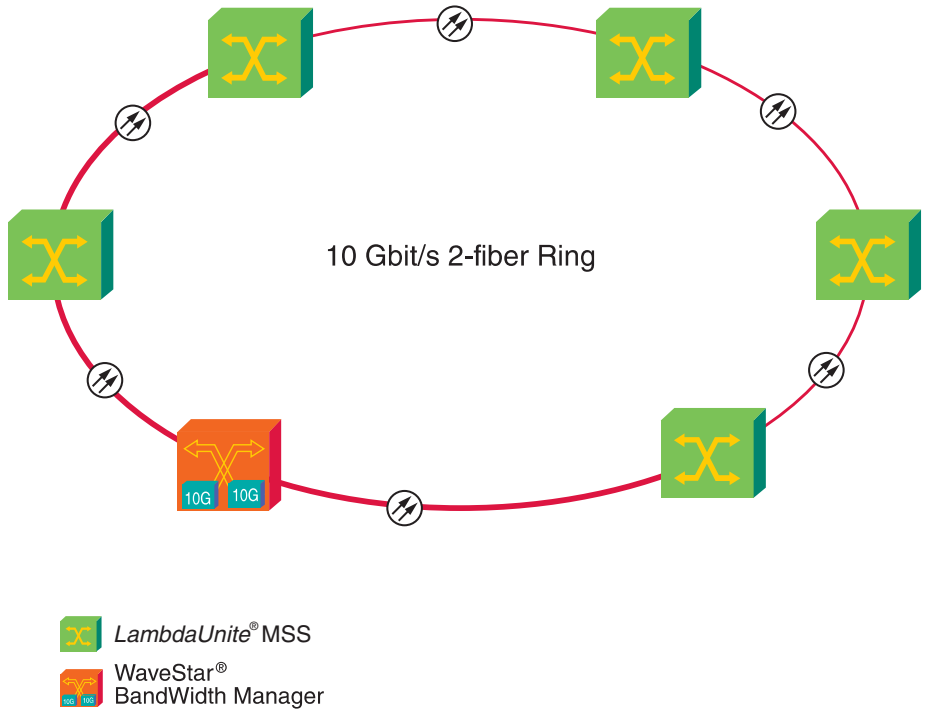
What is *WaveStar*® BandWidth Manager?

The *WaveStar*® BandWidth Manager integrates all access and transport rings within a network and efficiently manages bandwidth among these rings via a modular, scalable Synchronous Transport Module (STM) fabric. The switching unit is surrounded by a common input/output and managed by a common system controller.

BWM as direct part of 10-Gbit/s ring

WaveStar® BandWidth Manager can be equipped with integrated 10-Gbit/s interfaces, therefore it can be used as direct part of a 10-Gbit/s ring, BLSR/MS-SPRing protected in the 4-fiber as well as in the 2-fiber condition.

The following figure illustrates the interworking of 1675 LambdaUnite MSS with the *WaveStar*® BandWidth Manager in a 2-fiber ring example.



BWM connection via synchronous interfaces

It is possible to connect the *WaveStar*® BandWidth Manager, via 10-Gbit/s, 2.5-Gbit/s, 622-Mbit/s or 155-Mbit/s interfaces to 1675 LambdaUnite MSS.

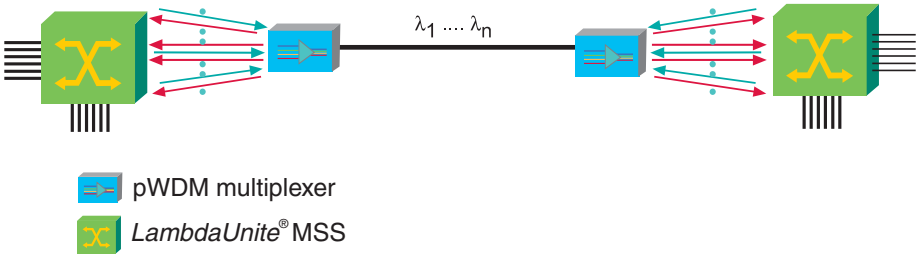
Interworking with Wavelength Division Multiplexing

Growing demand for extra capacity

A very efficient way to increase the capacity per fiber is to use distinct wavelength channels. 1675 LambdaUnite MSS supports both, passive Wavelength Division Multiplexing (pWDM) and Dense Wavelength Division Multiplexing (DWDM).

Passive WDM

Via the pWDM interfaces 1675 LambdaUnite MSS can interwork with the particular Original Equipment Manufacturer (OEM) pWDM multiplexer, as illustrated in the following figure. Up to 32 different 2.5-Gbit/s signals can be passively multiplexed into a single fiber and transported cost-efficiently over short and intermediate distances this way.



For further information about the OEM pWDM multiplexer please refer to the Appendix D of the *1675 LambdaUnite MSS Installation and System Turn-up Guide*.

The 1675 LambdaUnite MSS interfaces for pWDM applications are:

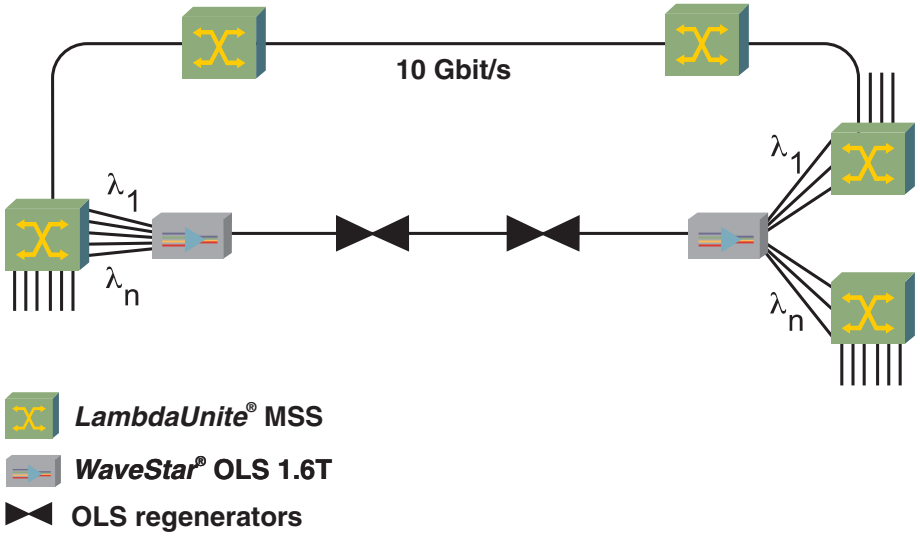
- 2.5-Gbit/s pWDM compatible interface (parent board with optical modules), 32 wavelengths.

Dense WDM

Dense Wavelength Division Multiplexing (DWDM) systems can be used with the 1675 LambdaUnite MSS for cost-efficient data transport over long and intermediate distances:

- *LambdaXtreme*™ Transport
- *WaveStar*® Optical Line System (OLS) 1.6T
- *Metropolis*® Enhanced Optical Networking (EON).

The following figure shows a topology example using the *WaveStar*® OLS 1.6T to transmit traffic from several 1675 LambdaUnite MSS aggregate interfaces via one single optical line.



LambdaXtreme™ Transport

With *LambdaXtreme*™ Transport the traffic of up to 64 different 40-Gbit/s signals can be transmitted via one single optical fiber. Using special lasers (“colored laser”) in the 1675 LambdaUnite MSS system, which all have their individual wavelengths, it is possible to connect the 40-Gbit/s interfaces of 1675 LambdaUnite MSS directly to *LambdaXtreme*™ Transport.

Alternatively, Optical Translators (OTs) can be used to translate the out-coming wavelength of the 40-Gbit/s interfaces and 10-Gbit/s interface to wavelengths specified for DWDM systems.

Distances of up to 4000 km can be bridged by using *LambdaXtreme*™ Transport together with 1675 LambdaUnite MSS.

WaveStar® OLS 1.6T and *Metropolis*® EON

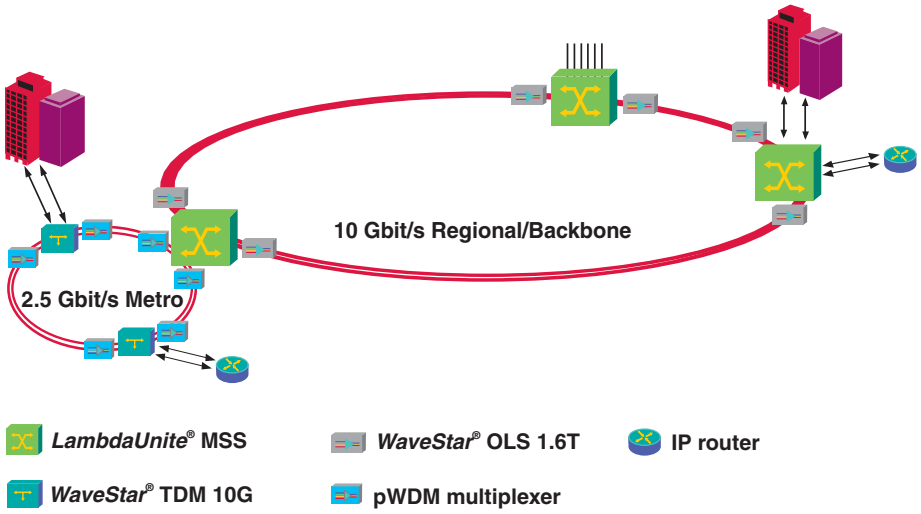
Using the *WaveStar*® OLS 1.6T, the traffic of up to 80 different 10-Gbit/s signals, with the *Metropolis*® EON up to 32 different 10-Gbit/s signals can be transmitted via one single optical line. Using special lasers (“colored laser”) in the 1675 LambdaUnite MSS system, which all have their individual wavelengths, it is possible to connect the 10-Gbit/s interfaces of 1675 LambdaUnite MSS directly to *WaveStar*® OLS 1.6T.

With *Metropolis*® EON and *WaveStar*® OLS 1.6T Optical Translator Units (OTUs) can be used to translate the out-coming wavelength of the 10-Gbit/s and 2.5-Gbit/s interface to wavelengths specified for DWDM systems.

Distances of up to 1000 km can be bridged by using the *WaveStar*® OLS 1.6T together with 1675 LambdaUnite MSS, and for the *Metropolis*® EON distances up to 640 km can be bridged together with 1675 LambdaUnite MSS.

Combined interworking with DWDM and PWDM

1675 LambdaUnite MSS provides flexible WDM solutions for different data transport spans. Inserting for example 2.5-Gbit/s colored laser interfaces for pWDM interworking into a single 1675 LambdaUnite MSS provides cost-efficient long distance and intermediate distance WDM applications, as depicted in the following figure.



4 Product description

Overview

Purpose

This chapter describes the 1675 LambdaUnite MultiService Switch (MSS) in terms of basic architecture, physical configuration and circuit packs.

Chapter structure

After a concise system overview, the transmission architecture is presented. A closer look is taken to the switch function.

The shelf configuration of the 1675 LambdaUnite MSS shelf is described, followed by a short description of the circuit packs contained.

Furthermore, this chapter deals with synchronization aspects within the network element and outlines the control architecture and the power distribution concept.

Contents

Concise system description	4-2
Transmission architecture	4-4
Switch function	4-5
Shelf configurations	4-6
Circuit packs	4-14
Synchronization	4-32
Control	4-55
Power	4-57
Cooling	4-58

Concise system description

The 1675 LambdaUnite MSS system architecture is based on a full non-blocking switch matrix with STS-1/VC-3 granularity. In the present release main switching units with the following capacities are available: 160-Gbit/s, 320-Gbit/s, and 640-Gbit/s. The granularity range can be extended down to VT1.5/VC-12 with a specific lower-order switching matrix.

1675 LambdaUnite MSS provides 32 universal slots, which can be flexible configured in 320-Gbit/s configurations with 10-Gbit/s (synchronous and Ethernet WANPHY), 2.5-Gbit/s, 622-Mbit/s, 155-Mbit/s and 1-Gbit/s Ethernet optical interface circuit packs , as well as 155-Mbit/s STM-1 and 45 Mbit/s DS3 electrical interface circuit packs. In 160-Gbit/s configurations the lower row is not operative, but the upper row can be flexible configured like mentioned above.

The mix and the number of 10-Gbit/s, 2.5-Gbit/s rings and linear links is only limited by the maximum number of operative slots. This makes 1675 LambdaUnite MSS a highly flexible system and allows for a vast variety of different configurations.

For further information about configuration and location rules please refer to “Port location rules ” (p. 6-11).

Applications

The system can be used as single or multiple Add/Drop Multiplexer (ADM), as single or multiple Terminal Multiplexer (TM) and as an Optical Switch (XC), using only one sub-rack. The system provides built-in cross-connection facilities and flexible interface circuit packs. Local and remote management and control facilities are provided via the TL1 interface and the Embedded Communication Channels (ECC).

With the Optical Network Navigation System (ONNS) 1675 LambdaUnite MSS provides automatic connection set-up and removal, automatic restoration and automatic topology discovery in meshed topologies. Due to the flexible architecture 1675 LambdaUnite MSS allows to integrate ONNS domains into existing classical networks. For further information please refer to “Optical Network Navigation System (ONNS)” (p. 2-66).

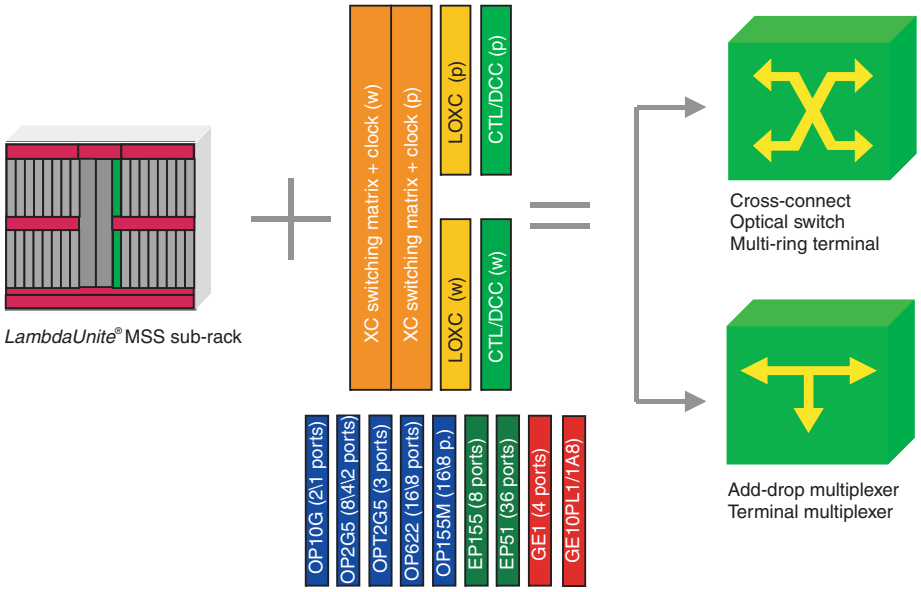
Halogen free cables

1675 LambdaUnite MSS systems can be ordered with halogen-free internal and external cabling.

Basic architecture

The basic 1675 LambdaUnite MSS architecture as outlined here covers the network element as a whole. The required number of the different plug-in units will be discussed later in this chapter.

The following figure gives an outline of the basic 1675 LambdaUnite MSS building blocks.



Transmission architecture

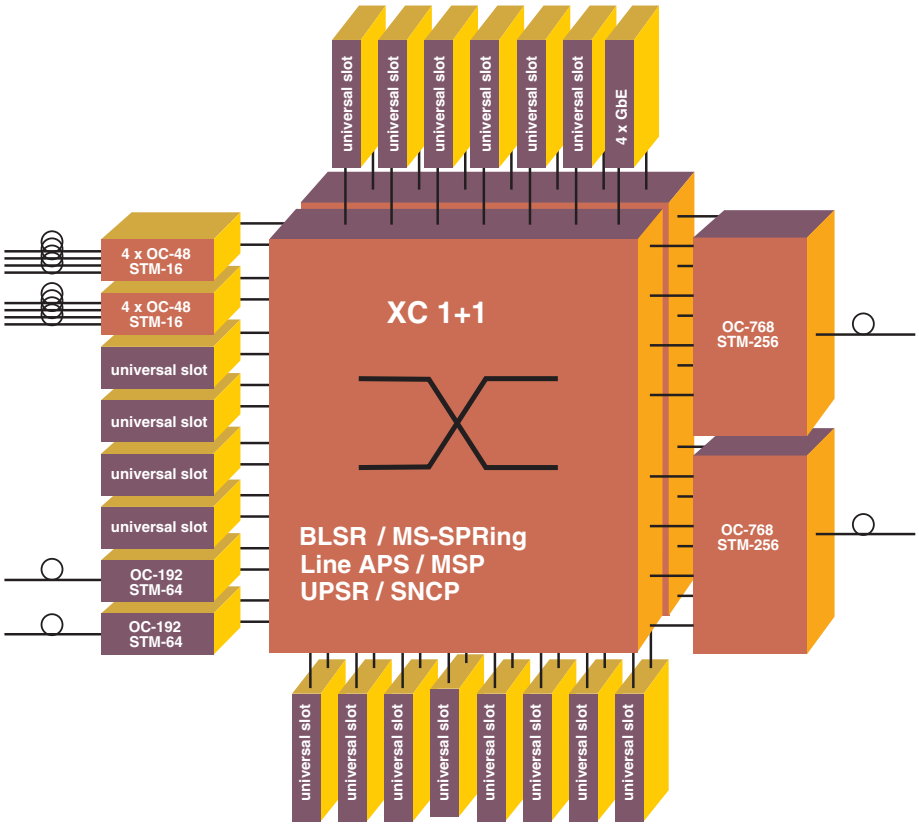
The 1675 LambdaUnite MSS transmission architecture is based on a centralized switching unit which is 1+1 protected. All traffic from/to the ports is fed to the central switch.

Transmission provisioning

Provisioning of the transmission circuit packs is controlled by the system controller circuit pack. Commands are received from OMS or *WaveStar*® CIT, which can both be connected locally to one of three LAN ports, or remotely via DCC channels.

Block diagram

The following figure shows a block diagram of the transmission architecture of the 1675 LambdaUnite MSS shelf, operating the 320-Gbit/s switching units.



Switch function

All traffic from/to port units is fed from/to the main switching unit (XC160, XC320 or XC640).

Switching capabilities

The total fully-non-blocking switching capacity is 160 Gbit/s (3072 STS-1 / 1024 VC-4), respectively 320 Gbit/s (6144 STS-1 / 2048 VC-4), or 640 Gbit/s (12288 STS-1 / 4096 VC-4). Additionally to SPE/VC switch capabilities, also overhead information from SONET/SDH I/O ports may be transparently switched. The switch itself is based on a bit sliced architecture providing this very high capacity on a single pack. Slicing / deslicing functions are part of the switch unit.

Traffic protection

Traffic protection switching (linear APS / MSP, BLSR/MS-SPRing, UPSR/SNCP) is performed centrally on the switch unit. All necessary switch information is transported via internal 2.5-Gbit/s transmission lines, the so called TXI (Transmission Exchange Interface) channels on the backplane directly towards the switch; the switch execution is done in hardware. Therefore, no interaction with the system controller is needed to perform traffic protection switching which increases speed and reliability.

1+1 Protection

To contribute to the overall system reliability and availability, the switching units are 1+1 equipment protected (c.f. “Block diagram” (p. 4-4) on the previous pages).

Shelf configurations

This section provides information about the different elements of 1675 LambdaUnite MSS and its configurations.

Shelf overview

The 1675 LambdaUnite MSS shelf provides the facilities to house the circuit packs. It consists of the mechanics, a backplane, a user panel and interface paddle boards (see "Interface paddle boards" (p. 4-12)) for the connections to the customer's infrastructure.

The 1675 LambdaUnite MSS shelf is designed for application in 600 mm (23.6 in) deep ETSI rack frames and in *Telcordia*™/NEBS compliant racks.

Optical interfaces

All optical ports bear LC connectors and are located on the front side of the subrack. If 1675 LambdaUnite MSS is mounted in a rack with doors you must use fiber connectors with angled boots.

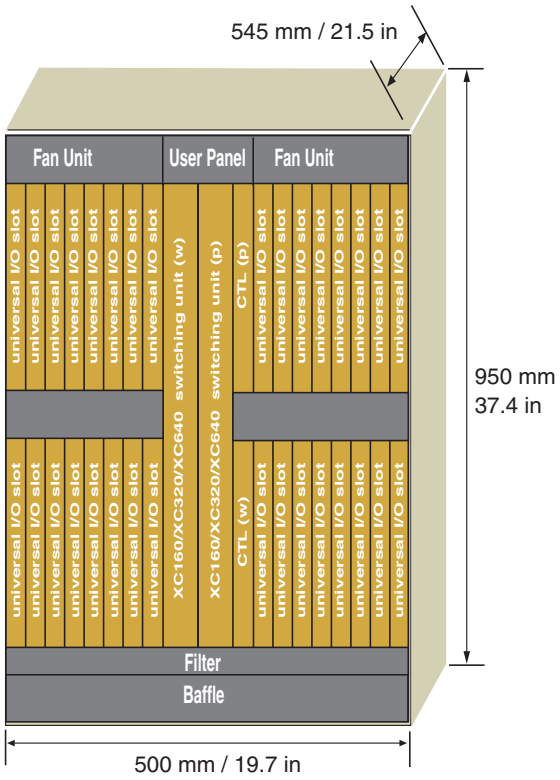
Electrical interfaces

All electrical transmission ports are located on electrical connection interfaces (ECIs) that can be inserted on the rear side of the subrack. The electrical transmission unit however is to be inserted on the front side of the subrack in the upper unit row. The ECIs are inserted on the rear side corresponding to the respective circuit pack positions. For further information about the ECIs please refer to "Interface paddle boards" (p. 4-12).

Important! For electrical transmission interfaces the use of rack extensions is recommended.

Shelf layout

The following figure depicts the 1675 LambdaUnite MSS shelf slots.



Circuit pack slots

The following table identifies the circuit pack slots of the 1675 LambdaUnite MSS shelf. For additional information about the transmission units please refer to Chapter 10, "Technical specifications".

Slot designation	Slot equipage
Universal slots[1] (32)	Universal slots can be used for the following circuit packs[2]: • Electrical DS3 (45 Mbit/s, plesiochronous) or EC-1 (51 Mbit/s, synchronous) port units (EP51…)[3] For the EP51 port units, there are dedicated slots for the electrical connection interfaces (ECI) on the rear side of the shelf. • Electrical 155-Mbit/s (STM-1E) port units (EP155…)[3] For the EP155 port units, there are dedicated slots for the electrical connection interfaces (ECI) on the rear side of the shelf. • Optical 155-Mbit/s port units (OP155…) • 622-Mbit/s port units (OP622…) • 2.5-Gbit/s port units (OP2G5…) • 10-Gbit/s port units (OP10…) • 1-Gigabit Ethernet interface (GE1) • High density private line Gigabit Ethernet unit (GE10PL1/1A8) • 10-Gigabit Ethernet WANPHY interface (realized on an OP10 port unit) • Lower-order cross-connection units (LOXC/1, LOXC40G2S, LOXC40G3S) • Portless TransMUX unit (TMUX2G5/1) *Lower-order cross-connection units (LOXC)*: The slots 4, 17, 18, 19, 37, and 39 can be used for lower-order cross-connection units, depending on the maximum switching capacity of the system and the type of LOXC . For more detailed information, please refer to: • “160-Gbit/s configuration” (p. 6-11), • “320-Gbit/s configuration” (p. 6-12), and • “640-Gbit/s configuration” (p. 6-14), respectively.
Controller slot (working)	Working CTL unit. System controller including non-volatile memory and DCC controller for the whole network element.
Controller slot (protection)	Protection CTL unit. Redundant system controller including non-volatile memory and DCC controller for the whole network element. After initial power up of the system one of the two CTLs is in standby mode.
XCW (switching unit working)	The switching circuit pack in this slot is paired with XCP switching unit (protection) in a 1+1 non-revertive protection mode configuration. Furthermore, this circuit pack contains the timing generator function for the NE.
XCP (switching unit protection)	The switching circuit pack in this slot is paired with XCW switching unit (working) in a 1+1 non-revertive protection mode configuration. Furthermore, this circuit pack contains the timing generator function for the NE. After initial power up of the system one of the two XCs is in standby mode.

Notes:

1. Slots for port units are called “universal slots”. The transmission capacity of a port unit in a universal slot can be up to 20 Gbit/s per slot.
2. Each circuit pack occupies one universal slot, if not stated otherwise.
3. The electrical port units (EP…) can only be used in the universal slots in the *upper row* of the shelf (slots 1…8, and 12…19), and in a DUR shelf of type DUR/2.
4. The terms “worker” and “protection” are used to describe the static role within a protection, whereas the terms “active” and “standby” are used to describe the current (dynamic) role. Please also refer to the *1675 LambdaUnite MSS User Provisioning Guide*.

Minimum configuration of plug-in units

The minimum recommended complement of plug-in units required for an operational 1675 LambdaUnite MSS shelf is

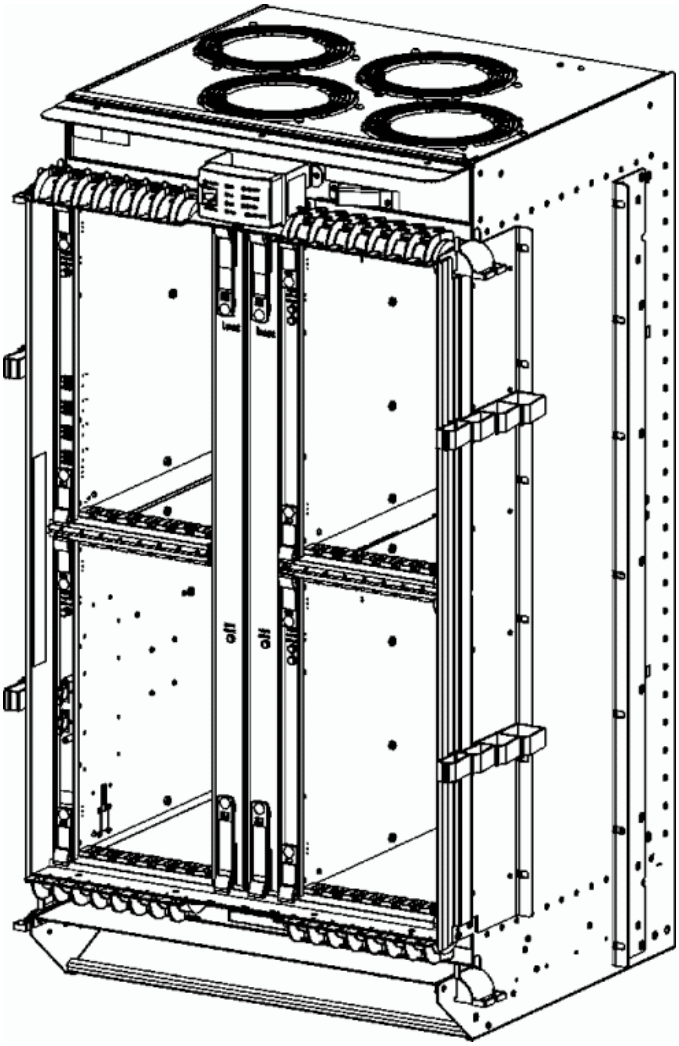
- two switching units, one working and one protection
- one controller unit (traditional applications) or two controller units, one working and one protection (ONNS functionality)
- the required transmission units in the universal slots.

A shelf equipped with these circuit packs would be fully functional. If ONNS applications are not used, the CTL redundancy is not required.

Other essential parts of the system are the User Panel, the Power Interfaces (PI), the fan unit, and the Controller Interface (CI-CTL); these parts are subsumed in the core assembly kits, delivered already mounted in the subrack.

Front view of 1675 LambdaUnite MSS sub-rack

In the following figure a front view of a 1675 LambdaUnite MSS sub-rack is shown, with a partial equipage.



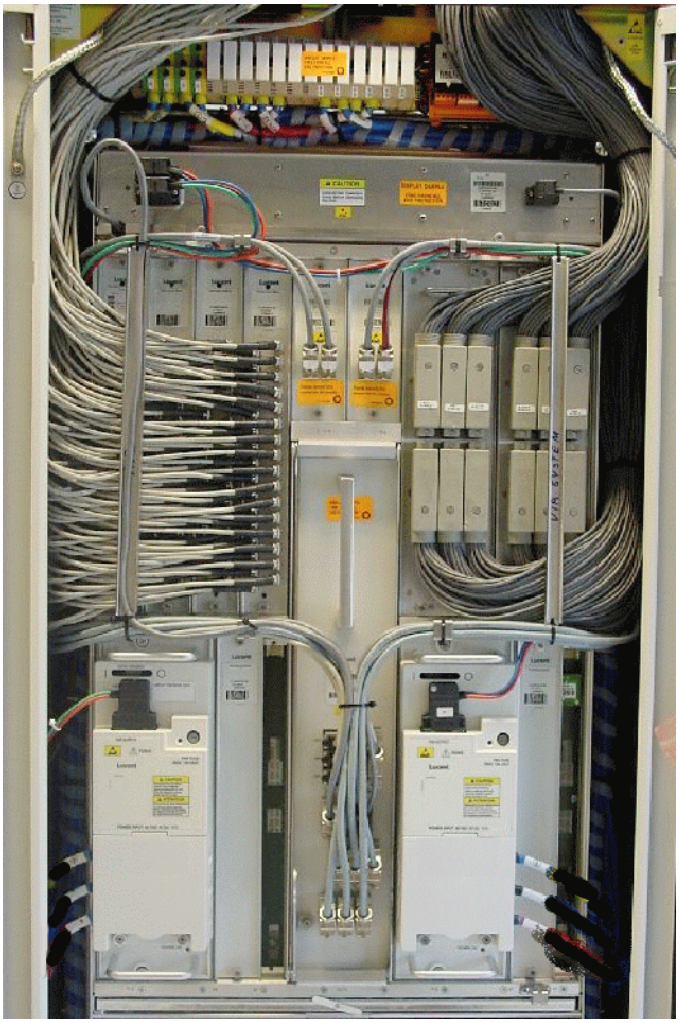
The sub-rack is equipped with two controller units, one 2.5-Gbit/s interface, one 10-Gbit/s interface, and two blank faceplates in the XCW/XCP slots; for operation these blank faceplates must be replaced by XC packs, and the empty slots must be covered by blank faceplates. We can also distinguish the four fans of the fan unit on top of the sub-rack, the user panel in front of the fan unit, and the fiber trays next to the subrack, at the sides and at the bottom of it.

Four-slot front cover plates

As an option, a four-slot front cover plate is available that covers four slots. Two four-slot front cover plates can be used to cover a complete subrack quadrant in order to prevent capacity growth on a power-limited 160G installation. Unlocking and removing the four-slot front cover plate is only possible by using a tool.

Rear view of 1675 LambdaUnite MSS sub-rack

The following figure shows a rear view of a 1675 LambdaUnite MSS sub-rack with the different interface paddle boards as listed below.



Interface paddle boards

A variety of interface paddle boards provide connection between customer cabling and the backplane. All the universal slots for transmission units are located on the front of the subrack, whereas all the interface paddle boards are inserted at the rear side of the subrack.

The following interface paddle boards can be inserted into the 1675 LambdaUnite MSS shelf:

- Two Timing Interfaces (TI) in the upper center part, providing external timing inputs/outputs
- One Controller Interface (CI-CTL) in the center, provides the external LAN interface, station alarm interface, MDI/MDO interface, user byte interface and interface for cables to User Panel and Fan Unit.
- Two Power Interfaces (PI) in the lower part of the back plane.
- Up to eight ECIs for EP155 operation (upper right part of the figure), two-slots wide, for the electrical 155-Mbit/s transmission units. The ECIs are inserted into the upper row of the back plane, corresponding to the circuit pack positions.

  There are two types of ECI for the electrical EP155 circuit packs:

  – ECI 155ME8 with 32 coax connectors (16 ports), providing connection for two unprotected 155-Mbit/s electrical transmission units , and

  – ECI 155MP8 with 16 coax connectors (8 ports), providing connection for one 155-Mbit/s electrical 1+1 protection pair: one worker unit and one protection unit.
- Up to four ECIs for EP51 operation (upper left part of the figure), four-slots wide, for the electrical 51- and 45-Mbit/s transmission units. The ECIs are inserted into the upper row of the back plane, corresponding to the circuit pack positions.

  This ECI bears 72 ports, providing connection

  – for up to two unprotected 45- /51-Mbit/s electrical transmission units, or

  – for up to two 45- /51-Mbit/s electrical 1+1 protection pairs, one worker unit and one protection unit each.

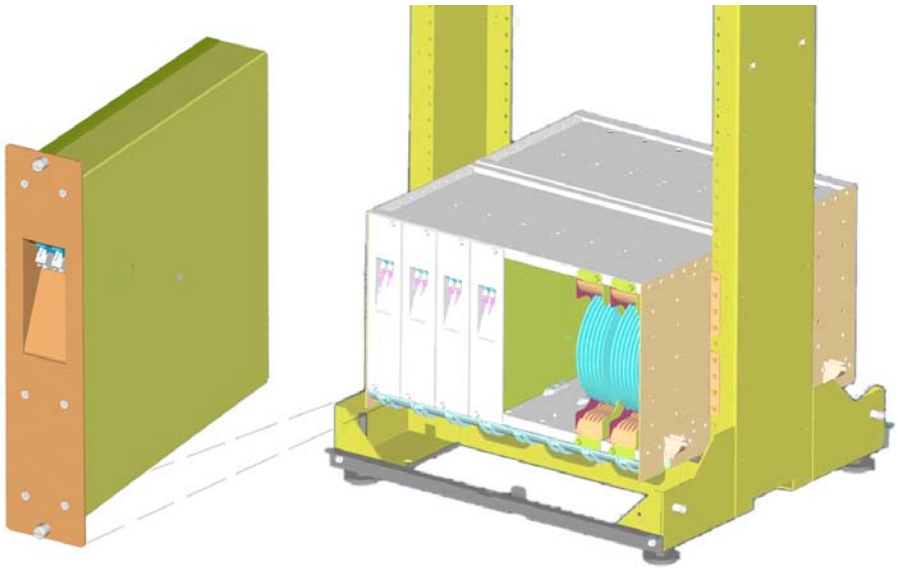
  Please note that for the transport of electrical signals the use of rack extensions is recommended, see also “Rack extensions” (p. 6-23).

  For more information about the configuration please refer to “Port location rules ” (p. 6-11).

1675 LambdaUnite MSS storage subrack

For the storage of Dispersion Compensation Modules (DCM) and overlength fiber 1675 LambdaUnite MSS offers a special storage subrack. Up to eight DCM-s or overlength reels fit into one storage subrack, and the storage subracks can be mounted in the 1675 LambdaUnite MSS rack, also in a back-to-back configuration.

The following figure shows a DCM and two 1675 LambdaUnite MSS storage subracks, mounted back-to-back in a bottom position of the rack. In this drawing the storage subrack in the front is partially equipped with four DCM-s on the left hand side, and with two fiber overlength storage boxes on the right hand side; two slots in between are left empty.



CWDM carrier box

The CWDM unit is a standalone rack-mountable unit that contains one CWDM multiplexing and one demultiplexing component with the necessary fiber connection, and is completely passive.

The standalone rack-mountable CWDM unit is designed to meet a maximum depth of 280 mm, a maximum height that is compliant to 1 unite product and fits into following rack types:

- NEBS-2000 designed for the 600 mm frameworks;
- 23 inch NEBS rack; used by large telecom customers in North America
- 600 mm wide ETSI rack; used in Europe and other SDH markets
- 19 inch NEBS rack
- 19 inch EIA rack; used by datacom market

Circuit packs

Supported circuit packs

The circuit packs supported by 1675 LambdaUnite MSS can be divided in two groups:

- *prime plug-in units*, inserted directly into the slots of the sub-rack
- *optical interface modules*, hosted by special prime plug-in units, so called *parent boards*.

For a complete and up-to-date list of all available circuit packs with the respective comcodes, please refer to the Engineering Drawings ED8C948-10 and ED8C948-20. For ordering information, see "Related information" (p. xvii).

Types of prime plug-in units supported by 1675 LambdaUnite MSS:

Short Name	Function	Ports per pack	Max. units per shelf	Max. ports per shelf
OP10	Optical I/O pack OC-192 / STM-64 10-Gbit/s Ethernet WANPHY	1	32	32
OP10/XTTC	Circuit pack with Xtreme compatible optics with tunable laser.	1	32	32
OP10/XTTL	Circuit pack with Xtreme compatible optics with tunable laser.	1	32	32
OP10D/PAR2	Optical I/O parent board for OC-192 / STM-64 / 10-Gbit/s Ethernet WANPHY optical interface modules	2	32	64
OP10/PAR1XFP (Parent board for 1 XFP)	Optical I/O parent board for OC-192 / STM-64 / 10-Gbit/s Ethernet WANPHY hot pluggable XFP interface modules. One pack fits into a single I/O slot (up to 32 ports fit into one single subrack).	1	32	32
OP10D/PAR2XFP (Parent board for up to 2 XFP)	Optical I/O parent board for OC-192 / STM-64 / 10-Gbit/s Ethernet WANPHY hot pluggable XFP interface modules. One pack fits into a single I/O slot (up to 64 ports fit into one single subrack).	2	32	64
OP2G5	Optical I/O pack OC-48 / STM-16	4	32	128
OP2G5/PARENT	Optical I/O parent board with OC-48 / STM-16 PWDM modules When using PWDM modules, always two PWDM modules must be mounted per parent board.	2	32	64
OP2G5D/PAR8	Optical I/O parent board for OC-48 / STM-16 optical SFP interface modules	8	32	256
OP2G5/PAR4	Optical I/O parent board for OC-48 / STM-16 optical SFP interface modules	4	32	128
OPT2G5/PAR3	Transparent optical I/O parent board for OC-48 / STM-16 optical SFP interface modules	3	32	96

Short Name	Function	Ports per pack	Max. units per shelf	Max. ports per shelf
OPLB/PAR8	Optical I/O parent board for OC-12 / STM-4 or OC-3 / STM-1 optical SFP interface modules	8	32	256
OP622	Optical I/O pack OC-12 / STM-4	16	32	512
OP155M	Optical I/O pack OC-3 / STM-1	16	32	512
EP155	Electrical I/O pack STM-1	8	16	128
EP51	Electrical I/O pack DS3/EC1	36	16	288
GE1	1-Gigabit Ethernet optical I/O pack	4	32	128
GE10PL1/1A8	Optical interface pack for 10GbE (1 XFP cage) or 1GbE (8 SFP cages)	9 (8 or 1)	32	288 (256 or 32)
TMUX2G5/1	Portless TransMUX card (only in combination with CTL/3T or CTL/3S)	n/a	24	n/a
XC160	XC with 160 Gbit/s capacity (switching matrix incl. timing generator, ONNS capable, 1+1 protection recommended, upgradable)	n/a	2	n/a
XC320/B	XC with 320 Gbit/s capacity (switching matrix incl. timing generator, ONNS capable, 1+1 protection recommended, upgradable)	n/a	2	n/a
XC640	XC with 640 Gbit/s capacity (switching matrix incl. timing generator, ONNS capable, 1+1 protection recommended)	n/a	2	n/a
LOXC/1	Lower-order cross-connection unit with 15-Gbit/s switching capacity (switching matrix for VC-3 lower-order, for VC-12, and for VT1.5 traffic; 1+1 protection recommended)	n/a	2	n/a
LOXC40G2S/1	Lower-order cross-connection unit with 40-Gbit/s switching capacity (switching matrix for VC-3 lower-order, for VC-12, and for VT1.5 traffic; 1+1 protection recommended), Only in combination with XC640 and CTL/3T or CTL/3S.	n/a	2	n/a
LOXC40G3S/1	Lower-order cross-connection unit with 40-Gbit/s switching capacity (switching matrix for VC-3 lower-order, for VC-12, and for VT1.5 traffic; 1+1 protection recommended), Only in combination with XC320 or XC640 and CTL/3T or CTL/3S.	n/a	2	n/a
CTL/-	System controller and DCC controller unit (no ONNS capability, not suitable for LOXC and TransMUX)	n/a	2	n/a
CTL/2	System controller and DCC controller unit (no ONNS capability in Release 10.05.54, not suitable for LOXC and TransMUX)	n/a	2	n/a
CTL/3T[1]	System controller and DCC controller unit with enhanced performance (no ONNS capability) Suitable for LOXC and TransMUX	n/a	2	n/a

Short Name	Function	Ports per pack	Max. units per shelf	Max. ports per shelf
CTL/3S [1]	System controller and DCC controller unit with enhanced performance, ONNS capable (1+1 protection for ONNS recommended) Suitable for ONNS applications, LOXC and TransMUX	n/a	2	n/a
CTL/4T	System controller and DCC controller unit (no ONNS capability, not suitable for LOXC and TransMUX)	n/a	2	n/a
CTL/4S	System controller and DCC controller unit (no ONNS capability in Release 10.05.54, not suitable for LOXC and TransMUX)	n/a	2	n/a

Notes:

1. Some CTL variants are delivered without *CompactFlash®* cards. Note that the system must not be turned on or operated before the appropriate *CompactFlash®* cards are installed on the CTLs. If the *CompactFlash®* cards are missing, the system will not work properly.

Types of optical interface modules supported by 1675 LambdaUnite MSS:

Short Name	Function	Parent board	Max. ports per board	Max. ports per shelf
OM10	Optical interface module OC-192 / STM-64	OP10D/PAR2	2	64
OMX10	OC-192 / STM-64 / 10-Gbit/s Ethernet LANPHY/WANPHY optical XFP interface module (10/40/80 km)	GE10PL1/1A8	1	32
		OP10/PAR1XFP	1	32
		OP10D/PAR2XFP	2	64
OM2G5/PWDM	Optical PWDM interface module OC-48 / STM-16 Always two modules must be mounted per parent board.	OP2G5/PWDM	2	64
OM2G5/21...59/WDM1	Optical PWDM interface module OC-48 / STM-16/OTM-n.1	OP2G5D/PAR8	8	256
		OP2G5D/PAR4	4	128
		OPT2G5/PAR3	3	96
OM10G/21...59/WDM1	Optical PWDM interface module OC-192 / STM-64 / OTM-n.2/10GbE	OP10/PAR1XFP	1	32
		OP10/PAR2XFP	2	64
OM2G5/CWDM	Optical SFP interface module (CWDM) OC-48 / STM-16 (40/80 km)	OP2G5D/PAR8	8	256
		OP2G5/PAR4	4	128
		OPT2G5/PAR3	3	96
OM2G5	Optical SFP interface module OC-48 / STM-16	OP2G5D/PAR8	8	256
		OP2G5/PAR4	4	128
		OPT2G5/PAR3	3	96

Short Name	Function	Parent board	Max. ports per board	Max. ports per shelf
OM622	Optical SFP interface module OC-12 / STM-4	OPLB/PAR8	8	256
OM155	Optical SFP interface module OC-3 / STM-1			
OMGE1	1-Gbit/s Ethernet LANPHY SFP interface module (SX/LX/ZX)	GE10PL1/1A8	8	256

Optical transmission units OP10

For interfacing to optical 10-Gbit/s signals, 1675 LambdaUnite MSS can be equipped with the OP10 circuit pack which is available in the following variants:

- 10-Gbit/s long reach interface (80 km), 1550 nm, 10G 1550nm LR/LH (80km), (KFA6)
- 10-Gbit/s intermediate reach / short haul and WANPHY Ethernet interface (40 km), 1550 nm
- 10-Gbit/s long reach / long haul hot-pluggable optical interface module (80 km), 1550 nm, (SFP)
- 10-Gbit/s intermediate reach / short haul hot-pluggable optical interface module (40 km), 1550 nm, (SFP)
- 10-Gbit/s intra-office hot-pluggable optical interface module (600 m), 1310 nm
- Extended Form-factor Pluggable (XFP) interface with OC192/STM64 DWDM & PWDM compatible 1.5 micron optics (intermediate reach and short-haul applications), 16 colors
- 10-Gbit/s intra-office interface (600 m), 1310 nm
- 10-Gbit/s interface for direct OLS 1.6T interworking, 80 colors, with provisionable out-of-band FEC
- 10-Gbit/s interface for direct *LambdaXtreme*™ Transport interworking, 128 colors, tuneable; C&L band, with provisionable out-of-band FEC

The electrical-to-optical and optical-to-electrical conversion is provided by the optics module(s) of these circuit packs. Several optical modules are used dependent on the required optical interface specifications.

The optics modules interface to the receive byte processor and the transmit byte processor by 16 times 622 Mbit/s (or 666 Mbit/s in case of strong FEC) interfaces.

The receive byte processor and transmit byte processor interface to the pointer processor through 16 times 622-Mbit/s TXI interfaces. The pointer processor itself provides the interface to the backplane with 4 times 2.5-Gbit/s TXI interfaces. These 2.5-Gbit/s TXI are doubled at the pointer processor, connecting to the working or the protection switch circuit pack (XC160, XC320, or XC640) respectively.

The 155-MHz board clock which is fed to the byte processors and to the pointer processor is generated out of the 6.48-MHz reference clock provided via the backplane.

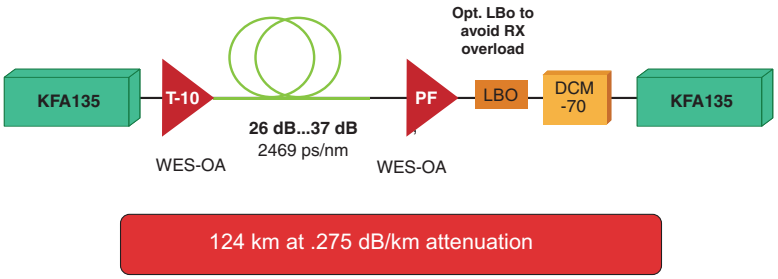
The circuit pack is equipped with an on-board function controller which interfaces with the system controller circuit pack (CTL).

The 40 km interface supports besides the SONET/SDH protocol also the 10-Gbit/s Ethernet WANPHY protocol. It is not fully compliant to IEEE 802.3ae but interworkable, accepting some limitations:

- No support for transparent loop setting
- Different K1/K2 default value
- No support for jitter test mode.

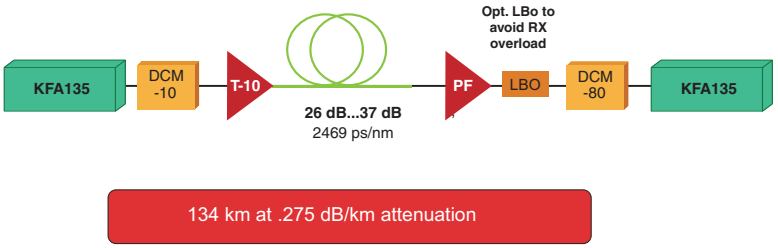
10G application for STM-64 Long Span on G.652 Fiber (124 km)

This application is reached by using the U64.2c2/3 line interface (KFA135) together with external co-located Booster- and Pre-Amplifier and an external collocated DCM.



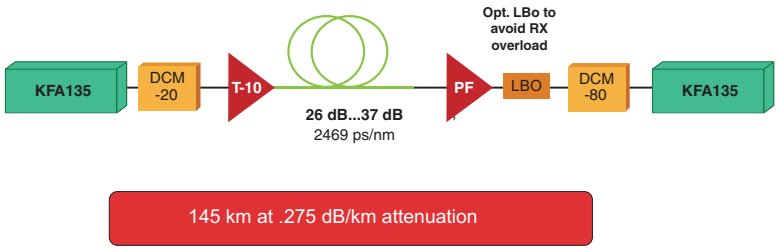
10G application for STM-64 Long Span on G.652 Fiber (134 km)

This application is reached by using the U64.2c2/3 line interface (KFA135) together with external co-located Booster- and Pre-Amplifier and an external collocated DCM.



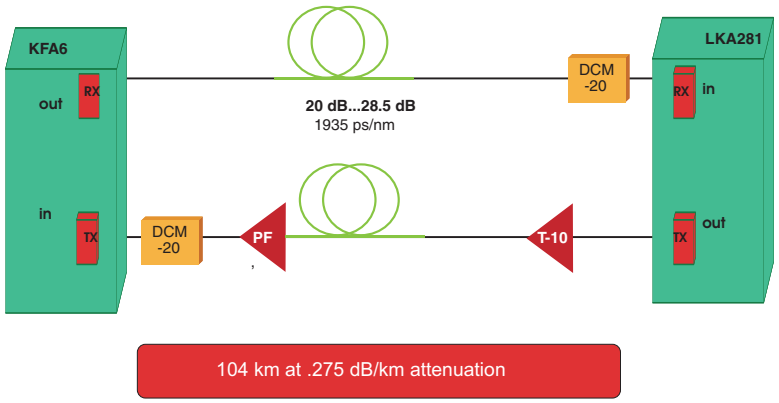
10G application for STM-64 Long Span on G.652 Fiber (145 km)

This application is reached by using the U64.2c2/3 line interface (KFA135) together with external co-located Booster- and Pre-Amplifier and an external collocated DCM.



Interworking between 1675 LambdaUnite MSS and *Metropolis*® ADM (Universal shelf) 10G interfaces

Interworking for very long haul between 1675 LambdaUnite MSS and *Metropolis*® ADM (Universal shelf) 10G interfaces can be realized using the following configuration,:



Optical transmission units OP2G5

For interfacing to optical 2.5-Gbit/s signals, 1675 LambdaUnite MSS can be equipped with OP2G5 circuit packs respectively the Small Form Factor Pluggable (SFP) parent board or the pWDM parent board, available in the current release in the following variants:

- 2.5-Gbit/s long reach optical Small Form Factor Pluggable (SFP) interface module (80 km), 1550 nm
- 2.5-Gbit/s long reach optical Small Form Factor Pluggable (SFP) interface module (40 km), 1310 nm
- 2.5-Gbit/s short reach / intra-office optical SFP interface module (2 km), 1310 nm
- 2.5-Gbit/s SFP interface, compatible for DWDM and pWDM long-haul applications, 16 colors, supporting WDM MUX/DEMUX
- 2.5-Gbit/s long reach interface module (40 km), 1,5 µm, pWDM compatible, 32 wavelengths; two are factory-mounted in the pWDM parent board (OP2G5/PARENT).

For the SFP parent board and the respective optical interface modules please observe the configuration rules described in "Port location rules " (p. 6-11).

The electrical-to-optical and optical-to-electrical conversion is provided by the four optical transceivers. Each transceiver interfaces to a MUX/DEMUX device.

The MUX/DEMUX devices interface to the byte and pointer processor device by 4 times 622-Mbit/s interfaces each.

The byte and pointer processor provides the interface to the backplane with four 2.5-Gbit/s TXI interfaces. The 2.5-Gbit/s TXIs are doubled at the byte and pointer processor, connecting to the working or the protection switch circuit pack (XC160, XC320, or XC640) respectively.

The 155-MHz board clock which is fed to the byte and pointer processor is generated out of the 6.48-MHz reference clock provided via the backplane.

The circuit pack is equipped with an on-board function controller which interfaces with the system controller circuit pack (CTL).

Transparent optical transmission units OPT2G5/PAR3

For transparently transporting optical 2.5-Gbit/s signals, 1675 LambdaUnite MSS can be equipped with OPT2G5/PAR3 circuit packs, each bearing three sockets for the following SFP modules:

- 2.5-Gbit/s long reach optical Small Form Factor Pluggable (SFP) interface module (80 km), 1550 nm
- 2.5-Gbit/s long reach optical Small Form Factor Pluggable (SFP) interface module (40 km), 1310 nm
- 2.5-Gbit/s short reach / intra-office optical SFP interface module (2 km), 1310 nm

For the SFP parent board and the respective optical interface modules please observe the configuration rules described in "Port location rules " (p. 6-11).

The optical specifications depend on the SFP used; please refer to the specifications of the single SFP.

The main features of the OPT2G5/PAR3 are

- 3 port OC-48 or STM-16
- optical SFP modules as used on other 2.5-Gbit/s circuit packs in 1675 LambdaUnite MSS
- hot pluggability for optical modules
- virtual concatenation, mapping into STS-3c-17v / VC-4-17v
- differential delay compensation for VCG members up to 32 ms (measured between the fastest and slowest VCG member)
- client signal SONET/SDH section/RS non-intrusive monitoring on ingress and egress

- consequent action configurable on egress: (generic AIS) OR (MS-AIS) OR (Laser OFF) upon diverse defects, such as LOF, path defects, VC group defects, mapping defects; ingress consequent action generic AIS
- ingress/egress fixed position/sequence of VC group per port

The electrical-to-optical and optical-to-electrical conversion is provided by the SFP module.

The MUX/DEMUX devices interface to the byte and pointer processor device by 5 times 622-Mbit/s interfaces each.

The byte and pointer processor provides the interface to the backplane by mapping the five 622-Mbit/s streams per port into the 2.5-Gbit/s TXI interfaces. The TXIs are doubled at the byte and pointer processor, connecting to the working or the protection switch circuit pack (XC160, XC320, or XC640) respectively.

The 155-MHz board clock which is fed to the byte and pointer processor is generated out of the 6.48-MHz reference clock provided via the backplane.

The circuit pack is equipped with an on-board function controller which interfaces with the system controller circuit pack (CTL).

Optical transmission unit OP622

For interfacing to sixteen optical 622-Mbit/s signals, 1675 LambdaUnite MSS can be equipped with the OP622 circuit pack which is available in the current release in the following variant:

- 622-Mbit/s long reach / long haul optical SFP interface module (80 km), 1530 nm
- 622-Mbit/s long reach / long haul optical SFP interface module (40 km), 1310 nm
- 622-Mbit/s intermediate reach / short haul optical SFP interface module (15 km), 1310 nm
- 622-Mbit/s intermediate reach interface (15 km), 1310 nm, 16 ports.

The electrical-to-optical and optical-to-electrical conversion is provided by the optical transceiver, which interfaces to a MUX/DEMUX device.

The MUX/DEMUX devices interface to the byte and pointer processor device by one 622-Mbit/s interface.

The byte and pointer processor provides the interface to the backplane with a 2.5-Gbit/s TXI interface. The 2.5-Gbit/s TXIs are doubled at the byte and pointer processor, connecting to the working or the protection switch circuit pack (XC160, XC320, or XC640) respectively.

The 155-MHz board clock which is fed to the byte and pointer processor is generated out of the 6.48-MHz reference clock provided via the backplane.

The circuit pack is equipped with an on-board function controller which interfaces with the system controller circuit pack (CTL).

Important! For this transmission unit only so called optical break-out cables (bundles of 12 single mode fibers) should be used.

Optical transmission unit OP155M

For interfacing to sixteen optical 155-Mbit/s signals, 1675 LambdaUnite MSS can be equipped with the OP155M circuit pack which is available in the current release in the following variant:

- 155-Mbit/s intermediate reach interface (15 km), 1310 nm, 16 ports.
- 155-Mbit/s long reach / long haul optical SFP interface module (80 km), 1530 nm
- 155-Mbit/s long reach / long haul optical SFP interface module (40 km), 1310 nm
- 155-Mbit/s intermediate reach / short haul optical SFP interface module (15 km), 1310 nm

The electrical-to-optical and optical-to-electrical conversion is provided by the optical transceiver, which interfaces to a MUX/DEMUX device.

The MUX/DEMUX devices interface to the byte and pointer processor device by a 622-Mbit/s interface.

The byte and pointer processor provides the interface to the backplane with a 2.5-Gbit/s TXI interface. The 2.5-Gbit/s TXIs are doubled at the byte and pointer processor, connecting to the working or the protection switch circuit pack (XC160, XC320, or XC640) respectively.

The 155-MHz board clock which is fed to the byte and pointer processor is generated out of the 6.48-MHz reference clock provided via the backplane.

The circuit pack is equipped with an on-board function controller which interfaces with the system controller circuit pack (CTL).

Important! For this transmission unit only so called optical break-out cables (bundles of 12 single mode fibers) should be used.

Electrical transmission unit EP155

For interfacing to eight electrical 155-Mbit/s signals, 1675 LambdaUnite MSS can be equipped with the EP155 circuit pack which is available in the current release in the following variant:

- 155-Mbit/s intra-office electrical interface for STM-1 signals, 8 ports.

The MUX/DEMUX devices interface to the byte and pointer processor device by a 622-Mbit/s interface.

The byte and pointer processor provides the interface to the backplane with a 2.5-Gbit/s TXI interface. The 2.5-Gbit/s TXIs are doubled at the byte and pointer processor, connecting to the working or the protection switch circuit pack (XC160, XC320, or XC640) respectively.

The 155-MHz board clock which is fed to the byte and pointer processor is generated out of the 6.48-MHz reference clock provided via the backplane.

The circuit pack is equipped with an on-board function controller which interfaces with the system controller circuit pack (CTL).

Important! For this transmission unit the use of rack extensions is recommended, see also "Rack extensions" (p. 6-23).

Electrical transmission unit EP51

For interfacing to 36 electrical DS3/EC1 signals, 1675 LambdaUnite MSS can be equipped with the EP51 circuit pack which is available in the current release in the following variant:

- 45- /51-Mbit/s intra-office electrical interface for DS3/EC1 signals, 36 ports.

The EP51 supports the following formats:

- EC1
- DS3 M23
- DS3 C-bit
- DS3 unframed or "clear channel."

The DS3/EC1 signal is transported transparently through the SONET/SDH network, that means no change of DS3/EC1 data takes place. (exception the P- bit used for parity calculation can be inserted newly).

To perform the electrical conversion and to detect LOS so-called LIU (Line Interface Unit) devices are used. Each of them supports 12 DS3/EC1 channels.

In case of DS3 it performs all relevant fault and performance functions, performs the mapping demapping to/from STS-1 and terminates the STS-1 path. In case of EC-1 it performs all relevant SONET functions (line/section termination / path monitoring, clock adaptation via pointer processing). Subsequently the 12 STS-1 channels are multiplexed into a TXI622 channel. These TXI622 channels are then adapted to the system internal TXI2G5 interfaces (only one out of 4 TXI2G5 is used), and broadcast respectively selected to and from the working and protection TXI2G5.

The timing function is built around the clock sync distribution device (CSD2). Via this device a 77.76-MHz clock and a 8-kHz synchronization signal is distributed to the various devices on the circuit pack.

Note that the card cannot be used as a timing source. (the device is able to monitor the S1 byte (timing marker) but there is no reference clock output available).

The circuit pack is equipped with an on-board function controller which interfaces with the system controller circuit pack (CTL).

Important! For this transmission unit the use of rack extensions is recommended, see also "Rack extensions" (p. 6-23).

Gigabit Ethernet transmission unit GE1

For interfacing to four optical 1-Gbit/s Ethernet signals, 1675 LambdaUnite MSS can be equipped with the GE1/SX4 or with the GE1/LX4 circuit pack. Each port provides a 1000Base-SX / 1000Base-LX optical Ethernet interface.

The Ethernet ports consist of an external optical LAN port that is connected to an internal synchronous WAN port via a crossbar device. An internal function controller is used for on-board control and supervision purposes.

Each LAN port consists of an optical module, a 1.25-Gbit Serialize/Deserialize (SerDes) device, and an Ethernet controller. The internal WAN port consists of an Ethernet controller and a Gigabit Ethernet Over SDH/SONET (GEOS) Flexible Programmable Gate Array (FPGA).

The internal interface to the backplane consists of two stages. The first stage is a backplane transceiver device which has an 8-bit parallel interface to the GEOS FPGA and a TXI622 interface to the second stage. The seconds stage combines the TXI622 interfaces to the TXI2G5 CML interface that is used on the backplane.

The internal function controller is built around an MPC860 processor. The asset uses 4 MB of Flash memory and 16 MB of SDRAM memory. A PQIO device is used to provide the interface to the system controller and to the ON (operations network).

The timing function of the Gigabit Ethernet board is built around the clock sync distribution device (CSD2). Via this device a 77.76-MHz clock and a 8-kHz synchronization signal is distributed to the various devices on the circuit pack.

DC power is applied to the Gigabit Ethernet board via two –48-V battery feeds. On-board DC/DC converters generate 3.3 V, 2.5 V and 1.8 V.

Gigabit Ethernet transmission unit GE10PL1/1A8

The customer interface consists of eight optical pluggable 1 Gigabit Ethernet ports (SFPs) plus one optical pluggable 10 Gigabit Ethernet Transceiver (XFP). Various types of transceivers with different reach are supported (250 m... 70 km). On bi-color LED (red and green) and one yellow LED will be used per port for status and activity signaling. It is only possible to use either up to eight 1 Gigabit ports or the 10 Gigabit port, a parallel use is not possible.

The GE10PL1/1A8 unit supports the following features:

- FP cages for 1GbE LANPHY, SFP's acc. P-2126.1 (for interfaces acc. I-2010.0, I-2020.0 and I-2025.0 (ZX))
- 1 XFP cage for 10G LANPHY, XFP's acc. P-2128.1 for interfaces acc. I-2005.1 LR, ...2 ER (and P-2128.2 (80km)
- GFP mapping - LCAS
- No layer 2 switching function
- Support of jumbo frames
- Link pass through capability

The following SFPs are supported for GbE:

- 1GbE 70km 1550nm
- 1GbE 0,5km 780nm
- 1GbE 5km 1310nm -

The following XFPs are supported

- 10GbE 10km 1310nm and 10G SDH/SONET w. proprietary reach (>> VSR)
- 10GbE 40km 1550nm and SR-64.2

Main switching unit

There are three types of the main switching unit (XC) available: the XC160, the XC320, and the XC640. They are connected with the interface units via the backplane bus (TXI). The main switching unit is a bit-sliced switching matrix for up to 3072 STS-1 or 1024 VC-4 (XC160), respectively 6144 STS-1 or 2048 VC-4 (XC320), respectively 12288 STS-1 or 4096 VC-4 (XC640) level signals. The bit-sliced data is generated in the data converter device, it will be desliced in the data converter after the XC. MS-SPRing/BLSR, 1+1 line APS / 1+1 MSP and SNCP/UPSR switching is supported on the switching unit.

The main switching unit receives the TXI2G5-signals unsliced (via the backplane) on the data converter devices. After 12-to-8 static preselection and a slicing function this data is forwarded to the switch matrix device. So, for each set of 12 incoming TXI2G5 links at the backplane side of the data converter devices, only 8 are active (static slot selection). The 8 active channels are 1-bit sliced and each bit slice is transported over a TXI2G5 link to the switching matrix device. In the switching matrix, the data is switched according to the defined scheme specified by using *WaveStar*® CIT or OMS. The 1-bit-sliced data which egresses the switching matrix devices is collected and desliced in the data converter devices before it leaves the main switching unit.

Timing generator function

The timing generator function in the 1675 LambdaUnite MSS network element is physically implemented on the cross-connection circuit pack (XC160, XC320, and XC640). The external physical timing interfaces (inputs and outputs) are located on the Timing Interface (TI) panel.

The timing generator is designed as Stratum 3 version meeting the requirements of ITU-T Rec. G.813 (SDH) and Bellcore TR-1244 (SONET).

The available timing modes are:

- Free running
- Hold-over (entered automatically if all configured references fail)
- Locked with reference to:
  - one of the external synchronization inputs
  - one of all of the OC-N / STM-N input signals.

1675 LambdaUnite MSS provides 1+1 equipment protection for the timing function as part of the XC.

For more information on the timing architecture, please refer to "Synchronization " (p. 4-32).

Lower-order cross-connection unit

The lower-order cross-connection unit LOXC/1 is designed to cross-connect lower-order traffic with a capacity of 15 Gbit/s in total (288 × 288 VC-3 (lower order), 6048 × 6048 VC-12 or 8064 × 8064 VT1.5). It is to be used in addition to the main switching units.

The lower-order cross-connection units of type LOXC40G2S/1 and LOXC40G3S/1 are designed to cross-connect lower-order traffic with a capacity of 40-Gbit/s in total (768 × 768 VC-3 (lower order), 16128 × 16128 VC-12 or 21504 × 21504 VT1.5). Both units can be used in addition to the main switching unit of type XC640 only.

The LO tributaries are created while a LO cross-connection is successfully created (implicit method) or by substructuring command (explicit method).

The following HO signals can be substructured:

- STS-1 (SONET) carrying VT1.5 signals,
- VC-4 (SDH) carrying LO VC-3 or VC-12 signals (or a mix of it).

The handling is user-friendly, employing a single-step approach: the user establishes only the port-to-port connection (client connection), while the internal connections between the XC and the LOXC (server connections) are provided accordingly, automatically by the NE.

This implies that the HO signal termination and LO tributary entities are only accessible after the creation of the low-order cross-connection. If those entities shall be provisioned in advance, the user may execute one step prior to the cross-connection setup - namely to substructure a HO signal without cross-connection setup.

The LOXC/1 provides a non-blocking lower-order switching matrix. 15 Gbit/s switching capacity can be reached by cross-connecting lower-order traffic out of contiguously filled higher-order tributaries (288 STS-1 / 96 VC-4).

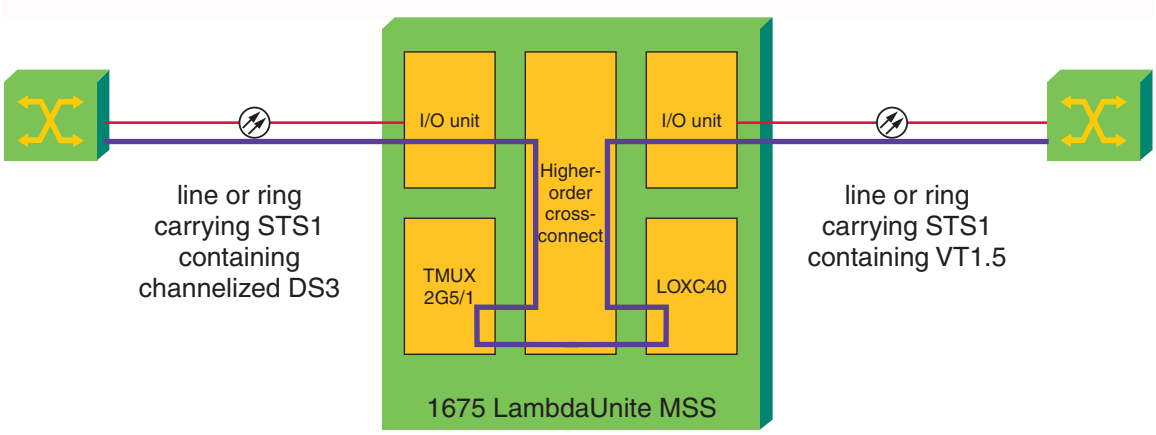
The LOXC40G2S/1 and the LOXC40G3S/1 provide a non-blocking lower-order switching matrix. 40 Gbit/s switching capacity can be reached by cross-connecting lower-order traffic out of contiguously filled higher-order tributaries.

1675 LambdaUnite MSS provides 1+1 equipment protection for the LOXC units, that works independently from the XC protection.

Portless TransMUX unit (TMUX2G5/1)

The Portless TransMUX unit (TMUX2G5/1) for 1675 LambdaUnite MSS is designed to support the remapping of channelized DS3 signals carrying DS1s into VT1.5 signals and vice versa.

The following figure shows the abstract network application together with the principle way of the signal through the system:



The following mappings are supported:

- STS-1 ↔ DS3 ↔ DS1 ↔ VT1.5 ↔ STS1 (mapping of channelized DS3 contained in an STS-1 into VT1.5s contained in STS-1)
- DS3 ↔ DS1 ↔ VT1.5 ↔ STS-1 (mapping of channelized DS3 from a DS3 port into VT1.5s contained in STS-1)
- DS3/STS1 ↔ DS1 ↔ VT1.5 ↔ STS1 ↔ VT1.5 ↔ DS1 ↔ DS3/STS1 (mapping of channelized DS3 from a DS3 port or an OC port into VT1.5s contained in STS1, cross-connecting them and mapping them back into channelized DS3 towards an OC port or DS3 port).

The TMUX2G5/1 unit supports a fixed mapping from DS1s of a channelized DS3 into VT1.5 (that is, a DS1 arriving at a certain STS1/DS3/DS1 position is always mapped to the same VT1.5/STS1 position), no DS1-switching takes place on this pack. DS1s are mapped asynchronously into VT1.5. Grooming of traffic is always done on VT1.5 level on LOXC40. “Transmuxed” traffic is always routed to LOXC40.

The TMUX2G5/1 unit is only usable in combination with LOXC40G3S/1 and LOXC40G2S/1. Operation in combination with LOXC/1 is not supported. This implies that the TMUX2G5/1 can only be used in NEs equipped with CTL/3T or CTL/3S. NEs equipped with any other type of CTL do not accept the provisioning of a TMUX2G5/1 unit.

TMUX2G5/1 units can be 1+1 equipment-protected, unprotected mode of operation is also supported. 1675 LambdaUnite MSS supports up to 12 active TMUX2G5/1 units, each of which can be equipment-protected. So in total up to 24 TMUX2G5/1 units may be present in an NE, 12 of them can be active.

Numbering scheme of DS1s and VT1.5s

Flat DS1 numbering	DS2 #	DS1 #	VTG #	VT #	Flat VT1.5 numbering
1	1	1	1	1	1
2	1	2	1	2	2
3	1	3	1	3	3
4	1	4	1	4	4
5	2	1	2	1	5
6	2	2	2	2	6
7	2	3	2	3	7
8	2	4	2	4	8
9	3	1	3	1	9
10	3	2	3	2	10
11	3	3	3	3	11

Flat DS1 numbering	DS2 #	DS1 #	VTG #	VT #	Flat VT1.5 numbering
12	3	4	3	4	12
13	4	1	4	1	13
14	4	2	4	2	14
15	4	3	4	3	15
16	4	4	4	4	16
17	5	1	5	1	17
18	5	2	5	2	18
19	5	3	5	3	19
20	5	4	5	4	20
21	6	1	6	1	21
22	6	2	6	2	22
23	6	3	6	3	23
24	6	4	6	4	24
25	7	1	7	1	25
26	7	2	7	2	26
27	7	3	7	3	27
28	7	4	7	4	28

Controller unit

The controller unit (CTL) provides the central control, supervision and security functions in the network element. For this purpose, it communicates with the function controllers on the individual interface circuit packs and the switch circuit packs.

Different types of the controller unit are available, offering different functionalities:

Controller type	Traditional SONET/SDH support	ONNS support	Support of • LOXC • TransMUX
CTL/-	+	–	–
CTL/2	+	–	–
CTL/3T	+	–	+
CTL/3S	+	+	+
CTL/4T	+	–	–
CTL/4S	+	–	–

Note: The ONNS controllers support the additional Signaling Communication Network (SCN) to support the intelligent control plane. This control plane can be established by dedicated SCN DCC channels for in-band signaling or by enabling the LAN port(s) for an out-of-band signaling channel.

Note: The CTL/- and CTL/4T Controller variants were initially delivered with a *CompactFlash*™ card with a disk size of 256 MB. If you want to use one of these Controller variants, then verify that a *CompactFlash*™ card with a disk size of 512 MB is used. If necessary, replace the 256 MB card by a 512 MB card. Alternatively, use a CTL/3T or CTL/3S Controller.

The CTL maintains system configuration data and system software are stored on an exchangeable *CompactFlash*® card of the following capacities:

- min. 512 MB for CTL/-, CTL/2, CTL/4T and CTL/4S
- 1 GB for CTL/3T and CTL/3S.

Some CTL variants are delivered without *CompactFlash*® cards. Note that the system must not be turned on or operated before the appropriate *CompactFlash*® cards are installed on the CTLs. If the *CompactFlash*® cards are missing, the system will not work properly.

CompactFlash® cards can be ordered separately and have their own comcode:

- *CompactFlash*® card 512 MB can be ordered using the comcode: 109449710
- *CompactFlash*® card 1 GB can be ordered using the comcode: 109763557 (from software release 10.5.5 on) or 109558544 (for earlier software releases), respectively

  Note: For performance reasons, it is recommended *not* to use *CompactFlash*™ cards with a storage capacity greater than 512 MB for Controllers of type CTL/- and CTL/2 because of the larger block size and the thus increased access times.

The following Controller variants *cannot* be ordered anymore, they do not fulfill the requirements for the current software version:

- CTL/- with a 256-MByte NVM storage capacity (*CompactFlash*® card).
- CTL/4T with a 256-MByte NVM storage capacity (*CompactFlash*® card), redesigned hardware, fully compatible with the CTL/-.

A further area of functionality is as an adjunct controller which handles the Data Communication Network (DCN), the LAN and other external control interfaces. Thus, it acts as a network layer router, de-coupling the routing of DCN through traffic from system control. The CTL also provides data link protocol termination for DCC type HDLC links and for 802.3 LAN type links.

The different controller units have different DCC capacities:

- CTL/4T and CTL/4S support a maximum of 64 DCC terminations simultaneously. CTL/3T and CTL/3S support a maximum of 180 DCC terminations simultaneously.

These DC channels can be

– regular section or line DCC links, respectively DCC_r or DCC_m links

– transparent DCC links that cross-connect the DCC information transparently through the system from one port to another (without D-byte termination).

The number of available DCC terminations can be calculated using the formula: **(number of regular DCCs + 2 × (number of transparent DCCs)) ≤ maximum number of DCCs**.

You can see that one transparent DCC equals two regular DCCs.

With the DCC slaving feature you can choose to switch the concerned DC channels together with line APS / MSP protection switching.

1675 LambdaUnite MSS provides 1+1 equipment protection for the controller unit.

Because of the extended importance of the controller unit in ONNS operations the 1+1 protection of the controller unit (redundancy) is highly recommended.

For further information please refer to "Optical Network Navigation System (ONNS)" (p. 2-66).

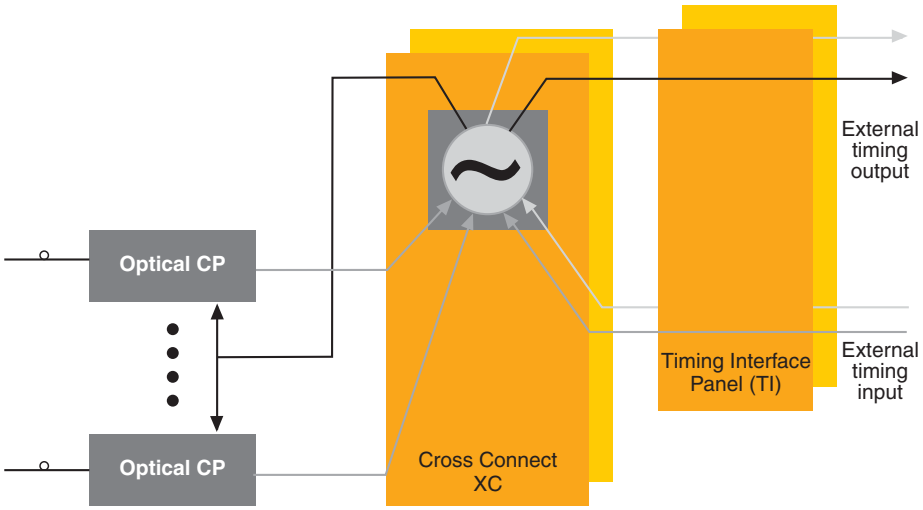
A further description of the control architecture can be found on "Control" (p. 4-55).

Synchronization

1675 LambdaUnite MSS synchronizes add, drop and through signals by using one timing source for all transmission. The system timing generator is normally locked to an external reference signal, such as a Primary Reference Source / Clock (PRS / PRC) or a line timing source. In the 1675 LambdaUnite MSS shelf, the timing function is physically located on the switching circuit pack (XC160, XC320, and XC640). If two XC circuit packs are present in the NE, 1+1 non-revertive protection of the timing sources is provided.

Timing function on the XC circuit packs

The timing functions on the XC circuit packs distribute timing signals throughout the shelf. These are used for clock, frame synchronization and multiframe synchronization.



NE synchronization mode

The synchronization characteristics of a 1675 LambdaUnite MSS system depend on the configurable NE synchronization mode (**System Synch Characteristics**). The NE synchronization mode basically impacts the NE-internal functional details, the expected external timing reference signals, the Synchronization Status Message (SSM) handling, and the output timing mode of operation.

1675 LambdaUnite MSS network elements support these NE synchronization modes:

SDH synchronization mode	The system timing complies with SDH synchronization requirements. The SDH synchronization mode should be chosen when a 1675 LambdaUnite MSS network element is to be integrated into an SDH network environment.

SONET synchronization mode	The system timing complies with SONET synchronization requirements. The SONET synchronization mode should be chosen when a 1675 LambdaUnite MSS network element is to be integrated into a SONET network environment.

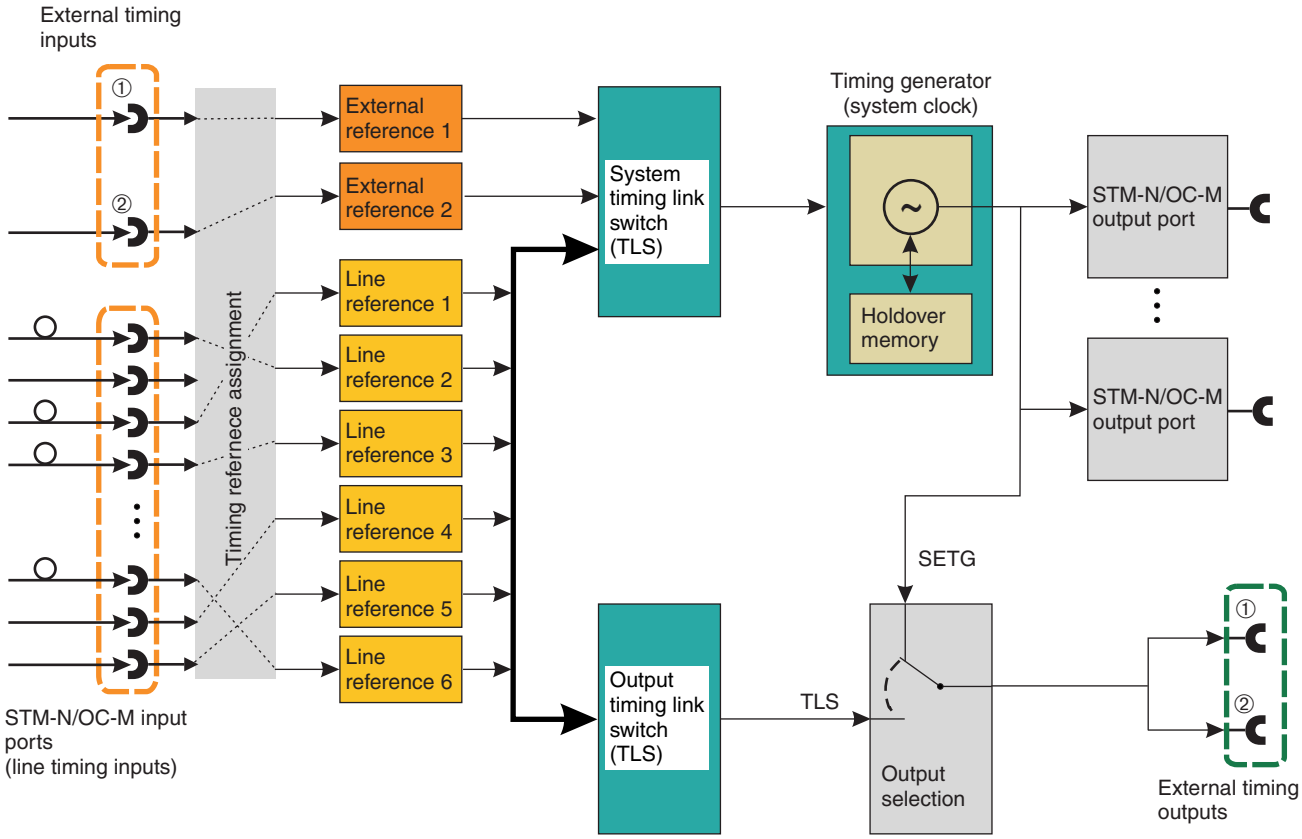
The NE-internal functional details that depend on the NE synchronization mode will subsequently be explained in more detail.

SDH- and SONET-specific functional implementation

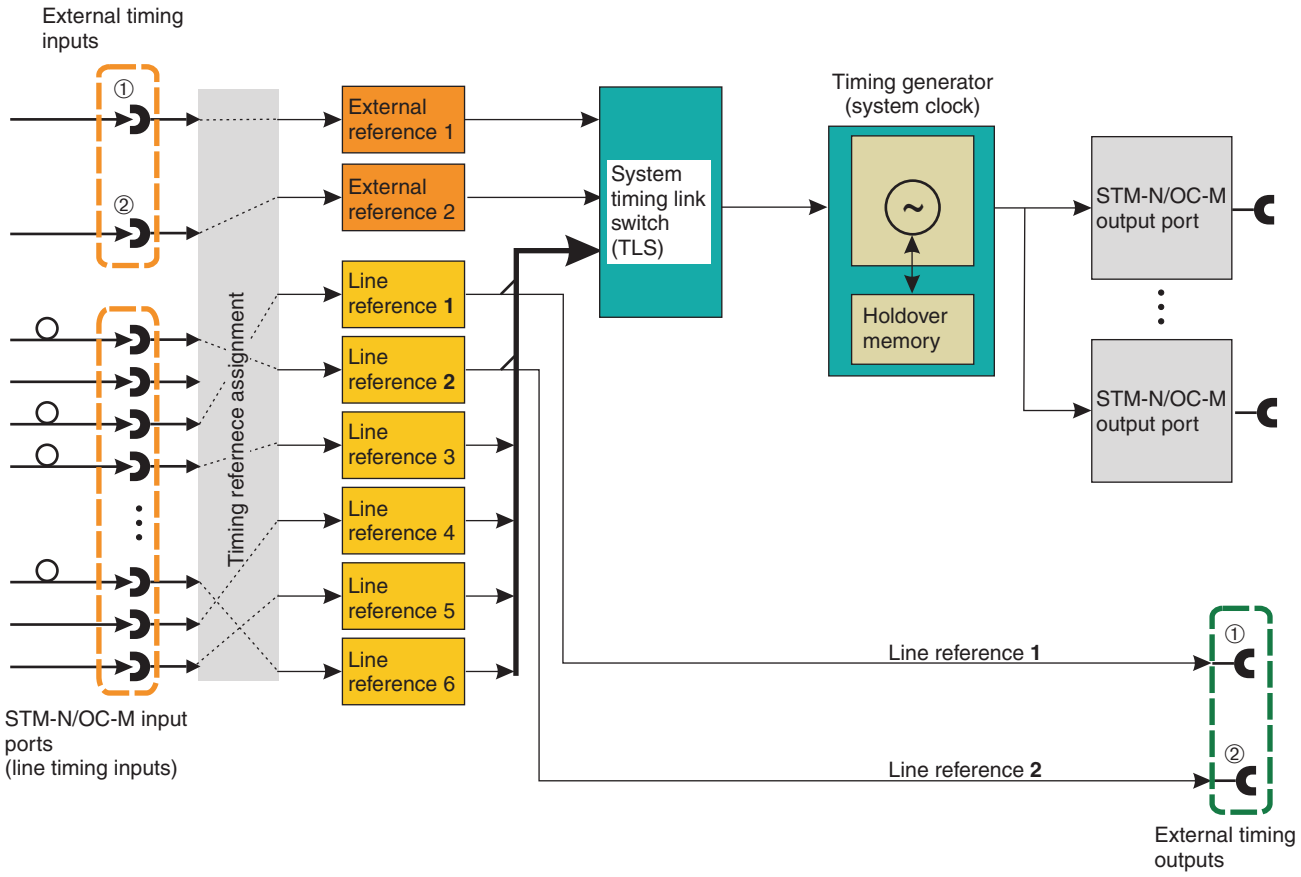
The implementations of the timing functionality for SDH and SONET differ in a few details. The SDH implementation has a separate output timing link switch for example, while the SONET implementation has two hardwired output lines.

The following two functional block diagrams serve to illustrate both the commonalities as well as the differences. However, not all aspects can be covered in these diagrams (for example the supported types of external timing input signals). These aspects will be dealed with in the subsequent sections.

The following functional block diagram gives an overview of the SDH-specific timing functionality of a 1675 LambdaUnite MSS network element.



The following functional block diagram gives an overview of the SONET-specific timing functionality of a 1675 LambdaUnite MSS network element.



Timing interfaces

Each 1675 LambdaUnite MSS network element can be equipped (from the rear side of the shelf) with up to two timing interfaces (TI A, TI B).

Each timing interface provides an external timing input and an external timing output.

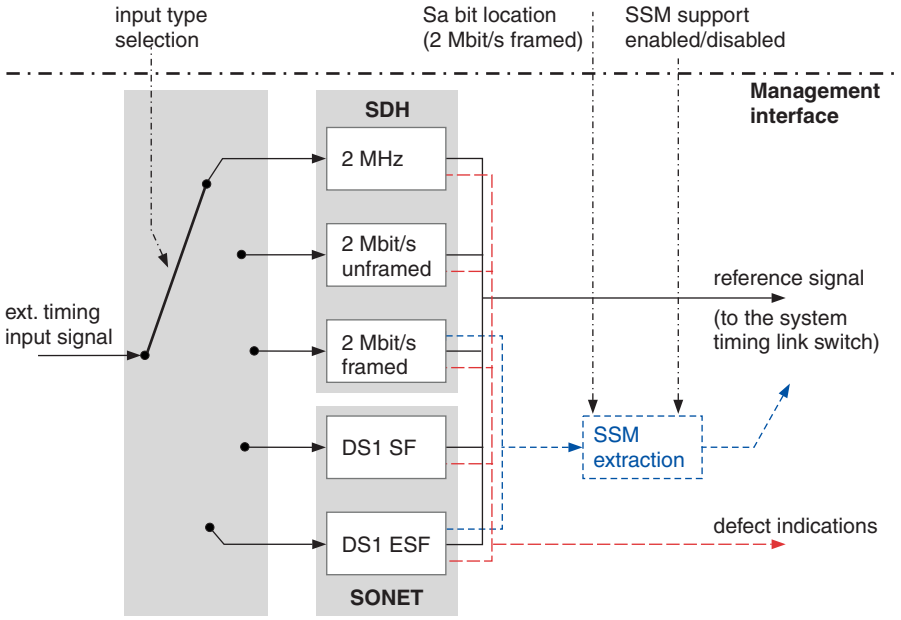
External timing inputs

An external timing input (station clock input) is an interface port which is specifically used to extract a synchronization reference to be used in the network element. A station clock input can be used for timing purposes only, it cannot be used for data transport.

The format of the station clock input signal is provisionable. These signal formats are supported, depending on the NE synchronization mode:

- SDH synchronization mode:
  - 2048 kHz (2 MHz) acc. to ITU-T Rec. G.703. This is the default setting in the SDH synchronization mode.
  - Unframed 2 Mbit/s acc. to ITU-T Rec. G.703.
  - Framed 2 Mbit/s acc. to ITU-T Rec. G.703 with a frame/multiframe structure acc. to G.704.
- SONET synchronization mode:
  - DS1 Superframe Format (DS1 SF) with B8ZS line code acc. to the Telcordia Technologies GR-499-CORE standard.
  - DS1 Extended Superframe Format (DS1 ESF) with B8ZS line code acc. to the Telcordia Technologies GR-499-CORE standard. This is the default setting in the SONET synchronization mode.

The following diagram gives a functional overview (including the provisioning options):



At the external timing inputs, the incoming signal is terminated, and the timing reference information is derived. Furthermore, the external timing input signal is monitored for defects, and the Synchronization Status Message (SSM) is extracted, where applicable (2 Mbit/s framed and DS1 ESF input signals with SSM support enabled). The SSM is used by the system timing link switch to select the best available reference signal.

Lockout of an external timing input

It is possible to temporarily lockout an external timing input. A locked out timing input is not available for timing reference selection.

External timing outputs

The external timing outputs (station clock outputs) can be used to synchronize other equipment in the same office.

The format of the station clock output signals is provisionable. These signal formats are supported, depending on the NE synchronization mode:

- SDH synchronization mode:
  - 2048 kHz (2 MHz) acc. to ITU-T Rec. G.703. This is the default setting in the SDH synchronization mode.
  - Unframed 2 Mbit/s acc. to ITU-T Rec. G.703.
  - Framed 2 Mbit/s acc. to ITU-T Rec. G.703 with a frame/multiframe structure acc. to G.704.
- SONET synchronization mode:
  - DS1 Superframe Format (DS1 SF) with B8ZS line code acc. to the Telcordia Technologies GR-499-CORE standard.
  - DS1 Extended Superframe Format (DS1 ESF) with B8ZS line code acc. to the Telcordia Technologies GR-499-CORE standard. This is the default setting in the SONET synchronization mode.

The clock reference for the station clock outputs also depends on the NE synchronization mode:

- SDH synchronization mode:

  The timing reference signal for the external timing outputs can be derived either from the NE-internal timing generator (SETG, “Synchronous Equipment Timing Generator”), or, via the output timing link switch (TLS), from the line timing references 1 to 6.

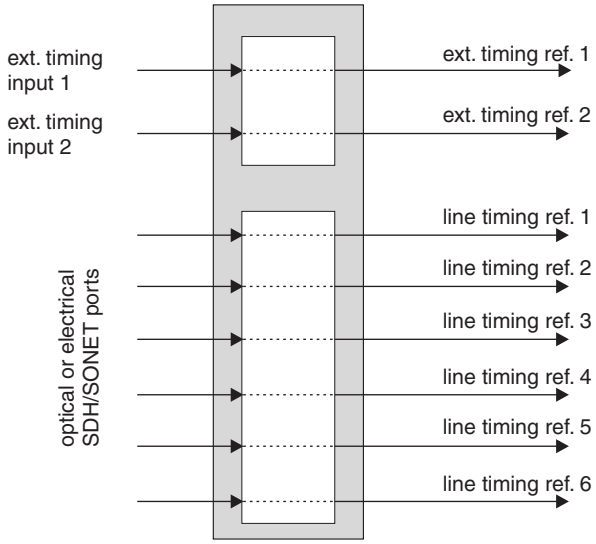
  The same timing reference signal is available at both station clock outputs.
- SONET synchronization mode:

  The timing reference signals for the external timing outputs are derived *directly* from the line timing references 1 and 2:
  - The timing reference signal for the external timing output 1 is derived directly from line timing reference 1.
  - The timing reference signal for the external timing output 2 is derived directly from line timing reference 2.

  It is possible to provision different signal formats for the two station clock output signals.

Timing references

The total number of timing references is limited to eight, two external timing references and six line timing references:



External timing references

There is a direct and fix relation between the two external timing inputs (station clock inputs) and the two external timing references:

- Ext. timing input 1 → ext. timing reference 1
- Ext. timing input 2 → ext. timing reference 2

However, the two external timing inputs are *not automatically assigned* as external timing references. This assignment needs to be done manually (and individually for both external timing references), and the internal timing generator needs to be operated in locked mode for the external timing reference assignment to take effect.

Important! When the system clock is locked to an external timing reference, then removing or replacing the standby cross-connection and timing unit (XC160, XC320, XC640) causes the external timing reference to be declared failed temporarily, and a timing reference switch will occur.

You have the following options:

- Before removing or replacing the standby cross-connection and timing unit, make sure that the system clock is *not* locked to an external timing reference by manually switching to a different timing reference (cf. “External switch requests” (p. 4-45)), or entering the forced holdover mode (cf. “System timing operational modes ” (p. 4-46)).
- Accept the intermediate timing reference switch (traffic is *not* affected).

Line timing references

Up to six out of the available incoming SDH/SONET transport signals can be assigned as line timing references. An 8-kHz timing reference signal is then derived from the inherent clock information.

The assignment needs to be done manually by specifying an SDH/SONET port as a line timing reference.

Important! Please observe these guidelines concerning the line timing reference assignment:

a	Only one SDH/SONET port *per port unit* can be assigned as a line timing reference.
b	The line timing reference assignment will have no effect on the system timing unless the **Clock Mode** in the **System Timing** tab is set to **Locked**.
c	Due to their role in the internal data communication between port units and the cross-connection and timing unit, it is *not possible* to select one of the following input ports as a line timing reference: • 2.5-Gbit/s port units (OP2G5): port 4 • 622-Mbit/s port units (OP622): ports 6, 7, 13, and 14 This applies to all OP2G5 or OP622 port units, and independent of the slot position where they are installed.
d	The OPT2G5 input signals *cannot* be used as line timing references because the Multiplex Section (MS) layer is neither terminated nor monitored on the OPT2G5 port unit. On the Multiplex Section (MS) layer, the Synchronization Status Message (SSM) functionality is located in the client signal.
e	EP51/EL36 port units: EC-1 or DS3 input signals *cannot* be used as line timing references. In the outgoing direction, however, the Synchronization Status Message (SSM) is supported.
f	When you assign a line timing reference while the timing generator operates in autonomous holdover mode, then a `Loss of Synchronisation` alarm will be reported for 10 seconds which, however, can be ignored. The timing functionality is not impaired.
g	GE1 and GE10PL1 port units: Gigabit Ethernet input signals at GE1, 1GEPL or 10GEPL ports *cannot* be used as line timing references.
h	The input signals of port 4 and port 8 of an OP2G5D/PAR8 port unit *cannot* be used as line timing references.

Timing reference selection

The 1675 LambdaUnite MSS timing function is designed such that always the most suitable clock source is selected for synchronization.

The selection is based on the quality of the available timing reference signals in combination with their provisionable priority:

- If the **System SSM Mode** is set to **QL Enable** (i.e. the SSM information is evaluated), then initially the signal with the highest quality level is used as the timing reference signal. If it fails, the system switches to the signal with the next lower quality level. If there are several timing references with the same quality level, they are used according to the priority list. Please also refer to "Timing reference switching" (p. 4-43).
- If the **System SSM Mode** is set to **QL Disable**, then only the priorities of the timing reference signals are evaluated. Please also refer to "Timing reference switching" (p. 4-43).

If all possible timing reference signals fail, then the timing generator autonomously enters the holdover mode.

Timing link switches

1675 LambdaUnite MSS supports two types of timing link switches:

- System timing link switch
- Output timing link switch (only in the SDH synchronization mode)

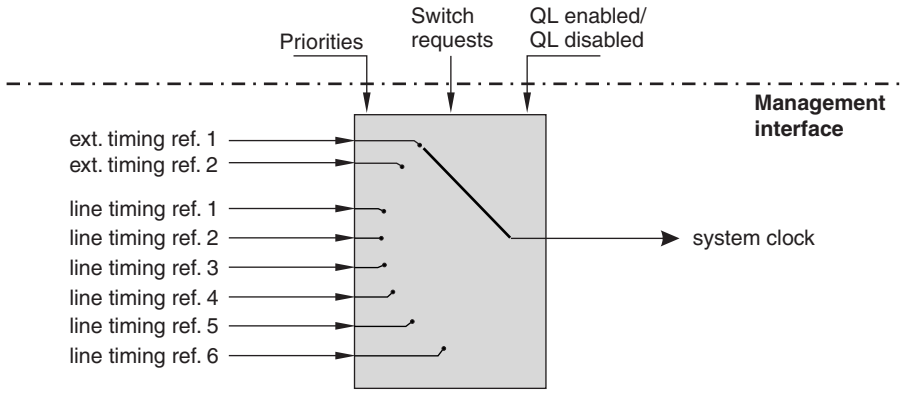
Automatic timing reference selection

A timing link switch (TLS) allows reference switching between all possible timing references. By means of a provisional input port priority scheme and, optionally, quality levels (QLs, determined by evaluating the Synchronization Status Message (SSM)), one of the inputs is selected as the reference to be forwarded to either the internal timing generator (in the case of the system timing link switch), or to the external timing output selection (in the case of the ouput timing link switch).

Please also refer to "Reference priorities" (p. 4-42) and "Timing reference switching" (p. 4-43).

System timing link switch

The system timing link switch has access to all available timing references (external as well as line timing references). It is used to supply the internal timing generator with the best available clock source. The internal timing generator then provides the clock information for the SDH/SONET output ports.



Re-synchronization after frequency offsets

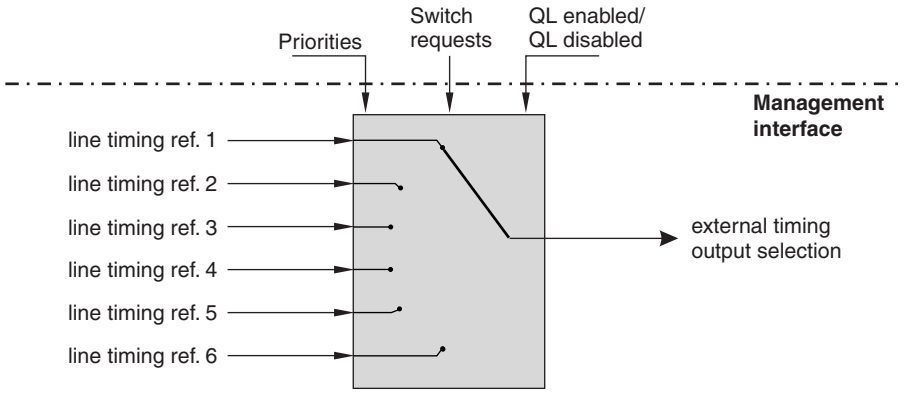
In case of a frequency offset at a timing input port which is currently used as the active system timing reference, a re-synchronization process starts which may last up to 180 seconds, depending on the size of the frequency step (a frequency offset of 5 ppm requires a re-synchronization period of approximately 24 seconds, for a example).

Usually, frequency offsets up to 12 ppm do not cause a timing reference protection switch, and are not externally visible. However, should it happen that you perform an XC protection switch immediately after such a frequency offset, then an output timing reference switch and even TXI failures might occur which will afterwards be cleared autonomously.

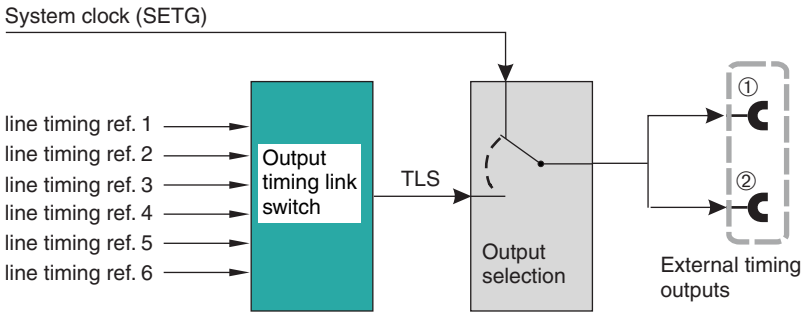
Output timing link switch

The output timing link switch is only supported in the SDH synchronization mode.

The output timing link switch has access to all available line timing references. It has no access to the external timing references because this could lead to undesirable synchronization loops in the office.



The output timing link switch is used to provide the external timing output selection with the best available line timing reference. Alternatively, the clock source for the external timing outputs can also be derived from the internal timing generator (SETG, "Synchronous Equipment Timing Generator"); please also refer to "SDH- and SONET-specific functional implementation" (p. 4-33).



Reference priorities

An input of a timing link switch can be assigned a priority between "1" (highest priority) and "8" (lowest priority). Furthermore, it is also possible to assign a priority of "0", which means to disable the corresponding input. It is possible to assign the same priority to multiple inputs.

Priority	Meaning	
0		Disabled
1	Highest priority	Enabled
↓	↓	
8	Lowest priority	

The selection between revertive and non-revertive mode is achieved via the priority setting of a timing reference. All references with the same priority are non-revertive timing references. If two timing references are the same in terms of quality level (QL) provisioning, revertive switching is provisioned by giving one timing reference a higher priority than the other. The timing reference with the highest priority always will be the active timing reference (if both are available).

In the default setting, all inputs are disabled (priority = 0).

Besides the possibility to permanently disable an input of a timing link switch, it is also possible to temporarily exclude an input ("Lockout"; see "External switch requests" (p. 4-45)).

Timing reference switching

1675 LambdaUnite MSS systems provide revertive timing reference switching between the enabled inputs of a timing link switch.

The selection criteria for the automatic timing reference selection are based on quality and priority, and can be provisioned to one of the following modes:

Quality level (QL) enabled mode (default setting)	The automatic timing reference selection is based on the incoming quality level (= Synchronization Status Message (SSM)) in combination with the reference priority.
Quality level (QL) disabled mode	The automatic timing reference selection is based on the reference priority only.

Manual timing reference switching is also possible by means of specific switch requests; please refer to "External switch requests" (p. 4-45).

Timing reference switching in QL-enabled mode

In the QL-enabled mode, the timing reference selection is according to the following criteria, provided that no external forced or manual switch request is active:

1. The active timing reference is selected from all the enabled inputs (enabled and not locked out) which have a quality level better than "Do not use for synchronization" (DNU/DUS).
2. Out of these timing references, the reference with the highest quality level is selected. The following order of quality levels applies (in decreasing order of quality from left to right):
   - SDH: PRC → SSU_T → SSU_L → SEC → DNU
   - SONET: PRS → STU → ST2 → ST3 → DUS

If there is more than one reference with the same highest quality level, then the reference with the highest priority is selected, if the priority is unique. If a group of references has the same highest priority (and the same highest quality level), then the existing reference selection remains unchanged if the selected reference is among this group. Otherwise, an arbitrary reference from this group is selected.

3. If a timing reference can be selected, then the output of the timing link switch forwards the corresponding quality level.

   If no timing reference can be selected, then the output forwards the signal status "Unacceptable" and the quality level "Do not use for synchronization" (DNU/DUS).

The automatic timing reference selection is triggered by the following events:

- The QL-enabled mode is activated, and no external forced or manual switch request is active.
- A change of the quality level is detected on an enabled input (enabled and not locked out), and no external forced switch request overrides the autonomous selection.
- A change of reference priority is detected on an enabled input (enabled and not locked out), and no external forced or manual switch request overrides the autonomous selection.
- An active external forced or manual switch request is cleared.
- Timing references are enabled or cleared from lockout, while no external forced or manual switch is active.
- The currently active timing reference is disabled or locked out.

Timing reference switching in QL-disabled mode

In the QL-disabled mode, the timing reference selection is according to the following criteria, provided that no external forced or manual switch request is active:

1. The active timing reference is selected from all the enabled inputs (enabled and not locked out) which have a "Normal" signal status.
2. Out of these timing references, the reference with the highest priority is selected.

   If a group of references has the same highest priority, then the existing reference selection remains unchanged if the selected reference is among this group. Otherwise, an arbitrary reference from this group is selected.
3. If a timing reference can be selected, then the output of the timing link switch forwards the signal status "Normal".

   If no timing reference can be selected, then the output forwards the signal status "No Signal".

In case the **System SSM Mode** is set to **QL Disable**, then the signal forwarded by the output timing link switch depends on the timing generator operational mode as follows:

Timing generator operational mode	Signal forwarded by the output timing link switch
Normal	"Do not use for synchronization" (DNU/DUS) in both synchronization modes
Locked	
Forced Holdover	
Free Running	"Do not use for synchronization" (DNU) in the SDH synchronization mode, "Synchronized - Traceability Unknown" (STU) in the SONET synchronization mode
Autonomous Holdover	

The automatic timing reference selection is triggered by the following events:

- The QL-disabled mode is activated, and no external forced or manual switch request is active.
- A change of the signal status is detected on an enabled input (enabled and not locked out), and no external forced switch request overrides the autonomous selection.
- A change of reference priority is detected on an enabled input (enabled and not locked out), and no external forced or manual switch request overrides the autonomous selection.
- An active external forced or manual switch request is cleared.
- Timing references are enabled or cleared from lockout, while no external forced or manual switch is active.
- The currently active timing reference is disabled or locked out.

External switch requests

These external switch requests exist:

Switch request	Meaning
Lockout	can be used to temporarily disable (lock out) an input of a timing link switch. The "ABN" LED on the user panel is lit while this request is active, and a corresponding abnormal condition event will be shown in the **Abnormal Condition Events** list.
Clear lockout	clears (reverts) a lockout request.

Switch request	Meaning
Forced switch to reference	can be used to force the selection of a desired (not locked out) timing reference for timing reference selection. The "ABN" LED on the user panel is lit while this request is active, and a corresponding abnormal condition event will be shown in the **Abnormal Condition Events** list.
Manual switch to reference	can be used to switch to another (not locked-out) timing reference, provided that the quality of the desired timing reference equals the quality of the previously active timing reference.
Clear reference switch	clears (reverts) a manual or forced switch request.
Clear wait-to-restore (not applicable to the SDH output timing link switch)	can be used to terminate the wait-to-restore period, i.e. to reset the wait-to-restore timer immediately.

Function of the internal timing generator

1675 LambdaUnite MSS synchronizes all add, drop and through signals by using one timing source. The timing is ensured by the internal timing generator on the cross-connection and timing unit which generates one common internal clock. When two cross-connection and timing units are present in the NE, a 1+1 non-revertive protection of the timing function is provided.

System timing operational modes

The 1675 LambdaUnite MSS internal timing generator can be operated in one of the following modes:

- *Locked mode*

  In the locked mode, the timing functions on the switching units can be provisioned to accept a timing reference signal with a specified priority from

  – synchronous line timing, one out of six, or

  – external netclock timing, one out of two.

  Only one of these reference signals can be active at a time. The timing reference signal is continuously monitored for error-free operation. If the reference signal becomes corrupted or unavailable, the timing function selects the timing reference signal that is next in the priority list. If all configured timing reference signals are corrupted or unavailable, the timing function enters the holdover mode.

  The timing function on the active switching unit synchronizes its internal Stratum 3 clock to the reference signal. The timing function on the standby XC circuit pack synchronizes its internal Stratum 3 clock to the active circuit pack. Then the timing functions distribute the clock signals to all circuit packs in the shelf.

The timing reference for the external netclock output can be provisioned independently from the timing reference for the system clock.

Synchronous line timing

In the locked mode, the timing functions on the XC circuit packs can be provisioned to accept a timing reference signal from an incoming synchronous signal (40 Gbit/s, 10 Gbit/s, 2.5 Gbit/s, 622 Mbit/s or 155 Mbit/s). The timing functions then employ the provisioned timing reference signal from the specified port unit to synchronize the transmission port units.

External netclock timing

Another possibility for the locked mode is to receive external reference timing. In this case the timing function on the active XC circuit pack receives a DS1 *Telcordia™* (B8ZS, SF and ESF format; if in ESF format SSM is supported) or a 2.048-MHz, 2-Mbit/s ITU-T reference signal from the external netclock inputs.

- *Free-running mode*

  The internal timing generator is supplying timing to the system, uncorrected by any external or line timing references, and independent from historical reference data.

  This is the default value of the system timing operational mode (especially needed for the initial startup of an NE when no valid timing references are assigned).

  However, the standby timing generator, if any, remains locked to the active timing generator.

  The “ABN” LED on the user panel is lit as long as the timing generator operates in the free-running mode, and a corresponding abnormal condition event will be shown in the **Abnormal Condition Events** list.

  It is strongly recommended to switch from the free-running mode to the locked mode as soon as valid timing references are available and assigned.

- *Holdover mode*

  The active timing generator enters the holdover mode if all configured timing reference signals fail. In the holdover mode, the active timing generator keeps its internal Stratum 3 clock at the point at which it was synchronized to the last known good reference signal. The standby timing generator remains locked to the active timing generator. When the reference signal is restored, the active timing generator exits the holdover mode and resumes the normal locked timing mode.

  Holdover mode is automatically available when the system clock is in the locked timing mode. The timing functions on the XC160/XC320/XC640 circuit packs monitor the quality of reference signals they receive. If one of the reference signals fails, 1675 LambdaUnite MSS uses the next in the priority list. If all reference signals fail, 1675 LambdaUnite MSS enters the holdover mode.

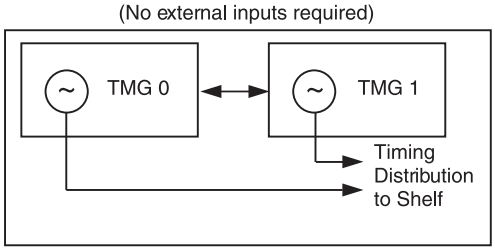
  Additionally it is also possible to force the internal oscillator on the timing generator into the holdover mode (forced holdover mode).

  Switching to the holdover mode is only possible from the locked mode.

  The “ABN” LED on the user panel is lit as long as the timing generator operates in the *forced holdover* mode, and a corresponding abnormal condition event will be shown in the **Abnormal Condition Events** list. *No* abnormal condition will be indicated when the timing generator operates in the *autonomous holdover* mode.

Important! When you assign a line timing reference while the timing generator operates in autonomous holdover mode, then a `Loss of Synchronisation` alarm will be reported for 10 seconds which can be ignored. The timing functionality is not impaired.

The following figure illustrates the free running mode, in which 1675 LambdaUnite MSS is synchronized by timing signals generated in the timing functions on the switching unit.



Timing protection

1675 LambdaUnite MSS uses non-revertive 1+1 protection switching to protect its timing function. If the active XC circuit pack fails and causes a switch to the standby circuit pack, the standby circuit pack becomes the active circuit pack. It remains the active circuit pack, even when the failed circuit pack is replaced. The replacement circuit pack becomes the standby circuit pack. There is no automatic revertive switching, but the timing protection switching can be done manually.

If the active timing generator were to fail while in holdover mode, then the standby timing generator would become the active timing generator and would switch to holdover mode (before switching, it was fed by the active timing generator) until the reference signal is restored to an acceptable quality.

Timing provisioning

The 1675 LambdaUnite MSS synchronization features can be provisioned by using *WaveStar*® CIT or OMS. Additionally, either timing generator circuit pack can be switched to be the active timing generator. However, when 1675 LambdaUnite MSS is provisioned for the locked mode, the holdover mode is entered automatically upon loss of all reference signals.

Control and status

The behavior of the timing generators is controlled by switching them among several defined states. As commands are issued or as failures occur and are cleared, the timing system switches from one state to another. The status of the timing is retrievable for user observation. You can issue commands to obtain status reports or to manually change the synchronization state from one to another.

There are three categories of commands

- Modify – to provision operating parameters
- Retrieve – to obtain parameter values, states and statuses
- Operate – to lockout a switch, force a switch or holdover mode or clear a state

Synchronization switching

Synchronization operations that can be user-controlled by commands include

- Non-revertive synchronization equipment switching
- Synchronization reference switching
- Synchronization mode switching

Synchronization Status Message (Timing Marker)

A Synchronization Status Message (SSM) is defined for SDH/SONET transport signals, and for framed 2-Mbit/s and DS1 ESF external timing input signals.

The purpose of the SSM is to signal the reference clock quality level from NE to NE in order to:

- enable the timing generator to extract the timing reference signal with the best quality,
- make it possible to autonomously enter the holdover mode in case there is no suitable timing reference signal, and to
- prevent timing loops.

These timing quality levels are defined (top down in decreasing order of timing quality):

SDH	SONET	Timing quality
PRC	PRS	*best* timing quality
SSU-T	STU	↓
SSU-L	ST2	↓
SEC	ST3	↓
DNU	DUS	↓

Depending on the type of timing reference signal the SSM is transported as follows:

- SDH/SONET transport signals:

  The SSM information is carried in the bits 5 to 8 of the S1 byte (in the SDH Multiplex Section Overhead (MSOH), or the first STS-1 of a SONET transport signal, respectively).
- Framed 2-Mbit/s external timing input signals:

The SSM information is carried in one of the Sa bits (Sa4 to Sa8) in time slot zero (TS0) of the 2-Mbit/s multiframe. Which Sa bit is to be used is provisionable.

- DS1 ESF external timing input signals:

  The SSM information is carried in the DS1 ESF data link code word.

The defined timing quality levels and the related bit combinations are listed in the following table.

Timing quality level		S1/Sa value	DS1 ESF data link code word
SDH	SONET		
–	Synchronized - Traceability Unknown (*STU*)	0000	00001000 11111111 (08 FF hex.)
–	Primary Reference Source (*PRS*)[1]	0001	00000100 11111111 (04 FF hex.)
Primary Reference Clock (*PRC*), acc. to ITU-T Rec. G.811	–	0010	–
Transit Node Clock (*SSU-T*), acc. to ITU-T Rec. G.812	–	0100	–
–	Stratum-2 clock (*ST2*)[1]	0111	00001100 11111111 (0C FF hex.)
Local Node Clock (*SSU-L*), acc. to ITU-T Rec. G.812	–	1000	–
–	Stratum-3 clock (*ST3*)[1]	1010	00010000 11111111 (10 FF hex.)
SDH Equipment Clock (*SEC*), acc. to ITU-T G.813, option 1	–	1011	–
–	–	1100	–
–	–	1110	–
Do not use for synchronization (*DNU*)	Do not use for synchronization (*DUS*)	1111	00110000 11111111 (30 FF hex.)

Notes:

1. According to the Telcordia Technologies TR-1244 standard.

The quality level “Do not use for synchronization” (DNU/DUS) is inserted autonomously if AIS, LOS or LOF is detected in the incoming signal.

The insertion of “DNU/DUS” into the outgoing timing signal can also be configured manually in order to avoid timing loops in the network.

SSM of lower quality than the internal system clock

If the SSM of a reference clock source indicates a lower quality than the internal clock (SEC), this reference is considered to be failed and the system switches to the reference with the next lower priority. When all assigned references are considered to be failed, the system enters the holdover mode.

No SSM available

As long as no SSM value has been accepted and evaluated at STM-N/OC-M interfaces, “Do not use for synchronization” (DNU/DUS) is assumed. This also applies at the initial system start-up.

SSM for 2 Mbit/s synchronization signals

1675 LambdaUnite MSS supports the interpretation and generation of SSM values for framed 2-Mbit/s timing input and output signals. The coding conforms to ITU-T Rec. G.704. One set of the Sa4 to Sa8 bits can be allocated for the Synchronization Status Message (SSM). The message set is similar to the SSM messages defined for bits 5 to 8 of the S1 byte belonging to an SDH signal.

Timing reference protection switching

If the **System SSM Mode** is set to **QL Enable**, then initially the signal with the highest quality level is used as timing reference signal. If it fails, the system switches to the signal with the next lower quality level. If there are several timing references with the same quality level, they are used according to the priority list.

If the **System SSM Mode** is set to **QL Disable**, then only the priorities of the timing reference signals are evaluated.

If all possible timing reference signals fail, then the timing generator enters the holdover mode.

The following signals can be used as a timing reference:

- 2 external netclock input signals (2048 kHz or 2 Mbit/s acc. to G.703)
- 6 reference signals derived from the incoming STM-N/OC-M line signals (N = 1, 4, 16, 64; M = 3, 12, 48, 192).

The following alarms cause a protection switch from a G.703 2-Mbit/s signal to another timing reference signal (acc. to the priority list):

- LOS (Loss of Signal)
- AIS (Alarm Indication Signal)
- LOF (Loss of Frame)
- LOM (Loss of Multiframe)

The following alarm causes a protection switch from a G.703 2048 kHz signal to another timing reference signal (acc. to the priority list):

- Loss of Signal

The following alarms and fault criteria cause a protection switch from an SDH/SONET signal to another timing reference signal (acc. to the priority list):

- LOS
- AIS
- LOF
- LOM

In case the **System SSM Mode** is set to **QL Disable**, then the SSM transmitted in the outgoing SDH/SONET signal is as follows:

- "Do not use for synchronization" (DNU) in the SDH synchronization mode,
- "Synchronized - Traceability Unknown" (STU) in the SONET synchronization mode.

User operation

By using the *WaveStar*® CIT, you can define whether the SSM is evaluated for the selection of the timing reference from which the system timing is derived (→ **System SSM Mode**). Furthermore, you can define the quality level of the outgoing timing signal (→ **Provisioned Quality Level**).

Timing equipment protection switching

A network element can contain two cross-connection and timing units (XC160, XC320, XC640) on which the timing function is located. If there are two timing functions, then timing equipment protection switching is possible.

When two timing circuit packs are present the timing is derived from one circuit pack, called the *active* timing circuit pack. The other timing circuit pack is standby. It produces the same timing signal as the active one but is not used, unless the active timing circuit pack fails.

For the initial start-up of the system and initial provisioning of the 1+1 protection group the free running mode is used. For the system reset after the 1+1 protection was in operation, the system restores the cross-connection and timing unit as the active one which was active before the system reset operation.

If only one cross-connection and timing unit is present (worker) and the protection cross-connection and timing unit is inserted, 1675 LambdaUnite MSS is in a warm-up state for about 6 minutes. During this time the system timing quality is set to "Do not use for synchronization" (DNU/DUS). During this state is is not allowed to perform a manual switch command to the standby cross-connection and timing unit.

Wait-to-restore time

If a timing signal is restored, then – after a defined period of time (wait-to-restore time, WTR) – a decision is made based on the timing quality and on priority selection criteria, which signal to select. If the same signal has the best quality and/or highest priority, then the same signal will be selected, else another signal.

Timing with facility loopbacks

1675 LambdaUnite MSS supports two types of facility loopbacks.

When a near side facility-loopback is set, the signal received is transparently looped to the corresponding output. However the SSM received in the S1 byte of the overhead will be overwritten with the value “Do not use for synchronization” (DNU/DUS) in the outgoing S1 byte. This is done because a looped signal should not be used as a timing reference in order to avoid timing loops.

Configuration rules

Consider the following configuration rules to set a facility loopback:

- When a facility loopback is performed for an SDH/SONET port, then this port cannot be assigned as a line timing reference.
- When an SDH/SONET port is assigned as a line timing reference, then no facility loopbacks can be performed for that port.

Timing slaved to MSP/APS groups

When a line timing reference is assigned to the worker port of a

If for instance the worker fibre is cut, the MSP/APS protection switch status shows an SF condition and the protection port is active. In this case timing will select the protection line as timing reference and the reference is not failed. When the timing reference is the active reference, in the outgoing S1 byte the quality DNU/DUS will be forwarded (in order to prevent timing loops when this signal is used as timing reference).

In the case that the timing reference is the active reference on the worker port of an MSP/APS group, in both worker and protection lines the quality DNU/DUS will be forwarded in the outgoing S1 byte.

Configuration rules

An MSP/APS group can only be provisioned successfully when no line timing reference is assigned to the protection port of the

Timing and 4-fiber MS-SPRing/BLSR protection switching

1675 LambdaUnite MSS supports two types of protection switching in 4-fiber MS-SPRing/BLSR groups:

- Timing slaved to the span switching protocol
- Timing independent of the ring switching protocol

Timing slaved to the span switching protocol

The same as for MSP/APS groups also applies to 4-fiber MS-SPRing/BLSR groups. There are 2 MSP/APS groups (spans), an east span and a west span. So in this case timing is slaved to the span switching protocol.

Configuration rules

Consider the following configuration rules:

- A 4-fiber MS-SPRing/BLSR protection group can only be provisioned successfully when no line timing reference is assigned to the protection port of the spans of the 4-fiber MS-SPRing/BLSR group (East/West protection port).
- It is not possible to derive a line timing reference from a protection port in a protection group (exception: asymmetric 4-fiber MS-SPRing/BLSR protection group). A line timing reference can only be assigned to the *worker port* of the East and West span.
- Assigning a line timing reference to a protection port is only allowed in an *asymmetric* 4-fiber MS-SPRing/BLSR protection configuration.

Timing independent of the ring switching protocol

When line timing is used in a 4-fiber MS-SPRing/BLSR protection group, on both spans (East and West), line timing is assigned. If one of the spans is failed (say east, both the worker line and the protection line of the MSP/APS spans are failed), a transmission ring switch will occur. However timing will not follow the transmission ring switch. It will be detected that the east line timing reference is declared failed. Timing will then switch automatically to the assigned line timing reference on the west span and the ring timing will be derived via the west side of the ring. When the span failure has recovered, timing switches back to the east line timing reference of the recovered span and timing will be derived again from the east side.

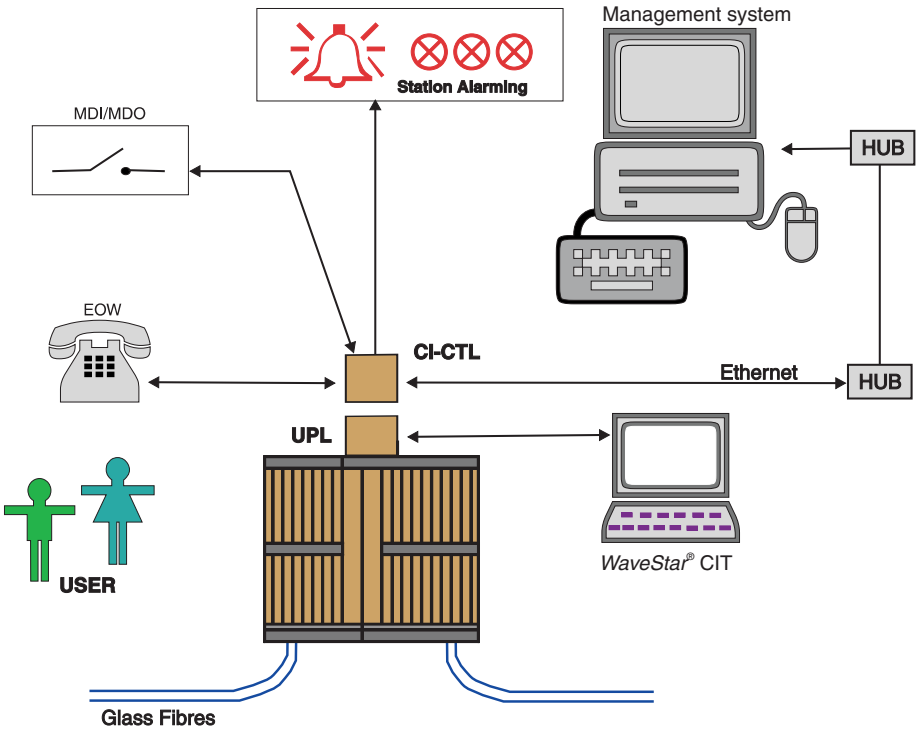
Please note, a span is only recovered when the worker line has no signal failure. When timing is switched back from the west side to the east side, always the worker line of the east side is selected.

Control

The functions in the 1675 LambdaUnite MSS network element are controlled by a system controller circuit pack (CTL) and by function controllers on the other circuit packs in the shelf. Overall shelf operation is controlled by signals received over the SDH Data Communication Channel (DCC) or the intra-office LAN (IAO LAN).

External control architecture

The following figure shows the external interfaces that have influence on the 1675 LambdaUnite MSS control architecture.



Internal control architecture

The following description shows the major paths of control and status information among the circuit packs in the 1675 LambdaUnite MSS shelf.

The control architecture is based on two levels of control. The highest level is the System Controller (CTL). The other circuit packs (OP10, OP2G5, OP622, OP155M, EP155, EP51, GE1 and XC160/XC320/XC640) contain a function controller. The function controller performs the local unit control and is connected to the system controller via the Operations Network Interface (ONI). Each CTL has two control functions, the System Control Function (SCF) and the DCC Control Function (DCF). Both control functions run independently from each other.

The Operations Network (ON) is the internal communications network and is physically implemented in a star topology. An ON hub function is placed on the SCF on the controller circuit pack (CTL).

The Equipment Management Protocol (EMP) control function is responsible for inventory data access, reset lines and equipment sensing (check physical availability of circuit packs).

In case of Data Communications Network (DCN) messages addressing the network element the DCN traffic is terminated on the DCC Control Function (DCF) on the CTL circuit pack. Application messages are forwarded to the System Control Function (SCF) on the CTL. In case of messages for other NEs the DCF decides on which of the channels listed below the message will be forwarded. Thus all channels are to be considered as bidirectional links.

The data communication network control function comprises the traffic

- from external interfaces and Craft Interface Terminal (CIT) at LAN ports forwarded via Controller Interface Protocol (CIP) to the DCF on CTL
- from the XC160/XC320/XC640 circuit pack where line DCC (DCCr) and section DCC (DCCm) are terminated via OverHead Interface (OHI) to the DCF on CTL.

Furthermore, the CTL is involved in user byte processing. The user bytes (E1, E2, F1) which are physically made available as 64-kbit/s channels at the Control Interface (CI), are fed to this interface via the XC160/XC320/XC640 and CTL circuit pack.

The CTL is also responsible for the control of equipment protection switching.

The Timing Interface Control (TIC) interaction ensures the isolation of a timing function in case that the CTL detects misbehavior. Normally the timing function on both XC160/XC320/XC640 can share the assigned functionality. However in case of isolation one timing function can take over all functionality. This is controlled by the TIC lines.

Additionally, the CTL supports the status indicators on the User Panel (UPL).

For further information about the controller features and capacities please refer to “Controller unit” (p. 4-29).

Power

1675 LambdaUnite MSS uses a distributed powering system, rather than bulk power supplies. To maintain high availability the power interface is duplicated. The system power supply is able to provide 3500 W power in the range -40 V to -72 V DC, respectively -48 V to -60 V DC nominal. Each circuit pack uses its own onboard power converter to derive the necessary operating voltages.

For detailed information about the power consumption please refer to "System power consumption" (p. 6-4) and to "Weight and power consumption" (p. 10-40).

Power interfaces

The office power supply is filtered and protected by circuit breakers on the power interfaces (PI/100) at the input to the shelf - except when the special "breaker-less" power interface version is used. To each power feeder one power interface is assigned. After that, the power supplies are distributed separately to each circuit pack, where they are filtered again and fused before being converted to the circuit pack working voltages.

The power interfaces are supervised individually by the system controller circuit pack (CTL). A green LED on every Power Interface indicates that an appropriate input voltage is available, that means it is in the range between –40.5 VDC and –72 VDC. Once the input voltage out of this range, an alarm message will be sent to the CTL.

Circuit breaker specifications

The circuit breaker located on the regular power interface (not on the special "breaker-less" power interface version) is designed to support a maximum rated current of 100 A with a BS characteristic (medium delay). It provides protection according to the EN 60950 in the power range up to 3500 W, in particular in the range of 87.5 - 48.6 A at 40 - 72 V.

Power indicator

The green PWR ON indicator on the user panel remains lit as long as a -48 V / -60 V supply is received from the circuit breakers.

Cooling

Fan unit

Cooling is provided by a plug-in fan unit placed on top of the sub-rack. Fans pull air through a filter below the circuit packs and force it through the sub-rack from bottom to top. An air flow baffle with air filter is integrated in the lower part of the subrack to prevent the intake of particles or exhaust air from below.

Fan controller

The fan unit includes four fans and a microcontroller that senses air flow, air temperature and fan faults. The microcontroller adjusts the speed of the fans to compensate for the failure of a fan or to conserve power when full air flow is not needed. It also reports the status of the fan unit to the system controller.

Important! The fan unit must be installed and operating in a shelf before any circuit packs are installed.

Air filter

The air filter, located below the subrack, must be replaced or cleaned under regular conditions (e.g. with Eurovent EU6 filters used in the HVAC) once every 3 months to ensure the proper cooling, as described in the *1675 LambdaUnite MSS User Provisioning Guide* chapter “Routine procedures” or as part of a trouble clearing procedure as described in the *1675 LambdaUnite MSS Maintenance and Trouble-clearing Guide*.

Fan speed control

The system autonomously determines the necessary fan rotation speed to produce sufficient airflow through the subrack in order to provide sufficient cooling for all system components.

The fan rotation speed can take on seven different values:

- Stage 1 is the lowest possible value which is needed for the minimal thermal configuration at 20 °C.
- Stage 7 is the maximum speed which the fan is capable to operate permanently.
- The intermediate stages are distributed between stage 1 and stage 7.

The initial value of the fan rotation speed is stage 4.

The system evaluates the need to update the fan speed every 5 minutes. Each circuit pack measures its current temperature and compares that temperature to a reference temperature specific for the circuit pack type. All circuit packs report their temperature difference to the Controller. From these values, the Controller determines the maximum temperature difference in the system (ΔT_{max}).

Depending on the value of ΔT_{max}, the fan rotation speed is increased or decreased by up to 3 stages per fan speed update interval according to the following rules:

$\Delta T_{max} \leq -6$ °C	The fan rotation speed is decreased by 3 stages.
-6 °C $< \Delta T_{max} \leq -4$ °C	The fan rotation speed is decreased by 2 stages.
-4 °C $< \Delta T_{max} \leq -2$ °C	The fan rotation speed is decreased by 1 stage.
-2 °C $< \Delta T_{max} < +2$ °C	The fan rotation speed remains unchanged.
$+2$ °C $\leq \Delta T_{max} < +4$ °C	The fan rotation speed is increased by 1 stage.
$+4$ °C $\leq \Delta T_{max} < +6$ °C	The fan rotation speed is increased by 2 stages.
$\Delta T_{max} \geq +6$ °C	The fan rotation speed is increased by 3 stages.

Consequent action in case of communication errors

In case of communication errors between the circuit packs and the Controller, which last longer than 2 minutes, the fan speed will be increased to stage 7 immediately. As the communication between the circuit packs and the Controller is interrupted, the worst case of shelf equipage is assumed, and therefore the fan rotation speed is increased up to its maximum.

Note: As during a circuit pack reboot or NE reboot, the communication between the circuit packs and the Controller is not available, in these cases the fan rotation speed will be increased up to its maximum, too.

Fan speed in maintenance condition

If the system operates in maintenance condition for at least two minutes, the fan rotation speed will be increased to stage 7 immediately.

Impact of temperature-relevant defects

When a `Unit Temperature too High` or a `Fan Failure` defect is present, the fan speed will be increased to stage 7.

5 Operations, administration, maintenance and provisioning

Overview

Purpose

This chapter describes hardware and software interfaces used for administration, maintenance, and provisioning activities, the system management function for the administration of the 1675 LambdaUnite MultiService Switch (MSS) and the maintenance and provisioning features available in the 1675 LambdaUnite MSS.

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Operations interfaces

Overview

Purpose

This chapter provides information about the operations interfaces of 1675 LambdaUnite MSS network elements.

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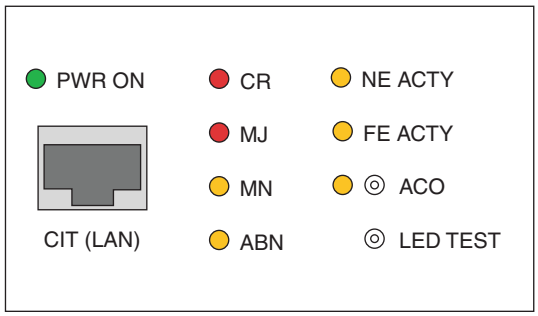
The 1675 LambdaUnite MSS user panel

User panel

The user panel of a 1675 LambdaUnite MSS network element provides visual status indicators, test and control buttons and a connector for connecting the *WaveStar*® CIT.



Visual indicators

Each user panel is equipped with the following LEDs:

LED	Function
PWR ON (green)	Indicates that power is applied to the shelf. The LED is lit if either 48V feeder is on.
CR (red)	Indicates that at least one critical alarm is present. Please refer to "User panel alarm LEDs" (p. 5-5).
MJ (red)	Indicates that at least one major alarm is present. Please refer to "User panel alarm LEDs" (p. 5-5).
MN (yellow)	Indicates that at least one minor alarm is present. Please refer to "User panel alarm LEDs" (p. 5-5).
ABN (yellow)	Indicates abnormal conditions – temporary conditions that may potentially be service affecting. Please refer to "Abnormal Conditions (ABN) LED" (p. 5-6).
NE ACTY (yellow)	Indicates that at least one near-end transmission alarm is present. Transmission alarms are all alarms of the category "Communication (Transport)" except `Remote Defect Indication`.
FE ACTY (yellow)	Indicates that at least one far-end transmission alarm (`Remote Defect Indication`) is present.
ACO (yellow)	Indicates that the alarm cut-off button (ACO button) was pressed. Please refer to "Alarm acknowledgment" (p. 5-5).

Controls and connectors

In addition to the LEDs, the user panel is equipped with two buttons and a connector:

Button/connector	Function
LED TEST	An LED test button for testing all shelf LEDs (except PWR ON on the user panel and the fan unit LEDs). Please refer to "LED test" (p. 5-6).
ACO	An alarm cut-off button to suppress both visual and audible alarm indications. Please refer to "Alarm acknowledgment" (p. 5-5).
CIT	An RJ-45 connector for connecting a *WaveStar*® CIT.

User panel alarm LEDs

There are three alarm LEDs on the user panel, Critical (CR), Major (MJ), and Minor (MN). They indicate that alarms of the corresponding severity are present. Alarms having a different alarm severity than CR, MJ or MN cannot be indicated via the user panel alarm LEDs.

An alarm LED (CR, MJ or MN) is lit, if at least one alarm of the corresponding severity is present.

Alarm acknowledgment

Alarms can be acknowledged (confirmed) by pushing the alarm cut-off button ("ACO" button). The alarm LEDs currently lit are switched off when the "ACO" button is pushed. Simultaneously the "ACO" LED is lit as a reminder of the alarms confirmed.

Please note that if a new alarm is detected afterwards, then the "ACO" LED is *not* switched off, and the alarm LEDs are lit according to the current alarm status.

Alarm signaling after a CTL protection switch

After a CTL protection switch, all currently present alarms are redetected and reported again. As a consequence the alarm signaling is as follows:

- All currently present alarms, including those that were previously acknowledged by means of the ACO button on the user panel, will be signaled again by the user panel LEDs and the office alarm interfaces.
- In the **NE Alarm List**, the same alarms are listed as before the CTL protection switch. They are, however, marked with a new timestamp.
- In the **NE Alarm Log**, alarms may appear twice, but with different timestamps, without a clear notification between the two log entries.

Abnormal Conditions (ABN) LED

The purpose of the “ABN” LED is to indicate any potentially service-affecting abnormal conditions, but not to report failures. The user panel alarm LEDs (CR, MJ, MN) are used for the latter purpose.

Abnormal Conditions

Abnormal conditions are manually initiated maintenance activities that could affect service or potentially mask the reporting of service affecting failures:

- A forced switch is active (protection group)
- A lockout of protection is active (protection group)
- The system operates in holdover mode (system timing)
- The system operates in free-running mode (system timing)
- A facility loopback is active
- A cross-connection loopback is active
- A test access session is active
- The system operates in maintenance mode
- The port control of an ONNS port is set to lockout

LED test

You can test the user panel and circuit pack LEDs by pushing the LED TEST button on the user panel.

During a test cycle the LEDs are activated for 5 seconds. Afterwards, the LED indications reflect to actual state of the network element which may either be the same state as before the test, or a new state, if changes occurred in the meantime.

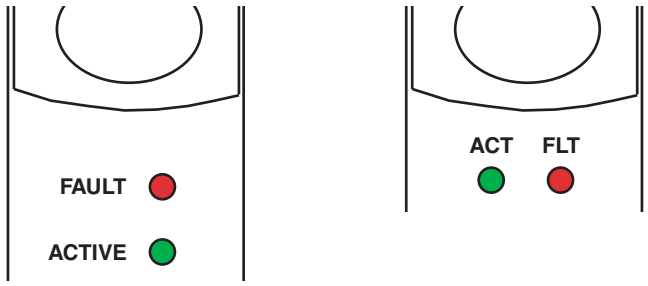
Please take note of the following:

- The “PWR ON” LED on the user panel and the fan unit LEDs are not affected by the LED test.
- The red fault LED of the GE1 port unit starts flashing for some seconds, in contrast to the LEDs of all other circuit packs, which are permanently on for some seconds.
- After an LED test, the restoration of the LED state may be delayed for some seconds on single circuit packs.

Circuit pack LEDs

Status LEDs on the circuit pack faceplate

1675 LambdaUnite MSS circuit packs are equipped with a red fault LED (labelled "FAULT" or "FLT") and a green activity LED (labelled "ACTIVE" or "ACT") located on the circuit pack faceplate.



Note that the Timing Interface (TI), the Connection Interface of the Controller (CI-CTL) and the user panel do not have a status LED, and that the Power Interface (PI) has a green LED only.

Activity LED

The green activity LED is used to indicate the activity state of an operational circuit pack. An operational circuit pack can be either in service (active) or in the standby mode. A flashing activity LED indicates that a circuit pack is in the initialization or self-test phase, or that a software download is ongoing. Please also refer to "Circuit pack status indications" (p. 5-11).

Fault LED

The red fault LED is primarily used to indicate failures attributable to the relevant circuit pack.

Please note that the indications via the red fault LED on the circuit pack faceplate is *controlled by defects*, not alarms.

The possible Fault LED states are shown in the following table.

Fault LED State	Meaning
On	indicates an equipment defect on the circuit pack.
Off	indicates that no defects are present.

Fault LED State	Meaning
Flashing	indicates an incoming signal defect, an external timing defect or a memory corruption. (see note 1)

Notes:

1. Please note that the red fault LED on the cross-connection and timing units (XC) is also flashing when port units are inserted into the system (the red fault LED is flashing while the port units are in the initialization phase). Furthermore, the red fault LED on an XC is also flashing when a software download to that XC is in progress. The reason for the flashing red fault LED is that temporarily the TXI communication channels cannot be established as long as the port units are still in the initialization phase, or while the software download is in progress.

Gigabit Ethernet port LEDs

On the faceplate of the Gigabit Ethernet circuit packs, there are additional LEDs associated with each external Ethernet port (LAN port).

GE1

The GE1 Gigabit Ethernet unit has the following port LEDs:

- The green LED labelled “LINK” indicates the link status, i.e. whether the status of the link is operational or not.
- The yellow LED labelled “DATA” indicates link activity (transceived data in both transmission directions).

GE10PL1

The GE10PL1 Gigabit Ethernet unit has the following port LEDs:

- A bicolored LED (red/green) for the following purposes:
  - The red LED indicates the fault status of the respective GbE port (1 GbE, or 10 GbE).
  - The green LED indicates the link status, i.e. whether the status of the link is operational or not.

  The red LED takes priority over the green LED, that is no link status information will be displayed as long as a fault condition is present for the respective port.
- A yellow LED indicates link activity (transceived data in both transmission directions).

Port LED indications of the GE10PL1 Gigabit Ethernet unit

The signaling via the red port LED of the GE10PL1 Gigabit Ethernet unit is as described in “Port LEDs associated to pluggable optical interface modules” (p. 5-10).

The signaling via the green link status LED is as follows:

- The LED is lit when the physical layer is up and traffic can be transmitted (No `LAN Loss of Signal` or `Loss of Synchronisation Eport` condition is present, and in case of 1 GbE interfaces, Autonegotiation is completed, bypassed or disabled).
- The LED is flashing (with a frequency of 1 Hz) when the physical layer is up but traffic *cannot* be transmitted (No `LAN Loss of Signal` or `Loss of Synchronisation Eport` condition is present, and in case of 1 GbE interfaces, Autonegotiation is configuring or failed).
- The LED is off when the physical layer is down (for example due to a `LAN Loss of Signal` or `Loss of Synchronisation Eport` condition).

Please note that autonegotiation is *not* supported for 10 GbE ports because autonegotiation is not defined for 10 Gigabit Ethernet ports by the standards.

The signaling via the yellow link activity LED is as follows:

- The LED is flashing when Ethernet frames have been transmitted or received over the related port.
- The LED is off when the port is not usable for transmission or reception, for example due to a missing pluggable module, or a `LAN Loss of Signal` condition, or due to squelching the output signal due to a Client Signal Fail (CSF) condition.

1 GbE interfaces

For 1 GbE interfaces, the maximum frame rate per second is about 1488095 frames (with a frame length of 64 Byte plus 20 Byte header and inter-frame gap).

The flashing frequency of the yellow link activity LED depends on the received/transmitted frame rate:

1 GbE interfaces	
The LED is flashing with a frequency of …	**when the frame rate is …**
1 Hz	up to about 148810 frames per second.
2 Hz	up to about 372024 frames per second.
4 Hz	up to about 744048 frames per second.
8 Hz	higher than about 744048 frames per second.

10 GbE interfaces

For 10 GbE interfaces, the maximum frame rate per second is about 14880952 frames (with a frame length of 64 Byte plus 20 Byte header and inter-frame gap).

The flashing frequency of the yellow link activity LED depends on the received/transmitted frame rate:

10 GbE interfaces	
The LED is flashing with a frequency of …	**when the frame rate is …**
1 Hz	up to about 1488095 frames per second.
2 Hz	up to about 3720238 frames per second.
4 Hz	up to about 7440476 frames per second.
8 Hz	higher than about 7440476 frames per second.

Port LEDs associated to pluggable optical interface modules

Each pluggable optical interface module has a red port LED associated for indicating failure conditions related to the optical interface module. The port LEDs are located on the parent board in the direct vicinity of the optical interface modules (OPLB/PAR8 and OP2G5D/PAR8 parent boards), or on the faceplate of the slide-in modules itself (OM10 slide-in modules); cf. “Optical interface modules” (p. 2-56).

The signaling via the red port LED is as follows:

- The LED is lit when a (hardware) failure of the associated optical interface module has been detected (`Optical Module Fail`).
- The LED is flashing when a `Loss of Signal` condition has been detected at the receive port of the associated optical interface module. As a precondition, however, the port monitoring mode needs to be “Monitored”. In addition to the port LED, also the red fault LED on the circuit pack is flashing in that case.

Indications on the insertion of circuit packs

The following sequence of indications will occur when you insert a circuit pack into its slot:

1. The red fault LED is lit.
2. The red fault LED goes off, and, after a delay of up to approximately 15 seconds, the green activity LED flashes as the circuit pack is initialized, self-tests are performed and software is downloaded.
3. The green activity LED stops flashing and
   a. is lit after the circuit pack or at least one port on a multi-port circuit pack becomes active.

      or

   b. goes off if the circuit pack is in the standby mode.

The green activity LED will be switched off and the red fault LED switched on if a failure is detected during initialization, self-test or software download.

If after the insertion of a circuit pack the red fault LED remains lit, then this indicates a defective function controller on the circuit pack.

Special case: Flashing red fault LED on XCs

Please note that the red fault LED on the cross-connection and timing units (XC) is also flashing when port units are inserted into the system (the red fault LED is flashing while the port units are in the initialization phase). Furthermore, the red fault LED on an XC is also flashing when a software download to that XC is in progress. The reason for the flashing red fault LED is that temporarily the TXI communication channels cannot be established as long as the port units are still in the initialization phase, or while the software download is in progress.

Circuit pack status indications

The following table provides an overview of the 1675 LambdaUnite MSS circuit pack status indications:

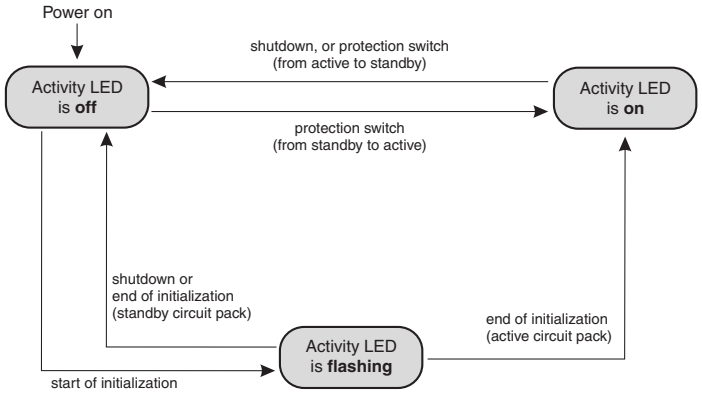
Operational State	Status	Red fault LED	Green activity LED
Circuit pack not yet operational	Powering-up (immediately after circuit pack insertion)[1]	●	–
	Initialization	–	✷
	Self-diagnostics		*Do not* open the latches of a Controller while its green activity LED is still flashing!
	Software download		
	Failure detected during initialization, self-test or software download	●	–
Active	No failure	–	●
	Failure of the circuit pack hardware	●	●
	Failure of an input signal	✷	●

Operational State	Status	Red fault LED	Green activity LED
Standby	No failure	–	–
	Failure of the circuit pack hardware	●	–
	Failure of an input signal[2] [3]	✷	–
Explanation: ● LED is on; – LED is off; ✷ LED flashes[4]			

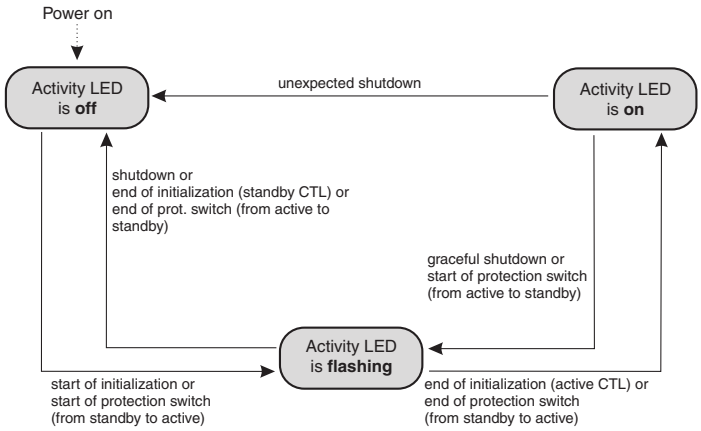
Notes:

1. This circuit pack state may possibly not be clearly recognizable depending on the duration of the power-up cycle and the time needed to start initialization.
2. A failure of an input signal is a failure detected at the input of at least one of the input ports of the circuit pack. A circuit pack input port may be a power supply, timing, control, or transmission port.
3. If a Portless TransMUX unit (TMUX2G5/1) is part of an equipment protection group and the DS3 test port is enabled, the red fault LED of the test port at the standby TMUX2G5 pack needs interpretation. The fault LED always visualizes the status of the physical port it belongs to, regardless whether the pack is active or standby. However, logically the test port exists only once per TMUX2G5/1 equipment protection group and can be enabled or disabled per group only. Therefore the fault LED is flashing once a DS3LOS condition is detected at the physical port. If, for example, the test port of a TMUX2G5/1 equipment protection group is enabled, a test set is connected to the active TMUX2G5/1 pack only, and nothing is connected to the standby TMUX2G5/1 pack, then the fault LED of the standby pack is flashing. If the activity LED (green) is off, that means the pack is standby, then a flashing fault LED can be ignored. If the activity LED is on, the flashing fault LED *is* relevant and should not be ignored.
4. The initial NVM synchronization phase of the standby Controller in a duplex control configuration is indicated by a flashing frequency of the green activity LED of 0.33 Hz (2 s on, 1 s off), while the flashing frequency is 1 Hz (0.5 s on, 0.5 s off) otherwise.
5. Please also refer to “Special case: Flashing red fault LED on XCs” (p. 5-11).

This diagram visualizes the circuit pack status indications via the green activity LED for circuit packs other than Controllers (port units and cross-connection and timing units):



This diagram visualizes the circuit pack status indications via the green activity LED for Controller (CTL) circuit packs:



The meaning of the terms “graceful shutdown” and “unexpected shutdown” is as follows:

“Graceful” shutdown	A software-controlled shutdown in order to avoid damage of the NVM file system. You can initiate a graceful shutdown of a Controller …: • … by performing a full reset of the Controller, or • … by opening the latches of the Controller, and *waiting for the green activity LED to stop flashing* before removing the Controller from its slot.

"Unexpected" shutdown	As the name implies, an unexpected shutdown of a Controller occurs unexpectedly, i.e. too suddenly to perform the shutdown in a controlled manner. Quickly removing a Controller (without waiting for the green activity LED to stop flashing) is an example of an unexpected shutdown. As a consequence, it cannot be excluded that the NVM file system might get corrupted.

Summary: Meaning of the circuit pack LED indications

The following tables summarize the possible meanings of the circuit pack LED indications.

The green activity LED is …	
… *off*	for a short period of time immediately after powering up of a circuit pack, i.e. after the insertion of a circuit pack, prior to the start of the circuit pack initialization.
	during normal operation if the circuit pack has the *standby role* in an equipment protection. This may be after successful initialization (following the circuit pack insertion) or after an equipment protection switch.
	after an "unexpected" shutdown of a Controller (applies to the green activity LED of the Controller only).
… *on*	during normal operation if the circuit pack is *unprotected* or has the *active role* in an equipment protection. This may be after successful initialization (following the circuit pack insertion) or after an equipment protection switch.
… *flashing*	during the circuit pack initialization (including self-diagnostics).
	during a software download.
	during a graceful (i.e. software-controlled) shutdown.
	during a protection switch.

Notes:

1. The initial NVM synchronization phase of the standby Controller in a duplex control configuration is indicated by a flashing frequency of the green activity LED of 0.33 Hz (2 s on, 1 s off), while the flashing frequency is 1 Hz (0.5 s on, 0.5 s off) otherwise.
2. The following impermissible equipage will be indicated by an indefinitely flashing green activity LED: Even though in LXC160 and LXC320 system configurations, the slot left to an installed LOXC/1 or LOXC40G3S/1 *must remain empty*, a port unit has been inserted in

such a slot. The circuit pack initialization of the incorrectly inserted port unit will not terminate, and the green activity LED will flash indefinitely.

The red fault LED is …	
… *off*	during the circuit pack initialization (including self-diagnostics).
	during a software download.
	during normal operation when *no defect* is present.
	during normal operation when the circuit pack is the *standby unit* in an equipment protection group.
… *on*	for a short period of time immediately after powering up of a circuit pack, i.e. after the insertion of a circuit pack, prior to the start of the circuit pack initialization.
	during normal operation if at least one “local equipment defect” is present.
	if only one single Controller is installed in a simplex control configuration, and this Controller is installed in the protection slot.
… *flashing*	during normal operation if at least one “client or server transport fault cause”, “data defect”, “internal interface defect”, “DCN defect”, or “synchronization defect” is present. A precondition however is that no “local equipment defect” is present at the same time, and that the circuit pack is *not* the standby unit in an equipment protection group.

Notes:

1. Please also refer to “Special case: Flashing red fault LED on XCs” (p. 5-11).
2. The alarm conditions that correspond to the mentioned types of defects or fault causes are listed subsequent to this table.

Local equipment defects

The red fault LED is on if at least one “local equipment defect” is present, which leads to one of the following alarm conditions:

- `Circuit Pack Failure`
- `IDE Flash Card Access Fail`
- `NVM Shortage`
- `Optical WDM Frequency Mismatch`
- `Resource Overload`

Client or server transport fault causes

The red fault LED is flashing if at least one “client or server transport fault cause” is present, which leads to one of the following alarm conditions:

- Alarm Indication Signal
- Backw Defect Transparent Ch
- Default K-bytes
- Degraded DS3 Line signal
- Degraded Signal
- DS3 Loss of Signal
- DS3 AIS
- DS3 Loss of Frame
- DS3 Application Mismatch ingress
- DS3 Idle Signal Indication ingress
- DS3 Remote Alarm Indication ingress
- DS3 Remote Alarm Indication egress
- DS3 AIS egress
- DS3 LOF egress
- Duplicate Ring Node
- Excessive Bit Error Ratio
- Extra Traffic Preempted
- Far End Signal Fail
- Forward Defect Indication
- Improper APS Codes
- Inconsistent Ring Protection Mode
- Local Squelch Map Conflict
- Loss of Frame
- Loss of Frame transp ch egress
- Loss of Multiframe
- Loss of Payload of OTN Channel
- Loss of Pointer
- Loss of Signal
- Node ID Mismatch
- Open Ring
- OTU2 Backward Defect Indication
- OTU2 Loss of Multiframe
- OTU2 Trace Identifier Mismatch

- OTU2 Loss of Frame
- Path Switch Denial
- Path Switch Inhibit
- Payload Defect Indication
- Payload Mismatch
- Payload Mismatch Transparent Ch
- Post DCM Signal Loss
- Pre DCM Signal Loss
- primary section Mismatch
- Prot. Arch. Mismatch
- Prot. Arch. Mode Mismatch
- Prot. Arch. Operation Mismatch
- Prot. Switch Byte Inappropriate
- Prot. Switch Byte Unacceptable
- Ring Discovery in Progress
- Ring Incomplete
- Ring Protection Switch Suspended
- Server Signal Fail
- Server Signal Fail Transparent Ch
- Signal Rate Mismatch
- Switch Channel Mismatch
- Trace Identifier Mismatch
- Traffic Squelched
- Unequipped
- Unknown Ring Type
- WTU3 Loss of Frame

Data defects

The red fault LED is flashing if at least one “data defect” is present, which leads to one of the following alarm conditions:

- Degraded Signal
- Excessive Bit Error Ratio
- GFP Extension Header Mismatch
- GFP Loss of Frame
- GFP User Payload Mismatch
- LAN Auto Negotiation Mismatch

- LAN Loss of Signal
- LOF Transparent Ch
- Loss of Alignment
- Loss of Multiframe
- Loss of Multiframe Transparent Ch
- Loss of Synchronisation Eport
- max number of VLAN instances reached
- Partial Loss of Capacity Rcvr
- Partial Loss of Capacity Transm
- Partial Transport Capacity Loss
- Payload Mismatch
- Sequence Number Mismatch
- Server Signal Fail
- Sink End Failure of Protocol
- Source End Failure of Protocol
- Total Loss of Capacity Rcvr
- Total Loss of Capacity Transm
- Total Transport Capacity Loss
- Trace Identifier Mismatch

Internal interface defects

The red fault LED is flashing if at least one “internal interface defect” is present, which leads to one of the following alarm conditions:

- Circuit Pack Comm Failure
- Comm Channel Failure
- ONI Failure on protecting CTL
- ONI Failure on working CTL
- Protection Clock Input Fail
- TXI Failure
- Unit Cooling Degraded
- Unit Temperature too High
- Worker Clock Input Fail

DCN defects

The red fault LED is flashing if at least one “DCN defect” is present, which leads to one of the following alarm conditions:

- `MS DCC failure`
- `MS Protocol Version Mismatch`
- `RS DCC failure`
- `RS Protocol Version Mismatch`

Synchronization defects

The red fault LED is flashing if at least one “synchronization defect” is present, which leads to one of the following alarm conditions:

- `Loss of Synchronisation`
- `Timing Reference Failure`

Fan unit status indicators

LEDs on the fan unit

The fan unit is equipped with a red fault LED labelled “FAULT” and a green activity LED labelled “PWR ON”.

Both LEDs are located on the rear side of the fan unit, the “PWR ON” LED on the left-hand side next to the two power supply connectors, the “FAULT” LED on the right-hand side next to the controller interface connector.

Activity LED

The green “PWR ON” LED is lit as long as power is supplied to the fan unit by at least one of the two fan unit power feeders.

Fault LED

The red “FAULT” LED is lit if a Fan Failure alarm has been detected for the fan unit.

Otherwise, the red “FAULT” LED will be off.

Fan unit failure

None of the two fan unit LEDs will be lit in the case of a `Fan Unit Failure` (that is, when the fan unit has no power supply).

Office alarm interfaces

Overview

1675 LambdaUnite MSS network elements provide the following two alarm interfaces to support office alarm signaling:

- rack alarm interface,
- station alarm interface.

Rack alarm facility

Three rack alarm lamps are located at the front and at the rear side on the top of the rack to indicate critical, major and minor alarms:

The rack alarm lamps have the following meaning:

- The red “Critical” lamp is lit when there is at least one critical alarm present which has not been acknowledged yet by means of the alarm cut-off (ACO) function.
- The red “Major” lamp is lit when there is at least one major alarm present which has not been acknowledged yet by means of the alarm cut-off (ACO) function.
- The yellow “Minor” lamp is lit when there is at least one minor alarm present which has not been acknowledged yet by means of the alarm cut-off (ACO) function.

Rack alarm interface

Rack alarm signaling is controlled via the rack alarm interface on the connection interface for the controller (CI-CTL) at the rear side of a 1675 LambdaUnite MSS network element. The alarm interface provides individual alarm outputs for each of the three alarm severities “Critical”, “Major” and “Minor”. The rack-top alarm interface cable to connect the rack alarm lamps is prefabricated when ordered, please also refer to the *1675 LambdaUnite MSS Installation Guide*.

As there may be up to two network elements installed in a rack, the rack alarm lamps may be connected to more than one network element. In this case the sum of all alarms is indicated. The rack alarm lamps match the “CR”, “MJ” and “MN” LED display on the user panel if only one network element is connected.

Furthermore, by corresponding wiring it is also possible to realize a two-lamp arrangement to indicate prompt and deferred alarms.

Station alarm interface

A station alarm interface is located on the connection interface for the controller (CI-CTL) at the rear side of a 1675 LambdaUnite MSS network element to connect a central office alarm facility.

The station alarm interface provides alarm outputs for critical, major and minor alarms, both visual and audible.

Related information

Please also refer to the *1675 LambdaUnite MSS Installation Guide*.

Miscellaneous Discretes

Purpose

Miscellaneous Discrete Inputs (MDIs) and Miscellaneous Discrete Outputs (MDOs) make it possible to use a 1675 LambdaUnite MSS network element to monitor and control external equipment co-located with the system.

Miscellaneous Discrete Inputs (MDIs)

1675 LambdaUnite MSS systems feature a set of eight MDIs for user-defined applications.

MDIs can be used to *monitor* co-located equipment, a temperature sensor for example. MDIs can thus be used to trigger the reporting of application-specific environmental alarms.

Each MDI can be assigned a **Name** as well as a descriptive text (**Environment Message**) identifying the type of environmental alarm condition in more detail. The latter will be displayed in the *WaveStar*® CIT **NE Alarm List** when a corresponding alarm is present.

Furthermore, an **Environmental Alarm Inversion** parameter can be specified per MDI which indicates whether an alarm is to be generated if the relay associated to the MDI is switched on (activated) or if the relay is switched off (deactivated). A “door closed” contact for example would activate the connected MDI whereas it might be intended that an open door causes an alarm.

Miscellaneous Discrete Outputs (MDOs)

1675 LambdaUnite MSS systems feature a set of eight MDOs for user-defined applications. These MDOs are realized by means of relays (make contact, latching type).

MDOs can be used to *control* external equipment, an additional fan or an air conditioning system for example.

Modes of operation

MDOs can be operated in two different modes:

- Automatic mode
  The MDO has a probable cause assigned and becomes activated as soon as at least one alarm with this probable cause is active.
- Manual mode
  The MDO is not bound to an alarm but can be activated/deactivated manually.

Transient behaviour in the case of a restart or reset

As the MDOs are initialized to a hardware-defined state after a system restart (power up) or a full reset of the Controller (CTL), they may transiently indicate a wrong state until the state before the reset is reestablished.

Provisioning MDIs and MDOs

Please refer to the *1675 LambdaUnite MSS User Provisioning Guide* for information on how to provision MDIs and MDOs.

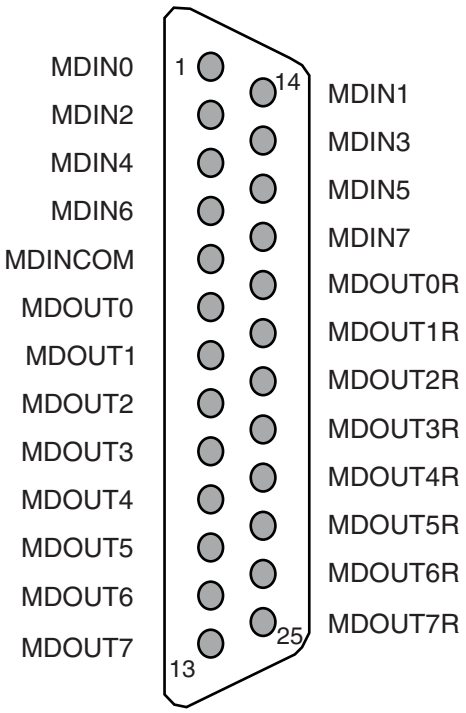
Electrical specifications

The MDI ports are sensitive to passive switches ($R_{on} \leq 50\ \Omega$, $R_{off} \geq 20\ k\Omega$) or input voltages up to –72 VDC (threshold voltage –3 V_{DC} to –10 V_{DC}) and are ESD safe up to 2 kV.

The MDO ports are capable to switch 500 mA at 72 V_{DC} and 2 A at 30 V_{DC} and are ESD safe up to 2 kV.

MDI/MDO interface

The MDI/MDO interface is realized on the CI-CTL (Connection Interface of the Controller) as a 25-pin female D-Subminiature connector. The pin assignment is as follows:



Pin assignment			Meaning
1		MDIN0	Miscellaneous Discrete Input 1
	14	MDIN1	Miscellaneous Discrete Input 2

Pin assignment			Meaning
2		MDIN2	Miscellaneous Discrete Input 3
	15	MDIN3	Miscellaneous Discrete Input 4
3		MDIN4	Miscellaneous Discrete Input 5
	16	MDIN5	Miscellaneous Discrete Input 6
4		MDIN6	Miscellaneous Discrete Input 7
	17	MDIN7	Miscellaneous Discrete Input 8
5		MDICOM	Miscellaneous Discrete Input - Common (connected to the negative polarity)
	18	MDOUT0R	Miscellaneous Discrete Output 1 - Return
6		MDOUT0	Miscellaneous Discrete Output 1
	19	MDOUT1R	Miscellaneous Discrete Output 2 - Return
7		MDOUT1	Miscellaneous Discrete Output 2
	20	MDOUT2R	Miscellaneous Discrete Output 3 - Return
8		MDOUT2	Miscellaneous Discrete Output 3
	21	MDOUT3R	Miscellaneous Discrete Output 4 - Return
9		MDOUT3	Miscellaneous Discrete Output 4
	22	MDOUT4R	Miscellaneous Discrete Output 5 - Return
10		MDOUT4	Miscellaneous Discrete Output 5
	23	MDOUT5R	Miscellaneous Discrete Output 6 - Return
11		MDOUT5	Miscellaneous Discrete Output 6
	24	MDOUT6R	Miscellaneous Discrete Output 7 - Return
12		MDOUT6	Miscellaneous Discrete Output 7
	25	MDOUT7R	Miscellaneous Discrete Output 8 - Return
13		MDOUT7	Miscellaneous Discrete Output 8

Orderwire and user bytes

Introduction

Each 1675 LambdaUnite MSS network element provides six user byte interfaces which can be used to access certain overhead bytes of an SDH/SONET transport signal. If desired, an external orderwire equipment can be connected to these interfaces for example.

User bytes can be provisioned by associating or disassociating an SDH/SONET port with a user byte interface.

Important! Orderwire and user byte access is supported by the following port units *only*:

- EP155
- OP155
- OPLB
- OP10

Overhead bytes

Each SDH/SONET port supports three dedicated user bytes:

- The *E1* byte in the SDH Regenerator Section Overhead (RSOH) or SONET Section Overhead (SOH) respectively can be used as a 64-kbit/s orderwire channel.
- The *E2* byte in the SDH Multiplex Section Overhead (MSOH) or SONET Line Overhead (LOH) respectively can be used as a 64-kbit/s orderwire channel.
- The *F1* byte in the SDH Regenerator Section Overhead (RSOH) or SONET Section Overhead (SOH) respectively can be used as a 64-kbit/s user data channel.

Each shelf provides user-provisionable access to up to six of these bytes from among the provisioned SDH/SONET ports in the shelf.

User byte interfaces

The six user byte interfaces are available on the CI-CTL (Connection Interface of the Controller; accessible from the rear side of the shelf).

There are four universal interfaces which can be used as G.703 co-directional or V.11 contra-directional interfaces. Two interfaces can be used as V.11 only.

These interfaces can be used as G.703 or V.11 interfaces:

- User byte interface #1 (USERBIO1)
- User byte interface #2 (USERBIO2)
- User byte interface #3 (USERBIO3)
- User byte interface #4 (USERBIO4)

These interfaces can only be used as V.11 interfaces:

- User byte interface #5 (USERBIO5)
- User byte interface #6 (USERBIO6)

Pin assignment

The pin assignment of the user byte interfaces is described in the *1675 LambdaUnite MSS Installation Guide*.

Related information

Please refer to the *1675 LambdaUnite MSS User Provisioning Guide* for information on how to configure orderwire and user bytes.

WaveStar® CIT

Graphical user interface

1675 LambdaUnite MSS is shipped with a GUI that runs on a customer-furnished desktop or laptop computer that fulfills the requirements below. The *WaveStar*® CIT (Craft Interface Terminal) is always delivered together with the respective NE software.

The GUI provides

- Control of operations, administration, maintenance and provisioning activities
- Security features to prevent unauthorized access
- Easy-to-use Transaction Language 1 (TL1) interface.

Definition

WaveStar® CIT is a PC-based GUI software handling the 1675 LambdaUnite MSS network elements one-by-one. It provides pull-down menus and extensive, context-sensitive on-line help. It offers a unified set of features for provisioning, testing, and reporting. The *WaveStar*® CIT is necessary to install and accept the system.

PC requirements

The minimum PC configuration to support the *WaveStar*® CIT application is described in the following table:

Pentium® 4, 1.3 GHz or higher recommended
Minimum 1 GByte of RAM
2 GB of free hard-disk drive space
CD-ROM drive (16× recommended)
CompactFlash® card device
SVGA card and monitor set to a minimum resolution of 1024 × 768 and 16 million colours
100BaseT LAN interface, installed and working
Adobe® *Acrobat*® *Reader*® for *Windows*® (version 3.01 or later)
One of the following operating systems: • *Microsoft*® *Windows*® XP • *Microsoft*® *Windows*® 7 It is *requested* to use an English *Microsoft*® *Windows*® version!
Up-to-date virus scanner software.

The performance of the user interface can be enhanced by using a higher-performance personal computer.

A shielded crossed Ethernet LAN cable (100BaseT) with 4-wire RJ-45 connectors is used for connecting the *WaveStar®* CIT to the NE.

WaveStar® CIT access

1675 LambdaUnite MSS supports local and remote access using a *WaveStar®* CIT. Remote access uses the DCC (data communications channel) or an external WAN connected to a 1675 LambdaUnite MSS LAN port.

Security function

1675 LambdaUnite MSS provides a security function to protect against unauthorized access to the *WaveStar®* CIT system functions (such as provisioning). Security is controlled through logins, passwords, and authorization levels for the system functions.

TL1 interface

You can use the GUI to manage all provisioning, testing, and report generation easily and intuitively, with the GUI handling the TL1 interface behind the scenes.

Maintenance and administrative activities

The *WaveStar®* CIT provides detailed information and system control of the following specialized local/remote maintenance and administrative activities:

- Provisioning
- Cross-connection assignments
- Protection switching
- Displaying performance-monitoring data
- Fault management (alarms lists, etc.)
- Polling inventory data of the NE
- Software download to the NE
- Loopback operation and testing
- Reporting.

Administration

Overview

Purpose

The system management function for the administration of 1675 LambdaUnite MultiService Switch (MSS) is operator administrated.

Security

The 1675 LambdaUnite MSS provides for secure system access by means of a two-tier security mechanism.

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Security

This section describes the various security features that the 1675 LambdaUnite MSS provides to monitor and control access to the system.

Two-tier security

The two tiers of security that protect against unauthorized access to the *WaveStar*® CIT and the network element functions are

- User login security (*WaveStar*® CIT)
- Network element login security (“System View”)

User login security

User login security controls access to the system on an individual user basis by means of

- Login ID and password assignment
- Login and password aging
- Autonomous indications and history records
- User privilege codes.

Network element login security

NE login security controls access to the system through a lockout mechanism to disable all but administrative logins.

Login and password assignment

To access the system, the user must enter a valid login ID and password. 1675 LambdaUnite MSS allows up to 500 login IDs and passwords. Two of these login IDs are for the Superuser authorization level. The others are for Privileged User, Maintenance, Reports Only, and General User authorization levels.

Login and password aging

The following aging processes provide additional means of monitoring and controlling access to the system:

- Login aging deletes individual logins if unused for a pre-set number of days or on a particular date (for example, for a visitor or for temporary access during installation)
- Password aging requires that users change passwords periodically.

Autonomous indications and history records

The system provides autonomous indications and history log records of successful and unsuccessful logins, as well as intrusion attempts for security audits.

User privilege codes

When a user is added to the NE, a separate user privilege code, which may include an authorization level, is assigned to that user for each of the functional categories, based on the type of work the user is doing. The user privilege codes may be accompanied by an authorization level represented by a number between 1 and 5, with 5 being the highest level of access. It is permissible to grant access to any combination of commands using a privilege code, except for full privileges, which are reserved for the two pre-installed superusers.

Functional categories

The functional categories for the user privilege codes may include

- Security (S)
- Maintenance (M)
- Performance monitoring (PM)
- Testing (T)
- Provisioning (P).

Authorization levels

Users can execute any commands at their functional categories' authorization level, as well as all commands at lower levels. For example, a user with authorization level 4 in the maintenance category can also execute commands listed in levels 3, 2, and 1 in the maintenance category.

Maintenance

Overview

Purpose

This section introduces the maintenance features available in the 1675 LambdaUnite MultiService Switch (MSS).

Definition

Maintenance is the system's capability to continuously monitor its equipment and the signals that it carries in order to notify the user of any current or potential problems. This enables the user to take appropriate proactive (preventive) or reactive (corrective) action.

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Maintenance signals

This section describes the maintenance signals available in 1675 LambdaUnite MultiService Switch (MSS).

Definition

1675 LambdaUnite MSS maintenance signals notify downstream equipment that a failure has been detected and alarmed by some upstream equipment (Alarm Indication Signal) or the 1675 LambdaUnite MSS, and they notify upstream equipment that a downstream failure has been detected (yellow signals).

Standards compliant

The fault monitoring and maintenance signals supported in the 1675 LambdaUnite MSS are compliant to ITU-T and *Telcordia*™ standards.

Monitoring failures

1675 LambdaUnite MSS continuously monitors its internal conditions and incoming signals. Read access to the path trace information is provided for all signals.

Signal maintenance

When defects are detected, the 1675 LambdaUnite MSS inserts an appropriate maintenance signal to downstream and/or upstream equipment.

Path unequipped

1675 LambdaUnite MSS inserts the Path Unequipped identifier to downstream and/or upstream equipment if paths are intentionally not carrying traffic.

Fault detection and reporting

When a fault is detected, 1675 LambdaUnite MSS employs automatic diagnostics to isolate the failed component or signal. Failures are reported to local maintenance personnel and to the OS so that repair decisions can be made. If desired, OS personnel and local personnel can use the *WaveStar*® CIT to gain more detailed information about a specific fault condition.

Fault history

All alarmed fault conditions detected and isolated by 1675 LambdaUnite MSS are stored and made available to be reported, on demand, through the *WaveStar*® CIT. In addition, a history of the 12000 most recent alarm events, of the 22000 most recent state change events and of the 2000 most recent database change events is maintained and available for on-demand reporting. Each event is date and time stamped.

Reports

1675 LambdaUnite MSS automatically and autonomously reports all detected alarm and status conditions through the

- Office alarm relays
- User panel
- Equipment LEDs
- Message-based OS.

Loopbacks and tests

This section describes the loopbacks and tests that the 1675 LambdaUnite MSS performs.

Loopback definition

A loopback is a troubleshooting test in which a signal is transmitted through a port unit to a set destination and then returned to the originating port unit. The transmitted and received signals are measured and evaluated by the user to ensure that the received signal is accurate and complete when compared to the originating signal.

Note that on ONNS I-NNI ports loopbacks are not allowed and therefore blocked by the system.

Software-initiated loopbacks

1675 LambdaUnite MSS can perform software-initiated facility loopbacks within the port units (near-end or in-loopbacks and far-end or out-loopbacks), as well as software-initiated cross-connection loopbacks. Active loopbacks are indicated by the abnormal (ABN) LED on the user panel.

Remote test access

The 1675 LambdaUnite MSS remote test access feature provides the possibility to access and survey specific traffic for testing purposes. You can select individual tributaries on different tributary rate levels and in different test access modes, depending on the configuration and cross connection type you want to observe.

Power on self-test

A Power ON Self Test (POST) is executed automatically after power up to verify correct system operation. This test consists of random access memory tests, of checksum tests and of specific tests for the hardware of the concerned unit they are performed in, such as controller pack, switching unit or transmission unit.

Additional diagnostic tests are performed for fault isolation, like for example bus, communication, temperature and voltage surveillance. These tests ensure that the system is capable of performing its required functions. If a defect is detected, the replaceable unit which should be replaced is identified by LEDs on the unit and by the alarm information displayed on the management software.

Circuit pack self-test

1675 LambdaUnite MSS supports a variety of self-tests designed to verify the health of individual transmission circuit packs.

Protection switching

This section describes the protection switching and redundancy mechanisms available in 1675 LambdaUnite MultiService Switch (MSS).

Definition

The following types of protection and redundancy are available (see Chapter 2, "Features"):

- 1+1 Linear APS, uni-directional and bi-directional, non-revertive and revertive , on all 10-Gbit/s, 2.5-Gbit/s, 622-Mbit/s, and 155-Mbit/s optical port types, compliant with ANSI T1.105.01
- 1+1 Multiplex Section Protection (MSP), uni-directional and bi-directional, non-revertive and revertive , on all 10-Gbit/s, 2.5-Gbit/s, 622-Mbit/s, and 155-Mbit/s, compliant with ITU-T Rec. G.841
- 1+1 Multiplex Section Protection (MSP), optimized bi-directional, non-revertive, on all 10-Gbit/s, 2.5-Gbit/s, 622-Mbit/s, and 155-Mbit/s optical port types, compliant with ITU-T Rec. G.841 including Annex B
- 1:1 Multiplex Section Protection (MSP), bi-directional, revertive, on all 10-Gbit/s, 2.5-Gbit/s, 622-Mbit/s, and 155-Mbit/s optical port types
- Bidirectional Line Switched Ring (BLSR), compliant with ANSI T1.105.01
- Multiplex Section Shared Protection Ring (MS-SPRing), compliant with ITU-T Rec. G.841
- Transoceanic Protocol on 4-fiber MS-SPRing with "TOP+EX" feature, refer to"Preemptible protection access" (p. 2-32)
- Uni-directional Path Switched Ring (UPSR) on all supported cross connection types, compliant with *Telcordia*™ GR-1400-CORE
- Sub-Network Connection Protection (SNCP) on all supported cross connection types, compliant with ETS 300417 and ITU-T Rec. G.783
- Mesh path protection (ONNS restoration), compliant with ITU-T Rec G.8080/Y.1301
- 1+1 redundancy for the electrical 155-Mbit/s interfaces is supported
- 1+1 redundancy for the electrical 45- /51-Mbit/s interfaces is supported
- 1+1 redundancy for the main switching unit with integrated timing generator (XC)
- 1+1 redundancy for the lower-order cross-connection unit (LOXC)
- 1+1 redundancy for the controller unit (duplex CTL)
- 1+1 redundancy for the portless TransMUX card (TMUX2G5/1)
- 1+1 redundancy for the power feed throughout the system (load sharing mode)
- DCC protection in combination with port protection (slaving) is supported.

Please note that the described transmission protection types are not applicable to ports configured as ONNS ports.

1+1 Line APS (SONET), 1+1 MSP and 1:1 MSP (SDH)

One physical working connection is protected by one physical stand-by connection. For the supported switching protocols refer to "Line protection" (p. 2-45).

BLSR (SONET), MS-SPRing (SDH)

A bidirectional line switched ring (BLSR) / multiplex section shared protection ring (MS-SPRing) is a self-healing ring configuration in which traffic is bidirectional between each pair of adjacent nodes and is protected by redundant bandwidth on the bidirectional lines that inter-connect the nodes in the ring.

UPSR (SONET), SNCP (SDH)

The principle of a UPSR/SNCP is based on the duplication of the signals to be transmitted and the selection of the best signal available at the subnetwork connection termination. The two (identical) signals are routed over two different path segments, one of which is defined as the main path and the other as standby path. The same applies to the opposite direction (bidirectional UPSR/SNCP). The system only switches to the standby path if the main path is faulty.

UPSR/SNCP is supported on all available cross-connection rates: from 1.5 Mbit/s up to 10 Gbit/s.

Mesh path protection (restoration)

Mesh path protection: ONNS enables end to end path protection. In case of a failure the "A" node, the node where the path set up was requested, starts a real time routing calculation to find an alternate route to restore the path. The routing is based on the availability of I-NNI ports. Once an alternate route is found ONNS establish the route and the traffic is restored.

Note that in the current release ONNS is not supported on the LOXC-switched transmission rates VT1.5, VC-12 and VC-3 (lower order).

Redundant switching unit

The switching matrix and the synchronization unit are located on the XC160/XC320/XC640 pack which is redundancy protected.

Duplex controller

The controller unit (CTL) is redundancy protected by a second CTL in the reserved CTL-P slot of the subrack (duplex CTL). The software and the configuration data is automatically distributed to the protection CTL, providing a memory back up.

Duplicated power feed

Power feed is duplicated throughout the system. Each circuit pack has its own DC/DC converter (distributed powering).

Performance monitoring

Performance Monitoring provides the user with the facility to systematically track the quality of a particular transport entity. This is done by means of continuous collection and analysis of the data derived from defined measurement points.

Basic measurement parameters

The following performance parameters are available to estimate the error performance of a section (SONET):

- ES (number of Errored Seconds in the received signal)
- SAS (A far-end SAS second is a second in which exclusively RDI (but no other defect) has been detected in the incoming signal.)
- SES (number of Severely Errored Seconds in the received signal)
- BBE (number of Background Block Errors in the received signal)
- CV (number of Code Violations in the received signal)
- SEFS (number of seconds during which the Severely Errored Framing defect was detected)
- LOSS (number of seconds during which the Loss of Signal defect was detected)
- UAS (number of Unavailable Seconds in the received signal)
- PJE (number of positive (PJE+) or negative (PJE-) pointer justification events counted over a one second interval.)
- FECC (accumulated number of detected and corrected FEC code violations per frame.)
- FC (number of times the incoming signal failed (AIS detected or inserted))
- AISS (number of seconds during which the AIS defect was detected)
- OR (number of Octets Received)
- OS (number of Octets Sent)

Overview of SDH performance parameters

The following overview shows the available SDH performance parameters and their association to the monitoring points Regenerator Section (RS), Multiplex Section (MS) and path (path termination and path monitoring at non-intrusive monitoring (NIM) points):

Parameter	Regenerator Section	Multiplex Section	Path
Near-end parameters			
ES	**RS-N-ES**	**MS-N-ES**	**VC-N-ES**
SES	**RS-N-SES**	**MS-N-SES**	**VC-N-SES**

Parameter	Regenerator Section	Multiplex Section	Path
BBE	**RS-N-BBE**	**MS-N-BBE**	**VC-N-BBE**
UAS	**RS-N-UAS**	**MS-N-UAS**	**VC-N-UAS**
PJE	–	**PJE+** / **PJE-**	–
FECC	–	**MS-FECC**	–
Far-end parameters			
ES	–	**MS-F-ES**	**VC-F-ES**
SES	–	**MS-F-SES**	**VC-F-SES**
BBE	–	**MS-F-BBE**	**VC-F-BBE**
UAS	–	**MS-F-UAS**	**VC-F-UAS**

Overview of SONET performance parameters

The following overview shows the available SONET performance parameters and their association to the monitoring points Section, Line and path (path termination and path monitoring at non-intrusive monitoring (NIM) points):

Parameter	Section	Line	Path
Near-end parameters			
CV	**CV-S**	**CV-L**	**CV-P**
ES	**ES-S**	**ES-L**	**ES-P**
SES	**SES-S**	**SES-L**	**SES-P**
UAS	–	**UAS-L**	**UAS-P**
FC	–	**FC-L**	**FC-P**
LOSS	**LOSS-S**	–	–
SEFS	**SEFS-S**	–	–
AISS	–	**AISS-L**	–
PJE	–	–	**PJE+** / **PJE-**
FECC	–	**FECC-L**	–
Far-end parameters			
CV	–	**CV-LFE**	**CV-PFE**
ES	–	**ES-LFE**	**ES-PFE**
SES	–	**SES-LFE**	**SES-PFE**
UAS	–	**UAS-LFE**	**UAS-PFE**
FC	–	**FC-LFE**	**FC-PFE**

Overview of DS3 performance parameters

The following overview shows the available DS3 performance parameters and their association to the monitoring points DS3 Line, DS3 path in the incoming direction (EP51 → XC), and DS3 path in the outgoing direction ("egress", XC → EP51):

Parameter	DS3 Line	DS3 path	
		Incoming direction (ingress)	Outgoing direction (egress)
Near-end parameters			
CV	**CV-L**	**CV-P**	**CV-P-EGR**
ES	**ES-L**	**ES-P**	**ES-P-EGR**
ESA	**ESA-L**	**ESA-P**	**ESA-P-EGR**
ESB	**ESB-L**	**ESB-P**	**ESB-P-EGR**
SES	**SES-L**	**SES-P**	**SES-P-EGR**
UAS	–	**UAS-P**	**UAS-P-EGR**
FC	–	**FC-P**	**FC-P-EGR**
LOSS	**LOSS-L**	–	–
SEFS	–	**SEFS-P**	**SEFS-P-EGR**
SAS	–	**SAS-P**	**SAS-P-EGR**
AISS	–	**AISS-P**	**AISS-P-EGR**
Far-end parameters			
CV	–	**CV-PFE**	**CV-PFE**
ES	–	**ES-PFE**	**ES-PFE**
ESA	–	**ESA-P**	**ESA-P-EGR**
ESB	–	**ESB-P**	**ESB-P-EGR**
SES	–	**SES-PFE**	**SES-PFE**
UAS	–	**UAS-PFE**	**UAS-PFE**
FC	–	**FC-PFE**	**FC-PFE**
SAS	–	**SAS-PFE**	**SAS-PFE-EGR**

Overview of Ethernet performance parameters

The following overview shows the available Ethernet performance parameters and their association to LAN ports (Ethernet ports) and WAN ports (VCG ports):

Parameter	LAN port	WAN port
GE1 units		

Parameter	LAN port	WAN port
CBR	**CBR**	**CBR**
CBS	**CBS**	**CBS**
PDE	**PDE**	**PDE**
Gigabit Ethernet units other than GE1		
EINB	**EINB**	**EINB**
EONB	**EONB**	**EONB**
EINF	**EINF**	**EINF**
EONF	**EONF**	**EONF**
EDFE	**EDFE**	**EDFE**
EDFC	**EDFC**	**EDFC**

Overview of performance parameters for transparent services

The following overview shows the available performance parameters for transparent services:

Parameter	Near-end	Far-end
ES	**ODU-N-ES**	**ODU-F-ES**
SES	**ODU-N-SES**	**ODU-F-SES**
BBE	**ODU-N-BBE**	**ODU-F-BBE**
UAS	**ODU-N-UAS**	**ODU-F-UAS**

Overview of Optical Channel (OCh) performance parameters

The following overview shows the available Optical Channel (OCh) performance parameters for OTU2 ports:

Parameter	Near-end	Far-end
FECC	**FECC**	-
LOSS	**LOSS**	-
BBE	-	-
SES	-	-
UAS	-	-

Enabling performance measurement points

Performance measurement points can be enabled via the management systems *Navis*®OMS and via the *WaveStar*® CIT. Please refer to the *1675 LambdaUnite MSS User Provisioning Guide* or the respective management system documentation.

Data storage

All data is stored in the current bin. The managed NE has a current data register (current bin) for 15 minutes and 24 hours. Once a termination point for measurements has been configured, you are able to get a snapshot view of the data gathered at any time (default).

Historic bins

The network element keeps a store of the historic 15 minute and 24 hour bins.

Interval	Number of historic bins	Total storage time
15 minute	32	8 hours
24 hours	1	1 day

Data retrieval

Performance Data can be polled via the *Navis*®OMS and via the *WaveStar*® CIT.

Reports

Via *Navis*®OMS the user is able to create reports from history data stored in the database of the network management system.

Zero suppression

Performance data sets with counter value zero, i.e. no errors occurred, will not be stored in the performance data log.

Threshold crossing alerts (TCAs)

1675 LambdaUnite MSS supports threshold reports (TRs), also called threshold crossing alerts (TCAs) on a per performance parameter basis by using TCA profiles. The TCA profiles are used to store the threshold values of the performance parameters related to a specific parameter group (for example parameters related to the SONET Section or Line, or parameters related to Ethernet ports).

When thresholding is activated for a performance parameter, the value of the parameter is compared aiganst the threshold value on a second by second basis. When the current counter value equals or exceeds the threshold value, then a threshold crossing alert will be reported as an event notification with a resolution of one second.

As threshold crossing alerts are events, they are stored in the network element event log, and displayed in the list of TCA events.

A report can be generated and displayed on *Navis*® OMS and *WaveStar*® CIT.This feature complies with *Telcordia*™ GR253-CORE (2000) and ITU-T G.784 and G.826.

Types of TCA profiles

TCA profiles exist for these groups of performance parameters:

- Parameters related to the SDH Regenerator Section and Multiplex Section
- Parameters related to the SONET Section and Line
- Parameters related to SDH and SONET tributaries (including VCG tributaries)
- Parameters related to DS3 ports
- Parameters related to Ethernet and VCG ports
- Parameters related to transparent services (Optical Data Unit, ODU1)

A default profile is predefined for each of these TCA profile types, but TCA profiles of these types can also be newly created, modified or deleted.

The threshold settings of an existing TCA profile can be reset to default settings.

This applies to TCA profiles associated with one of the following performance parameter groups:

- SDH Regenerator and Multiplex Section
- SONET Section and Line
- SDH path:
  - higher-order path
  - lower-order path
- SONET path:
  - higher-order path
  - lower-order path
- Ethernet
- Transparent services (Optical Data Unit, ODU1)
- DS3
- DS1

Fault localization

Performance alarms give only a hint that the signal quality at a certain measurement point is degraded. They can be used as a help for fault localization. The severity of such an alarm is strongly dependent on the application of your network. Often it can be helpful to define a very low threshold value in order to realize a signal degradation at a very early stage .

Clearing

The clearing of the alarms is done automatically at the end of the first complete interval during which no threshold crossing occurred.

Reports

This topic contains information about the

- Active alarms and status reports
- Performance monitoring reports
- History reports
- Report on circuit pack, slot, port and switch states
- Version/equipment list
- Synchronization reports

Active alarms and status reports

1675 LambdaUnite MSS provides an on-demand report (*WaveStar*® CIT NE Alarm List) that shows all the active alarm and status conditions. 1675 LambdaUnite MSS automatically displays the local alarm and status report on the *WaveStar*® CIT. The *WaveStar*® CIT can be configured to show the following alarm levels and alarm conditions: Either

- Critical (CR)
- Major (MJ)
- Minor (MN)
- Not Alarmed (status) (NA)

or

- Prompt
- Deferred
- Info

Among others, the alarm issue point and a description of each alarm condition are included in the report along with the date and time detected. The report also indicates whether or not the alarm is service-affecting.

Additionally the status “abnormal condition” is displayed on the user panel and by the *WaveStar*® CIT, if at least one of the following is true:

- the system is in maintenance condition
- the system timing is set to free running
- there is a loop back active
- there is forced switch active.

Performance monitoring reports

1675 LambdaUnite MSS provides reports that contain the values of all performance monitoring registers requested at the time of the report. The start time of each register's recording period is also included. The reports provide all performance monitoring data that was recorded in a series of 15-minute and 24-hour storage registers.

Performance parameters report

1675 LambdaUnite MSS provides another report that contains a summary of all performance parameters that have crossed their provisioned 15-minute or 24-hour thresholds within the history of the 15-minute and 24-hour registers.

A series of 32 previous and one current 15-minute registers are provided for each parameter, allowing for up to 8 hours and 15 minutes (495 minutes) of history in 15-minute registers. Also, one current and one previous 24-hour registers are provided, allowing for up to 2 days (48 hours) of history in 24-hour registers.

Report on pack, slot, port and switch states

This on-demand report displays

- Circuit pack, transmission port, and timing port state information
- Protection group switch states.

Version/equipment list

The version/equipment list report is an on-demand report that lists all

- Provisioned or pre-provisioned circuit packs
- Circuit packs that are present.

Synchronization report

The synchronization report is an on-demand report that lists the system synchronization status.

Maintenance condition

Maintenance condition

The maintenance condition (or “maintenance mode”) is an exceptional mode of operation in order to handle error conditions the system cannot autonomously manage.

The maintenance condition is characterized as follows:

- Write access to the active configuration database (NVM) is restricted to those data related to the limited set of operations that are allowed in maintenance mode.

  Furthermore, only those autonomous state change events (i.e. also notifications) are allowed that do not change the active configuration database (NVM).
- Only a limited set of operations is allowed:
  - Changing the NE name (*only* possible in maintenance mode).
  - Setting the NE's date and time.
  - Changing the default setting for the NE's synchronization mode (*only* possible in maintenance mode).
  - Changing the default setting for the optical interface standard.
  - Changing the default setting for the tributary operation mode.
  - Setting the maximum switch capacity of the system.
  - Setting an IP address and the IP subnet mask, both for IP access on LAN, and for the SCN (*only* possible in maintenance mode).
  - Setting the IP default router address and LAN port.
  - Setting the T-TD raw mode and length value port.
  - Setting the LAN status (general, OSI, IP), designated router, OSI node, and TARP LAN storm suppression (TLSS) of LANs.
  - Restoring a database (combined database download and activation; *only* possible in maintenance mode)
- Only a limited set of operations is allowed:
  - Changing the NE name (*only* possible in maintenance mode).
  - Setting the NE's date and time.
  - Changing the default setting for the NE's synchronization mode (*only* possible in maintenance mode).
  - Changing the default setting for the optical interface standard.
  - Changing the default setting for the tributary operation mode.
  - Setting the maximum switch capacity of the system.
  - Setting an IP address and the IP subnet mask, both for IP access on LAN, and for the SCN (*only* possible in maintenance mode).
  - Setting the IP default router address and LAN port.

- Setting the T-TD raw mode and length value port.
- Setting the LAN status (general, OSI, IP), designated router, OSI node, and TARP LAN storm suppression (TLSS) of LANs.
- Restoring a database (combined database download and activation; *only* possible in maintenance mode)
- Enabling or disabling the enhanced mode of SA/NSA alarm classification for ports that are involved in a UPSR path protection (*only* possible in maintenance mode).
- Setting the delay for the transition of the port monitoring mode from “Auto” to “Monitored” (port mode timer).
- Retrieving data from the active configuration database (NVM).
- Leaving (terminating) the maintenance mode (*only* possible in maintenance mode).

The maintenance mode can either be entered manually to perform operations that are only possible in maintenance mode, such as changing the NE name (TID) for example, or autonomously by the system when an exceptional situation occurred.

Orderwire

This section provides information about orderwire.

Description

Engineering Orderwire (EOW) provides voice or data communications for maintenance personnel to perform facility maintenance. 1675 LambdaUnite MultiService Switch (MSS) provides three channels of 64-kbit/s (E1, E2 and F1) on the 10-Gbit/s- and 155-Mbit/s interfaces for orderwire applications, for example:

- Local Orderwire (SONET) / Regenerator Section Orderwire (SDH)
- Express Orderwire (SONET) / Multiplex Section Orderwire (SDH).

Provisioning

Overview

Purpose

This section contains information about the following features:

- Local or remote provisioning
- Preprovisioning circuit packs
- Circuit pack replacement provisioning
- Original value provisioning

Definition

Provisioning refers to assigning values to parameters used for specific functions by network elements. The values of the provisioned parameters determine many operating characteristics of a network element.

References

For more information about provisioning parameters and original values using the *WaveStar*® CIT, refer to the *1675 LambdaUnite MSS User Provisioning Guide*.

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Introduction

Local or remote provisioning

The 1675 LambdaUnite MSS software allows local and remote provisioning of all user-provisionable parameters. The provisionable parameters and values (current and original) are maintained in the nonvolatile memory of the controller circuit pack.

Preprovisioning circuit packs and SFPs

To simplify circuit pack installation, parameters can be provisioned before inserting the corresponding circuit pack or SFP. The appropriate parameters are automatically downloaded when the corresponding circuit pack or SFP is installed. All system parameters and values (current and original) are retrievable on demand regardless of the means used for provisioning.

Circuit pack replacement provisioning

Replacement of a failed circuit pack is simplified by the 1675 LambdaUnite MSS automatic provisioning of the original circuit pack values. The controller circuit packs maintain a provisioning map of the current provisioning values. When a transmission and/or a timing circuit pack is replaced, the controller automatically downloads the previous provisioning parameters to the new circuit pack.

Original value provisioning

Installation provisioning is minimized with factory-preset values. Each provisionable parameter is assigned an original value at the factory. The provisionable parameters are automatically set to their original values during installation.

There are two complete sets of data (parameters and their values) located in the nonvolatile memory of the controller circuit pack under normal conditions:

- The first set contains the system parameters and their original values (values assigned to a parameter at the factory).
- The second set contains the system parameters and their current values (values currently being used by the system).

Please note that the original values assigned at the factory cannot be changed. However, the current values can be overridden through local or remote provisioning.

6 System planning and engineering

Overview

Purpose

This chapter provides general System Planning and Engineering information for 1675 LambdaUnite MultiService Switch (MSS).

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General planning information

This section provides general planning information for 1675 LambdaUnite MSS.

Planning considerations

When planning your network, you should consider the

- Power planning
- Cooling Equipment
- Transmission capacity
- Port location rules
- Synchronization
- Floor plan layout
- Equipment interconnection.

Engineering and installation services group

Alcatel-Lucent maintains an Engineering and Installation Services group to assist you in planning and engineering a new system. The Engineering and Installation Services group is a highly skilled force of support personnel dedicated to providing customers with quality engineering and installation services. These specialists use state-of-the-art technology, equipment, and procedures to provide customers with highly competent, rapid response services.

For more information about the Engineering and Installation Services group, refer to Chapter 8, “Product support”.

Intended use

This equipment shall be used only in accordance with intended use, corresponding installation and maintenance statements as specified in this documentation. Any other use or modification is prohibited.

Electrical interface safety

NOTICE

Service-disruption hazard

The electrical intra-building ports of 1675 LambdaUnite MSS system are suitable for connection to intra-building or unexposed wiring or cabling only. The intra-building ports of the equipment or subassembly must not be metallically connected to interfaces which connect to the outside plant (OSP) or its wiring.

These interfaces are designed for use as intra-building interfaces only and require isolation from the exposed OSP cabling. The addition of primary protectors is not sufficient protection in order to connect these interfaces metallically to OSP wiring.

This warning applies to the following interfaces:

- 155-Mbit/s intra-office interface for electrical STM-1 signals (EP155)
- 51-Mbit/s intra-office interface for electrical EC1 signals (EP51/EL36)
- 45-Mbit/s intra-office interface for electrical DS3 signals (EP51/EL36)
- DS3 test access interface (TMUX2G5/1)

Power planning

This section provides general power planning information for 1675 LambdaUnite MSS.

System power consumption

The power consumption of 1675 LambdaUnite MSS depends on its configuration (160-Gbit/s, 320-Gbit/s, or 640-Gbit/s configuration) and on its equipage.

The maximum power consumption (fully equipped with the most consuming cards) is as follows:

- 1900 W for the 160-Gbit/s configuration
- 3345 W for the 320-Gbit/s configuration
- 3500 W for the 640-Gbit/s configuration.

For more information about power consumption of the individual circuit packs, refer to "Weight and power consumption" (p. 10-40).

For equipment heat release information, please refer to "Heat release" (p. 6-6).

Power distribution

The power supply of the rack is provided by the Power Distribution Panel (PDP) at the top of the rack. This PDP provides doubled power supply to the subrack.

The subrack uses a distributed powering system, rather than bulk power supplies. The system power supply is able to provide 3500 W power in the range -40 V to -72 V DC, respectively -48 V to -60 V DC nominal. Each circuit pack uses its own onboard power converter to derive the necessary operating voltages.

Dual power feeds, power interfaces (PI)

Office power feeders A and B are filtered and protected by circuit breakers at the input to the subrack. This is done by the PI units, one unit is assigned to each power feeder. The supplies are distributed separately to each circuit pack, where they are filtered again and fused before being converted to the circuit pack working voltages. For the main over-current protection of the system a centralized circuit breaker located in the PI is used.

The circuit breaker specification depends on the power interface type:

- 63 A for the core assembly kit 1 power interface (PI - PBH1)
- 100 A for the core assembly kit 2 power interface (PI/100 - PBH3)

The A and B power inputs are supervised individually by the system controller circuit pack (CTL). A green LED on every Power Interface (PI) indicates that the input power is available which means that it is above -39.0 V ± 1.0 V. As soon as the input voltage is below 39.0 V ±1. 0 V, an alarm message will be send to the CTL.

Grounding

The grounding and earthing of the system covers the requirements for MESH-BN and MESH-IBN according to ETSI 300 253 or ITU K.27. With the PDP it is possible to connect or to disconnect the DC returns to GRD. At this way, the system can be applied in a MESH-BN or MESH-IBN environment.

Cooling equipment

This section provides general cooling equipment information for 1675 LambdaUnite MSS.

Fan units

Cooling is done by fans. 4 fans are located in the fan unit above the upper row of boards in the Dual Unit Row (DUR) subrack. They aspirate air through a filter located below the lower row of boards and force the air through the subrack from bottom to top.

Air flow baffle

An air flow baffle is integrated in the subrack to prevent the fan unit from drawing in the exhaust air from the subrack below.

Mounting the subrack allow no gaps between the baffle mounted below the 1675 LambdaUnite MSS subrack and any equipment mounted directly below. Observing this rule avoids thermic stress due to hot exhaust air from the equipment below the subrack entering the air flow baffle.

Heat release

Important! As 1675 LambdaUnite MSS is a highly integrated system, the heat release may exceed the objective values recommended by NEBS GR-63-core, Sec. 4.1.4, depending on the system equipage and on the rack configuration.

Note: To cope with high heat release, aisle spacings may be increased and/or restriction to 1 NE per rack has to be taken into consideration. In the case of excessive heat release special equipment room cooling may be required.

References

For more information about cooling, please refer to “Cooling” (p. 4-58) and to the *1675 LambdaUnite MSS Maintenance and Trouble-clearing Guide*.

Environmental conditions

Environment

Compliant with EN300 019-1-3 for Class 3.1 Environment “Stationary use at weather protected locations” and *Telcordia*™ GR-63 (Bellcore):

	Temperature range	Humidity
Normal operation	+5°C to +40°C	up to 85%
Short term operation	-5°C to +50°C	up to 90% (conditions last at most 72 hours per year during at most 15 days)
Storage	-25°C to +55°C	up to 100%

EMC

1675 LambdaUnite MSS meets the emissions requirement as per FCC 47 CFR part 15 Subpart B for class A computing device.

The equipment described in this manual has been tested and found to comply with the limits for a Class A product. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and must be installed and used in accordance with the *1675 LambdaUnite MSS Installation and System Turn-up Guide*. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

1675 LambdaUnite MSS is compliant with EN300 386-2: “EMC requirements for Public Telecommunication Network Equipment”, IEC 61000-4-x series (immunity) and *Telcordia*™ GR-1089-core (emission and immunity).

Radiated emission	EN 55 022 Class A GR-1089-core chapter 3
Conducted emission	DC-power, ETS 300 386-1, 20 kHz - 30 MHz (corresponds with EN 55022 class A) Telecom. Ports, CISPR 22 Amd, Class B GR-1089-core
Electro-static discharge	IEC 61000-4-2, tested at level 4 (contact discharge 8 kV, air 15 kV; NEBS level 3 requirement) GR-1089-core chapter 2
Radiated immunity	IEC 61000-4-3, tested at level 3 GR-1089-core
Electrical fast transients	DC Power, IEC 61000-4-4 (tested at level 1, 0.5 kV) Telecom. Ports, IEC 61000-4-4 (tested at level 1, 0.5 kV) There is no requirement regarding G-1089 but there are objectives in GR513 (O4-21) for power ports. GR-1089-core

Surges	IEC 61000-4-5, tested at level 1 (0.5 kV with performance criterion B and additional 0.8 kV (series resistor 6 Ω) and 1.5 kV (series resistor 12 Ω) the system shall not be damaged and shall continue to operate. Indoor Telecom. Ports, ETS 300 386-1, Tested at 0.5 kV GR-1089-core ITU K.41
Continuous wave	IEC 61000-4-6 DC Power, IEC 61000-4-6 (tested at level 3) Telecom. Ports, IEC 61000-4-6 (tested at level 3) GR-1089-core
Compliant with LVD	EN 60950
NEBS L3 compliance	The subrack and all circuit packs comply with NEBS Level 3.
CE Certification	CE compliant with European Directive 89/336/EEC

Building requirements for 1675 LambdaUnite MSS operation

1675 LambdaUnite MSS is designed for areas with restricted access, in particular:

- For central office (CO) applications according to *Telcordia*™ GR-1089-CORE, section 1.1 and GR-63-CORE, section 1.1,
- For telecommunication centres according to ETS 300 019-1-3, section 4.1.

For equipment heat release information, please refer to “Heat release” (p. 6-6).

Transmission capacity

This section provides general information about transmission capacity for 1675 LambdaUnite MSS.

Capacity

The 1675 LambdaUnite MSS Dual Unit Row sub-rack (DUR) provides 160 Gbit/s, 320-Gbit/s respectively 640 Gbit/s switching capacity, depending on the switching units applied. This allows you to equip the subrack with various circuit packs (port units and portless units) for different types of interfaces as summarized in "Circuit packs" (p. 4-14).

Circuit pack capacities

This section summarizes the transmission and switching capacities per circuit pack type.

Port units transmission capacities

The following table lists the transmission capacity provided by each port and per circuit pack.

Circuit pack		Max. number of STS-1 equivalents		Max. number of STM-1 equivalents	
		per port	per circuit pack	per port	per circuit pack
10-Gbit/s synchronous and Ethernet WAN (2 or 1 port per unit)		192	384	64	128
2.5-Gbit/s synchronous (8, 4 or 2 ports per synchronous, 3 ports per transparent unit)		48	384	16	128
622-Mbit/s synchronous (16 or 8 ports per unit)		12	192	4	64
155-Mbit/s synchronous (16 or 8 ports per OP155M, 8 ports per EP155)		3	48	1	16
45- /51-Mbit/s synchronous (36 ports per EP51)		1	36	(1/3)	(12)
1-Gbit/s Ethernet (4 ports per unit)		21	84	7	28
1/10-Gbit/s Ethernet	1 × 10 Gbit/s	192	192	64	64
	8 × 1 Gbit/s	21	168	7	56

Portless units transmission capacities

The portless units use the following transmission capacities:

Circuit pack	Transmission capacity
Lower-order cross-connection unit (LOXC/1)	15 Gbit/s
Lower-order cross-connection unit (LOXC40G2S/1)	40 Gbit/s
Lower-order cross-connection unit (LOXC40G3S/1)	40 Gbit/s
Portless TransMUX card (TMUX2G5/1)	5 Gbit/s

References

For more information about transmission capacity, please refer to Chapter 4, “Product description”.

Port location rules

This section provides an overview of configuration restrictions and recommendations about using circuit packs and slots efficiently.

160-Gbit/s configuration

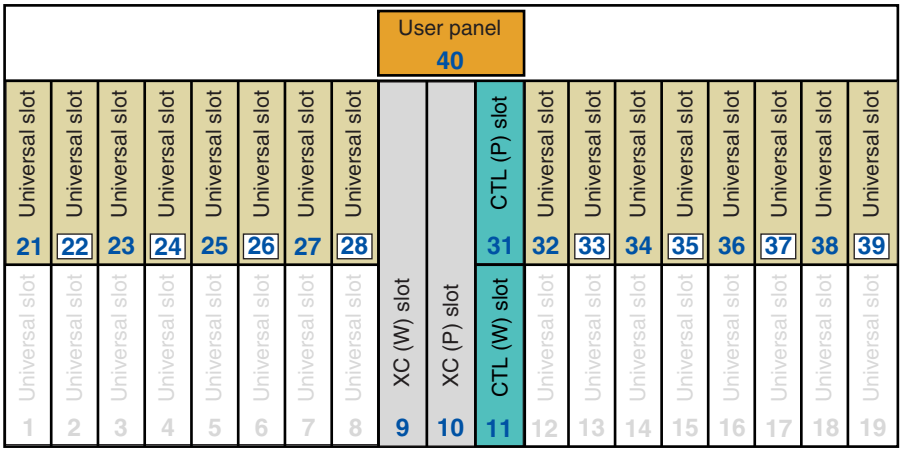
In 160-Gbit/s configurations the following rules and guidelines apply:

1. The maximum switching capacity of the system is 160 Gbit/s (3072 × 3072 VC-3/STS-1; 1024 × 1024 VC4).
2. Each type of cross-connection and timing unit (XC160, XC320, XC640) can be provisioned in slot 9 and slot 10. However, the maximum switching capacity that can be used is 160 Gbit/s, and the slot equipage rules as described above apply independent of which cross-connection and timing unit is used.
3. Port units can only be used in the upper row of the DUR shelf, i.e. in the universal slots 21 … 28 and 32 … 39.

   When a port unit is installed in any of the remaining universal slots, then the green activity LED of that port unit will be flashing, and a `Circuit Pack Type Mismatch` alarm will be reported. An attempt to preprovision a port unit for any of these slots will be denied.

   Cover all unequipped slots with blank front plates.
4. Port units with a transmission capacity of 20 Gbit/s (for example an OP2G5D/PAR8 parent board, equipped with 8 optical modules) can only be used in the universal slots 22, 24, 26, 28, 33, 35, 37, and 39. The slot left to a slot where such a port unit is installed has to remain unequipped, i.e. cannot be used for other applications. Please also refer to the diagram subsequent to this list.
5. Lower-order cross-connection units of type LOXC/1 can be used in the universal slots 37 (worker slot) and 39 (protection slot). The slot left to an LOXC/1 has to remain unequipped, i.e. cannot be used for other applications.
6. Lower-order cross-connection units of type LOXC40G2S/1 *cannot* be used in a system with a maximum switching capacity of 160 Gbit/s. However, the universal slot pairs 36/37 and 38/39 *can* be used for lower-order cross-connection units of type LOXC40G2S/1 (2 slots wide) after the system has been upgraded to a maximum switching capacity of 640 Gbit/s (XC in-service upgrade XC160 → XC640).
7. Lower-order cross-connection units of type LOXC40G3S/1 *cannot* be used in a system with a maximum switching capacity of 160 Gbit/s. However, the universal slots 2/3/4 and 17/18/19 *can* be used for lower-order cross-connection units of type LOXC40G3S/1 (3 slots wide) after the system has been upgraded to a maximum switching capacity of 320 Gbit/s (XC in-service upgrade XC160 → XC320).

8. XC160 cross-connection and timing units support ONNS applications (cf. "LXC supporting ONNS applications" (p. 6-16)).
9. It is recommended to use *two* XC160 cross-connection and timing units. Thus, both the cross-connection as well as the timing function are automatically 1+1 equipment protected. However, an LXC configuration with a single, but unprotected XC160 cross-connection and timing unit is also possible.



front view

320-Gbit/s configuration

In 320-Gbit/s configurations the following rules and guidelines apply:

1. The maximum switching capacity of the system is 320 Gbit/s (6144 × 6144 VC-3/STS-1; 2048 × 2048 VC-4).
2. Only XC320 or XC640 cross-connection and timing units can be provisioned in slot 9 and slot 10. However, the maximum switching capacity that can be used is 320 Gbit/s, and the slot equipage rules as described above apply independent of whether an XC320 or XC640 is used.

   When an XC160 cross-connection and timing unit is installed in slot 9 or slot 10, then the green activity LED of that XC160 will be flashing, and a `Circuit Pack Type Mismatch` alarm will be reported.
3. Port units can be used in *all* universal slots.

   However, port units with a transmission capacity of 20 Gbit/s (for example an OP2G5D/PAR8 parent board, equipped with 8 optical modules) can only be used in the universal slots 2, 4, 6, 8, 13, 15, 17, 19, 22, 24, 26, 28, 33, 35, 37, and 39. The slot left to a slot where a 20-Gbit/s port unit is installed has to remain unequipped, i.e. cannot be used for other applications. Please also refer to the diagram subsequent to this list.

   Cover all unequipped slots with blank front plates.
4. Lower-order cross-connection units:

If lower-order traffic (VT1.5, VC-12, or lower-order VC-3) is to be switched, a lower-order cross-connection unit (LOXC) must be installed in dedicated universal slots or slot combinations.

- Lower-order cross-connection units of type LOXC/1 (occupying 1 slot, with a switching capacity of 15 Gbit/s) can be used in the universal slots 4, 17, 18, 19, 37, and 39. The slots 4, 17, 18 and 37 are worker slots. The slots 19 and 39 are protection slots. If the worker LOXC/1 is installed in slot 4, 17, or 18, then the protection LOXC/1 must be installed in slot 19. If the worker LOXC/1 is installed in slot 37, then the protection LOXC/1 must be installed in slot 39.
- Lower-order cross-connection units of type LOXC40G3S/1 (3 slots wide, with a switching capacity of 40 Gbit/s) can be used in the universal slot 4 and 19 (worker LOXC40G3S/1 in the slots 2/3/4, protection LOXC40G3S/1 in the slots 17/18/19)

  The slot left of the LOXC40G3S/1, that means slot 1 and slot 16, cannot be used and must remain empty.

Please note that *at most one LOXC equipment protection group may exist, i.e. at most one worker slot can be used in combination with one protection slot* (please also refer to the *1675 LambdaUnite MSS User Provisioning Guide*).

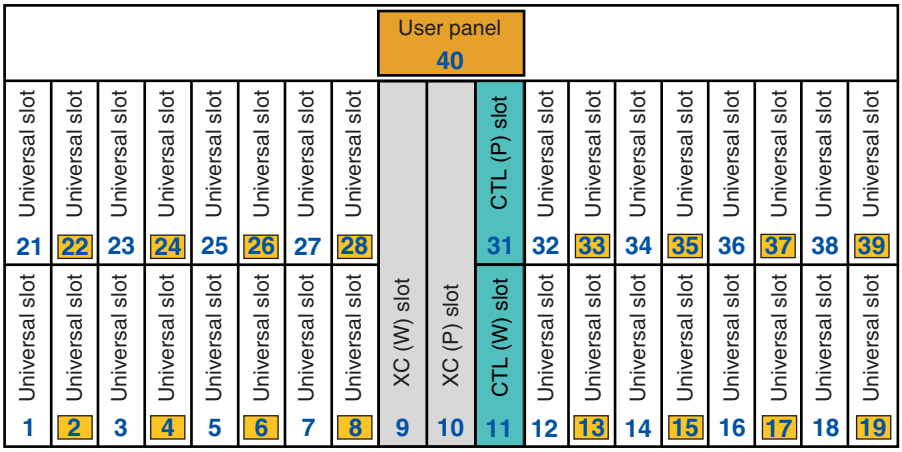
5. The Portless TransMUX unit (TMUX2G5/1), which occupies 1 slot, can be provisioned in any universal slot of the system. However, please note that the TMUX2G5/1 unit can only be used in combination with lower order cross-connection units of type LOXC40G2S/1 and LOXC40G3S/1. Operation in combination with LOXC/1 is not supported. This implies that the TMUX2G5/1 can only be used in NEs equipped with Controllers of type CTL/3T or CTL/3S. NEs equipped with any other type of Controller do not accept the provisioning of a TMUX2G5/1 unit.

   While configuring the system, consider the impact of the Portless TransMUX unit (TMUX2G5/1) on the lower-order switching capacity of the LOXC40. The traffic being transmultiplexed by 12 Portless TransMUX unit and being forwarded to LOXC40 allocates 30 Gbit/s (12 × 48 STS1) from the available backplane capacity of the LOXC40. Therefore only 10 Gbit/s are available for VT1.5 connections from LOXC40 towards I/O packs. Each DS3 filled to 100 % of its capacity and connected to a TMUX2G5/1 costs 2.5 Gbit/s of the LOXC40 switching capacity per direction, or 5 Gbit/s in sum. As a result, eight fully used TMUX2G5/1 circuit packs would cost a switching capacity of 8 × 2 × 2.5 Gbit/s = 40 Gbit/s and thus completely use the LOXC40.

   The full transmultiplexed bandwidth of 12 TMUX2G5/1 pairs can only be crossconnected completely if it is sent back to TMUX2G5/1 packs again (sort of DS1-crossconnect emulation). For applications where transmultiplexed traffic is handed off as VT1.5 over an I/O pack and all DS1s of DS3 signals are used and have to be crossconnected, no more than eight TMUX2G5/1 pairs can be handled.

   The full number of 12 active TMUX2G5/1 (or 12 pairs) in the system can only be used if the DS3s transport DS1s up to around 30% of their capacity (average value).

6. Only the XC320/B variant of the XC320 cross-connection and timing units supports ONNS applications (cf. "LXC supporting ONNS applications" (p. 6-16)).
7. It is recommended to use *two* XC320 cross-connection and timing units. Thus, both the cross-connection as well as the timing function are automatically 1+1 equipment protected. However, an LXC configuration with a single, but unprotected XC320 cross-connection and timing unit is also possible.



front view

640-Gbit/s configuration

These rules and guidelines apply when the **Maximum Switch Capacity** of the system is set to **LXC640**:

1. The maximum switching capacity of the system is 640 Gbit/s (12288 × 12288 VC-3/STS-1; 4096 × 4096 VC-4).
2. Only XC640 cross-connection and timing units can be provisioned in slot 9 and slot 10.

   When any cross-connection and timing unit other than an XC640 is installed in slot 9 or slot 10, then the green activity LED of that cross-connection and timing unit will be flashing, and a `Circuit Pack Type Mismatch` alarm will be reported.
3. Port units can be used in *all* universal slots, and *all* universal slots can be used.
4. Lower-order cross-connection units:

If lower-order traffic (VT1.5, VC-12, or lower-order VC-3) is to be switched, a lower-order cross-connection unit (LOXC) must be installed in dedicated universal slots or slot combinations.

- Lower-order cross-connection units of type LOXC/1 (occupying 1 slot, with a switching capacity of 15 Gbit/s) can be used in the universal slots 4, 17, 18, 19, 37, and 39. The slots 4, 17, 18 and 37 are worker slots. The slots 19 and 39 are protection slots. If the worker LOXC/1 is installed in slot 4, 17, or 18, then the protection LOXC/1 must be installed in slot 19. If the worker LOXC/1 is installed in slot 37, then the protection LOXC/1 must be installed in slot 39.
- Lower-order cross-connection units of type LOXC40G2S/1 (2 slots wide, with a switching capacity of 40 Gbit/s) can be used in the universal slot pairs 3/4, 16/17, 18/19, 36/37, and 38/39. The slot pairs 3/4, 16/17 and 36/37 are worker slots. The slot pairs 18/19 and 38/39 are protection slots. If the worker LOXC40G2S/1 is installed in the slot pairs 3/4 or 16/17, then the protection LOXC40G2S/1 must be installed in the slot pair 18/19. If the worker LOXC40G2S/1 is installed in the slot pair 36/37, then the protection LOXC40G2S/1 must be installed in the slot pair 38/39.
- Lower-order cross-connection units of type LOXC40G3S/1 (3 slots wide, with a switching capacity of 40 Gbit/s) can be used in the universal slot 4 and 19 (worker LOXC40G3S/1 in the slots 2/3/4, protection LOXC40G3S/1 in the slots 17/18/19).

  The slot left of the LOXC40G3S/1, that means slot 1 and slot 16, cannot be used and must remain empty.

Please note that *at most one LOXC equipment protection group may exist, i.e. at most one worker slot can be used in combination with one protection slot* (please also refer to the *1675 LambdaUnite MSS User Provisioning Guide*).

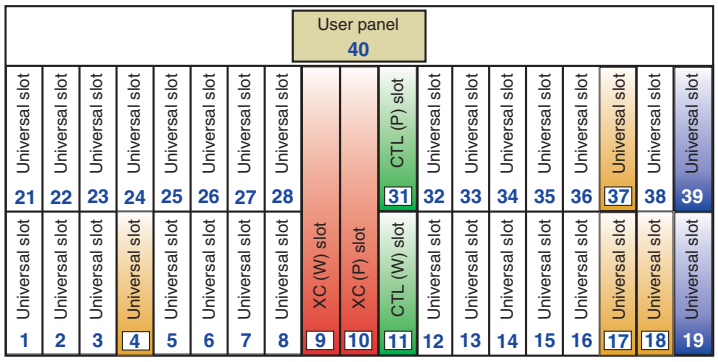
5. The Portless TransMUX unit (TMUX2G5/1), which occupies 1 slot, can be provisioned in any universal slot of the system. However, please note that the TMUX2G5/1 unit can only be used in combination with lower order cross-connection units of type LOXC40G2S/1 and LOXC40G3S/1. Operation in combination with LOXC/1 is not supported. This implies that the TMUX2G5/1 can only be used in NEs equipped with Controllers of type CTL/3T or CTL/3S. NEs equipped with any other type of Controller do not accept the provisioning of a TMUX2G5/1 unit.

   While configuring the system, consider the impact of the Portless TransMUX unit (TMUX2G5/1) on the lower-order switching capacity of the LOXC40. The traffic being transmultiplexed by 12 Portless TransMUX unit and being forwarded to LOXC40 allocates 30 Gbit/s (12 × 48 STS1) from the available backplane capacity of the LOXC40. Therefore only 10 Gbit/s are available for VT1.5 connections from LOXC40 towards I/O packs. Each DS3 filled to 100 % of its capacity and connected to a TMUX2G5/1 costs 2.5 Gbit/s of the LOXC40 switching capacity per direction, or 5 Gbit/s in sum. As a result, eight fully used TMUX2G5/1 circuit packs would cost a switching capacity of 8 × 2 × 2.5 Gbit/s = 40 Gbit/s and thus completely use the LOXC40.

The full transmultiplexed bandwidth of 12 TMUX2G5/1 pairs can only be crossconnected completely if it is sent back to TMUX2G5/1 packs again (sort of DS1-crossconnect emulation). For applications where transmultiplexed traffic is handed off as VT1.5 over an I/O pack and all DS1s of DS3 signals are used and have to be crossconnected, no more than eight TMUX2G5/1 pairs can be handled.

The full number of 12 active TMUX2G5/1 (or 12 pairs) in the system can only be used if the DS3s transport DS1s up to around 30% of their capacity (average value).

6. XC640 cross-connection and timing units support ONNS applications (cf. "LXC supporting ONNS applications" (p. 6-16)).
7. It is recommended to use *two* XC640 cross-connection and timing units. Thus, both the cross-connection as well as the timing function are automatically 1+1 equipment protected. However, an LXC configuration with a single, but unprotected XC640 cross-connection and timing unit is also possible.



front view

LXC supporting ONNS applications

A special shelf equipage is required for ONNS applications.

Observe these rules and guidelines with regard to an LXC configuration supporting ONNS applications:

1. Only Controllers of type *CTL/3S* (with a 1-GByte *CompactFlash*® card) and cross-connection and timing units of type *XC160, XC320/B, or XC640* are suitable for ONNS applications.

   It is recommended to use *two* suitable Controllers (duplex control) and *two* suitable cross-connection and timing units (XCs). Thus, both the Controllers as well as the cross-connection and timing units are automatically 1+1 equipment protected.
2. All available port units except for EP51 and OPT2G5 can be used for ONNS applications.
3. Lower-order cross-connections are *not* supported by ONNS.

In addition, the permissible shelf equipage depends on the maximum switch capacity, cf.

- “160-Gbit/s configuration” (p. 6-11)
- “320-Gbit/s configuration” (p. 6-12)
- “640-Gbit/s configuration” (p. 6-14)

LOXC circuit packs

If lower-order traffic (VC-3 (lower-order), VC-12, or VT1.5) is to be switched, a lower-order cross-connection unit (LOXC) must be installed in dedicated universal slots or slot combinations.

Important! There may be at most *one active* LOXC. A second LOXC has to be configured for equipment protection purposes. This implies that at most one LOXC equipment protection group is used.

The possible universal slots or slot combinations for the lower-order cross-connection functionality also depend on the maximum switching capacity of the system provided by the higher-order switching unit (XC…).

Supported slot combinations for LOXC/1

Lower-order cross-connection units of type LOXC/1 (occupying 1 slot, with a switching capacity of 15 Gbit/s) can be used in the universal slots 4, 17, 18, 19, 37, and 39. The slots 4, 17, 18, and 37 are worker slots. The slots 19 and 39 are protection slots. If the worker LOXC/1 is installed in slot 4, 17, or 18, then the protection LOXC/1 must be installed in slot 19. If the worker LOXC/1 is installed in slot 37, then the protection LOXC/1 must be installed in slot 39.

The supported slot combinations for the usage of LOXC/1 units are shown in the following table. There, “W” represents the working unit, “P” the protecting unit.

Higher-order switching unit		Slot assignment		
		LOXC/1 (W)	LOXC/1 (P)	Remarks and additional equipage guidelines
XC160		37	39	The slots 36 and 38 must remain empty.
XC320	Option 1[1]	4	19	The slots 3 and 18 must remain empty. The slots 2 and 17 are reserved, i.e. may be used in case a future in-service upgrade is planned towards LOXC units which occupy 2 or 3 slots, respectively.
	Option 2	17	19	Alternative equipage if slot 4 is occupied. The slots 16 and 18 must remain empty.
	Option 3[2]	37	39	The slots 36 and 38 must remain empty.

Higher-order switching unit		Slot assignment		
		LOXC/1 (W)	LOXC/1 (P)	Remarks and additional equipage guidelines
XC640	Option 1[1]	4	19	The slots 3 and 18 are reserved, i.e. may be used in case a future in-service upgrade is planned towards LOXC units which occupy 2 slots.
	Option 2[3]	17	19	The slots 16 and 18 are reserved, i.e. may be used in case a future in-service upgrade is planned towards LOXC units which occupy 2 slots.
	Option 3[4]	18	19	–
	Option 4[5]	37	39	The slots 36 and 38 are reserved, i.e. may be used in case a future in-service upgrade is planned towards LOXC units which occupy 2 slots.

Notes:

1. Option 1 is the preferred option.
2. XC320, option 3, is the preferred option for a seamless upgrade from XC160 to XC320.
3. XC640, option 2, is the preferred option for a seamless upgrade from XC320 to XC640.
4. XC640, option 3, is the preferred option for maximized automatic I/O slot usage by *Telcordia*® Technologies.
5. XC640, option 4, is the preferred option for a seamless upgrade from XC160 to XC640.

Supported slot combinations for LOXC40G2S/1

Lower-order cross-connection units of type LOXC40G2S/1 (2 slots wide, with a switching capacity of 40 Gbit/s) can be used in the universal slot pairs 3/4, 16/17, 18/19, 36/37, and 38/39. The slot pairs 3/4, 16/17 and 36/37 are worker slots. The slot pairs 18/19 and 38/39 are protection slots. If the worker LOXC40G2S/1 is installed in the slot pairs 3/4 or 16/17, then the protection LOXC40G2S/1 must be installed in the slot pair 18/19. If the worker LOXC40G2S/1 is installed in the slot pair 36/37, then the protection LOXC40G2S/1 must be installed in the slot pair 38/39.

The supported slot combinations for the usage of LOXC40G2S/1 units are shown in the following table. There, “W” represents the working unit, “P” the protecting unit.

Higher-order switching unit		Slot assignment		
		LOXC40G2S/1 (W)	LOXC40G2S/1 (P)	Remarks and additional equipage guidelines
XC640	Option 1	3/4	18/19	The slots 2 and 17 are reserved, i.e. may be used in case a future in-service upgrade is planned towards LOXC units which occupies 3 slots.
	Option 2	16/17	18/19	Alternative equipage.
	Option 3	36/37	38/39	Alternative equipage.

Supported slot combinations for LOXC40G3S/1

Lower-order cross-connection units of type LOXC40G3S/1 (3 slots wide, with a switching capacity of 40 Gbit/s) can be used in the universal slot 4 and 19 (worker LOXC40G3S/1 in the slots 2/3/4, protection LOXC40G3S/1 in the slots 17/18/19)

Supported LOXC system configurations and upgrades

"LOXC" is the collective term for lower-order cross-connection units (LOXC/1, LOXC40G2S/1, LOXC40G3S/1).

"LOXC40" is the collective term for lower-order cross-connection units with a switching capacity of 40 Gbit/s (LOXC40G2S/1, LOXC40G3S/1).

These system configurations and in-service hardware upgrades of lower-order cross-connection units are supported:

Current system configuration		Desired system configuration after the upgrade
1 In a system with a max. switching capacity of 160 Gbit/s, two LOXC/1 are installed: • Worker: slot 37 • Protection: slot 39	→	Two LOXC40G2S/1 occupying the same slots. **XC in-service upgrade required** Please note that such an upgrade is only possible after the max. switching capacity of the system has been upgraded to 640 Gbit/s. This requires an XC in-service upgrade XC160 → XC640.
2a In a system with a max. switching capacity of 320 Gbit/s, two LOXC/1 are installed: • Worker: slot 4 • Protection: slot 19	→	Two LOXC40G2S/1 occupying the same slots. **XC in-service upgrade required** Please note that such an upgrade is only possible after the max. switching capacity of the system has been upgraded to 640 Gbit/s. This requires an XC in-service upgrade XC320 → XC640.
2b In a system with a max. switching capacity of 320 Gbit/s, two LOXC/1 are installed: • Worker: slot 4 • Protection: slot 19	→	Two LOXC40G3S/1 occupying the same slots.
3 In a system with a max. switching capacity of 320 Gbit/s, two LOXC/1 are installed: • Worker: slot 17 • Protection: slot 19	→	Two LOXC40G2S/1 occupying the same slots. **XC in-service upgrade required** Please note that such an upgrade is only possible after the max. switching capacity of the system has been upgraded to 640 Gbit/s. This requires an XC in-service upgrade XC320 → XC640.

Current system configuration		Desired system configuration after the upgrade
4 In a system with a max. switching capacity of 320 Gbit/s, two LOXC/1 are installed: • Worker: slot 37 • Protection: slot 39	→	Two LOXC40G2S/1 occupying the same slots. **XC in-service upgrade required** Please note that such an upgrade is only possible after the max. switching capacity of the system has been upgraded to 640 Gbit/s. This requires an XC in-service upgrade XC320 → XC640.
5 In a system with a max. switching capacity of 640 Gbit/s, two LOXC/1 are installed: • Worker: slot 4 • Protection: slot 19	→	Two LOXC40G2S/1 occupying the same slots. The upgrade is possible *without* a system capacity upgrade.
6 In a system with a max. switching capacity of 640 Gbit/s, two LOXC/1 are installed: • Worker: slot 17 • Protection: slot 19	→	Two LOXC40G2S/1 occupying the same slots. The upgrade is possible *without* a system capacity upgrade.
7 In a system with a max. switching capacity of 640 Gbit/s, two LOXC/1 are installed: • Worker: slot 18 • Protection: slot 19	→	*LOXC upgrade not possible!*
8 In a system with a max. switching capacity of 640 Gbit/s, two LOXC/1 are installed: • Worker: slot 37 • Protection: slot 39	→	Two LOXC40G2S/1 occupying the same slots. The upgrade is possible *without* a system capacity upgrade.

Important!

- As an LOXC40G2S/1 occupies two slots while an LOXC/1 occupies only one, the slot left to an LOXC/1 needs to be empty before an upgrade towards an LOXC40G2S/1 can be performed.
- As an LOXC40G3S/1 occupies three slots while an LOXC/1 occupies only one, the two slots left to an LOXC/1 need to be empty before an upgrade towards an LOXC40G3S/1 can be performed.

Portless TransMUX unit (TMUX2G5/1)

The Portless TransMUX unit (TMUX2G5/1) can be provisioned in any universal slot of the system.

The TMUX2G5/1 unit is only usable in combination with LOXC40G3S/1 and LOXC40G2S/1. Operation in combination with LOXC/1 is not supported. This restriction implies that the TMUX2G5/1 can only be used in NEs equipped with CTL/3T or CTL/3S. NEs equipped with any other type of CTL do not accept the provisioning of a TMUX2G5/1 unit.

Equipment protection

TMUX2G5/1 units can be used unprotected or 1+1 protected. The system supports up to 12 independent pairs of TMUX2G5/1 units, that means 12 1+1 equipment protection groups of type “TMUX”. A 1+1 equipment protection group of type “TMUX” can be created using two TMUX2G5/1 units in slot pairs of adjacent slots. The left of both slots is always assigned to be the worker slot. The allowed slot combinations are: {1, 2}; {3, 4}; {5, 6}; {7, 8}; {12, 13}; …; {18, 19}; {21, 22}; …; {27, 28}, {32, 33}; …; {38, 39}.

Double-density parent boards

The OP10D/PAR2 and the OP10D/PAR2XFP parent boards can be equipped with up to 2 optical interface modules, each having a transmission capacity of 10 Gbit/s. Thus, the transmission capacity of a fully equipped OP10D/PAR2 or OP10D/PAR2XFP port unit is 20 Gbit/s.

For the 10-Gbit/s optical interface modules to work with the OP10D/PAR2, slide-in modules with an Alcatel-Lucent proprietary design are used. These modules can be installed in the factory only.

The OP2G5D/PAR8 can be equipped with up to 8 SFP modules with a transmission capacity of 2.5 Gbit/s each. A fully equipped OP2G5D/PAR8 has a transmission capacity of 20 Gbit/s.

In configurations other than the 640-Gbit/s configuration, the so-called double-density cards (OP10D/PAR2, OP10D/PAR2XFP, and OP2G5D/PAR8) must be inserted according to the following rules.

In 160-Gbit/s configurations, double-density cards are supported only in every second slot (in slot number 22, 24, 26, 28, 33, 35, 37, 39), and the slot on the left side next to it must be left empty.

In 320-Gbit/s configurations, double-density cards are supported only in every second slot (in slot number 2, 4, 6, 8, 13, 15, 17, 19, 22, 24, 26, 28, 33, 35, 37, 39), and the slot on the left side next to it must be left empty.

Low bandwidth SFP parent board

The 622-Mbit/s / 155-Mbit/s Small Form Factor Pluggable (SFP) parent board (OPLB/PAR8) supports up to eight SFPs.

The parent board is provisioned entirely for one transmission rate:

- 622 Mbit/s or
- 155 Mbit/s.

SFP modules

The optical plug-in modules for the OPx/PARy parent boards and for the transparent parent board OPT2G5 are so called Small Form Factor Pluggable units (SFPs). They can be inserted and removed in a live system (“hot pluggable”).

The number of inserted SFPs can be configured flexibly between 0 and the maximum number (3, 6 or 8). The remaining SFP slots can be left empty and should be covered by a faceplate.

The 1675 LambdaUnite MSS SFPs are marked by the manufacturer and checked upon insertion, in order to protect from accidental insertion of non 1675 LambdaUnite MSS specific SFPs.

EP155 electrical circuit packs

The electrical 155-Mbit/s units (EP155) can be inserted in the upper slot row only, thus up to 16 circuit packs fit into one 1675 LambdaUnite MSS subrack. In case of 1+1 protection of these circuit packs adjacent slot pairs odd/even are used, for example slot 21 and 22.

The combination of EP155 and EP51 is possible, taking into account the slot coverage of the respective connection interfaces (ECIs), .

EP51 electrical circuit packs

The electrical DS3/EC1 units (EP51) can be inserted in the upper slot row only, thus up to 16 circuit packs fit into one 1675 LambdaUnite MSS subrack when configured for 1+1 protection. The protected circuit packs are inserted into adjacent slot pairs odd/even, for example slot 21 and 22.

The maximum number of active circuit packs is limited to eight. This means that when a protection switch takes place, the protection card becomes active and the previous in service card becomes in active

The combination of EP51 and EP155 is possible, taking into account the slot coverage of the respective connection interfaces (ECIs).

Optical port unit protection

In the case of optical port protection (1+1 Linear APS / 1+1 MSP) it is recommended to place the working port unit and the protection port unit side by side for ease of maintenance.

Floor plan layout

This section gives information about the space needed to mount 1675 LambdaUnite MSS subracks and racks.

Rack dimensions

The regular racks require an area of 600 mm × 600 mm (23.6 in × 23.6 in) (width × depth) in accordance with ETSI 300 119 and with Bellcore GR-63. This area represents the absolute system limits which is not exceeded in the operating state by protruding elements such as switches or plugs. The rack height can be chosen in accordance with the local conditions. Standard height is 2.2 m (86.6). Heights 2.6 m (102.4 in), 2.125 m (83.6 in) and a seismic rack (2.0 m (78.7 in) height) are possible depending on customer requirements (not standard delivery).

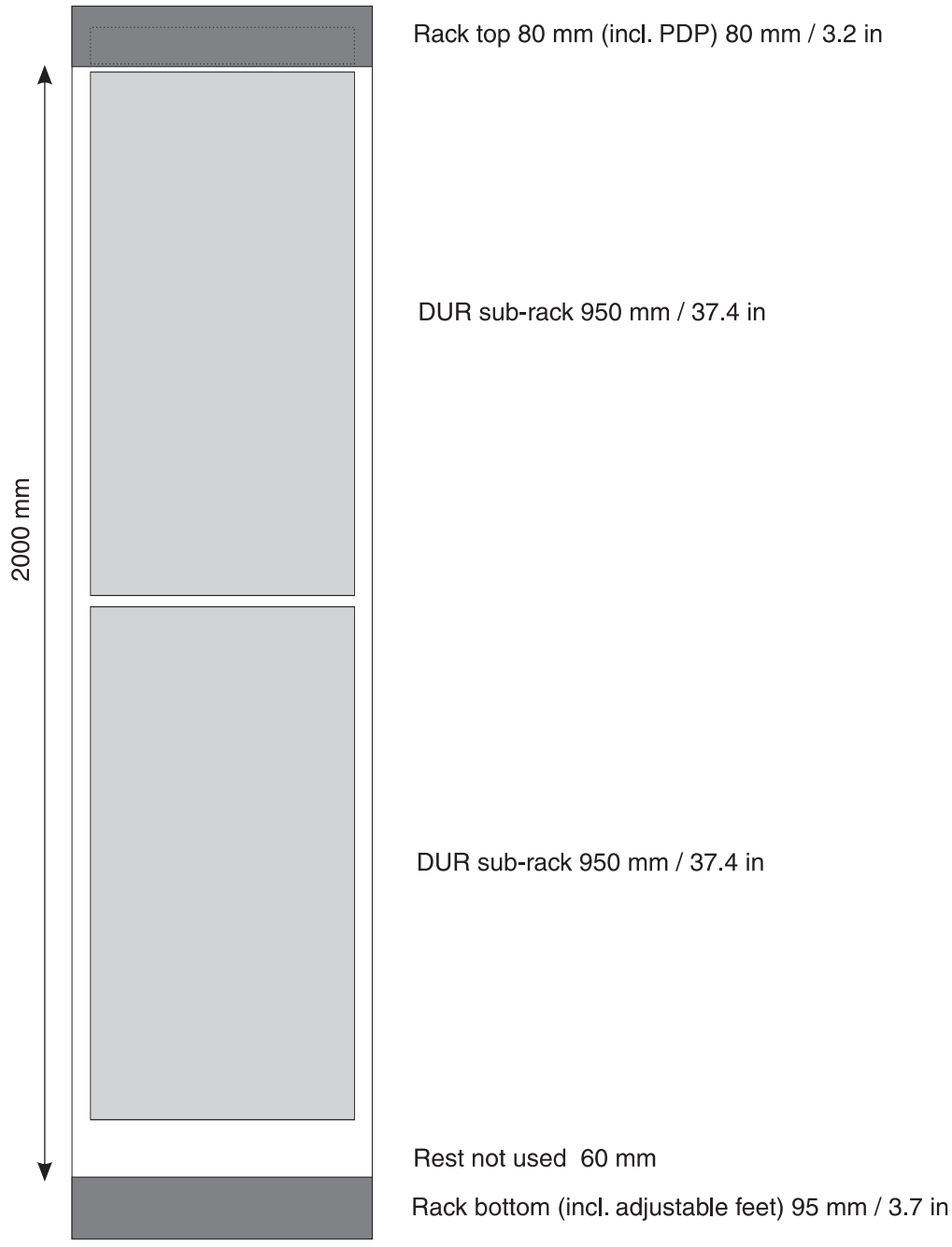
For equipment heat release information, please refer to “Heat release” (p. 6-6).

Rack extensions

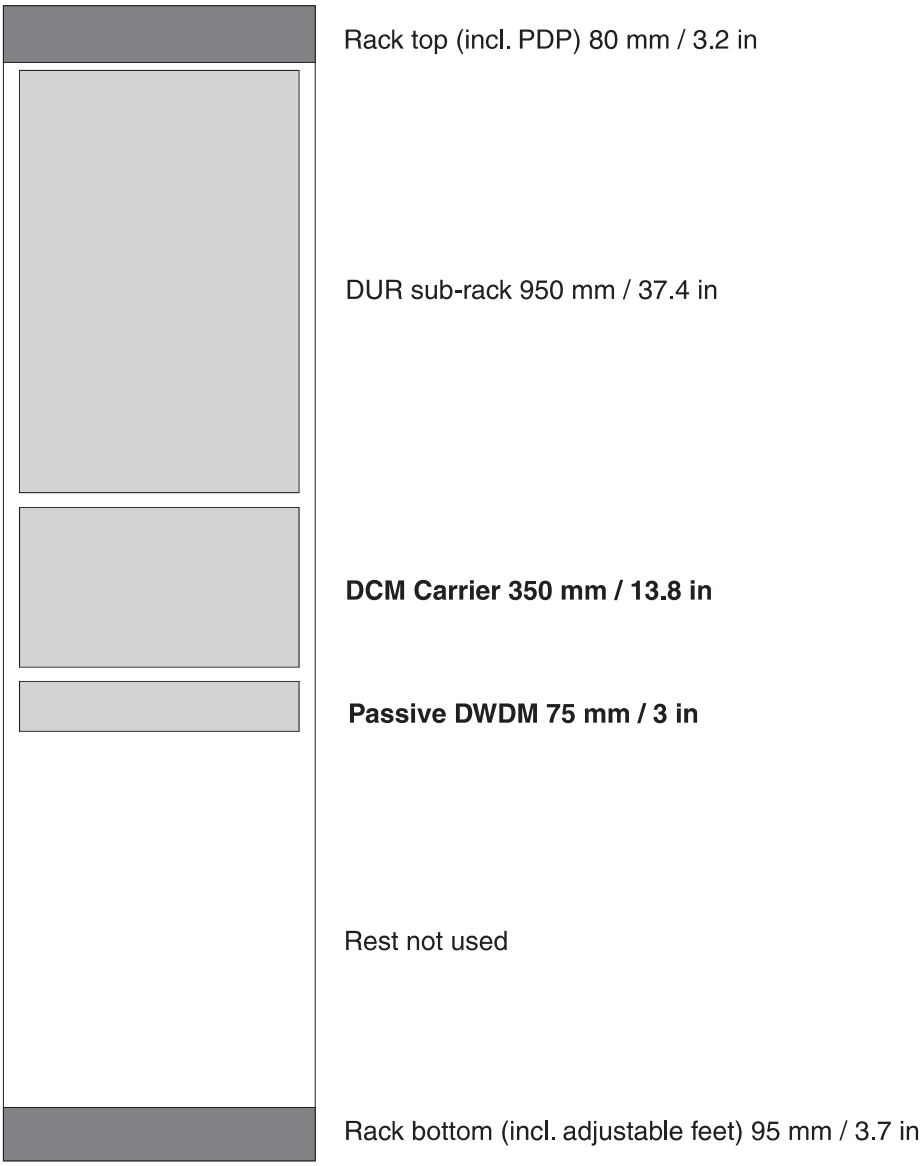
For the transport of electrical signals the use of rack extensions is recommended. The width of the regular rack is increased by these extensions, to ease cable handling and guidance. These rack extensions are mounted on both sides of the regular rack, providing an extra width of 75 mm (2.9 in) on either side; the side plates of the rack can be mounted on the rack extensions, resulting in a total rack width of 750 mm (29.5 in).

Rack equipping

Depending on the desired configuration, the appropriate rack height must be chosen. The following figure gives an example of a 2.2 m (86.6 in) rack (ETSI 300 119) equipped with two Dual Unit Row (DUR) shelves.

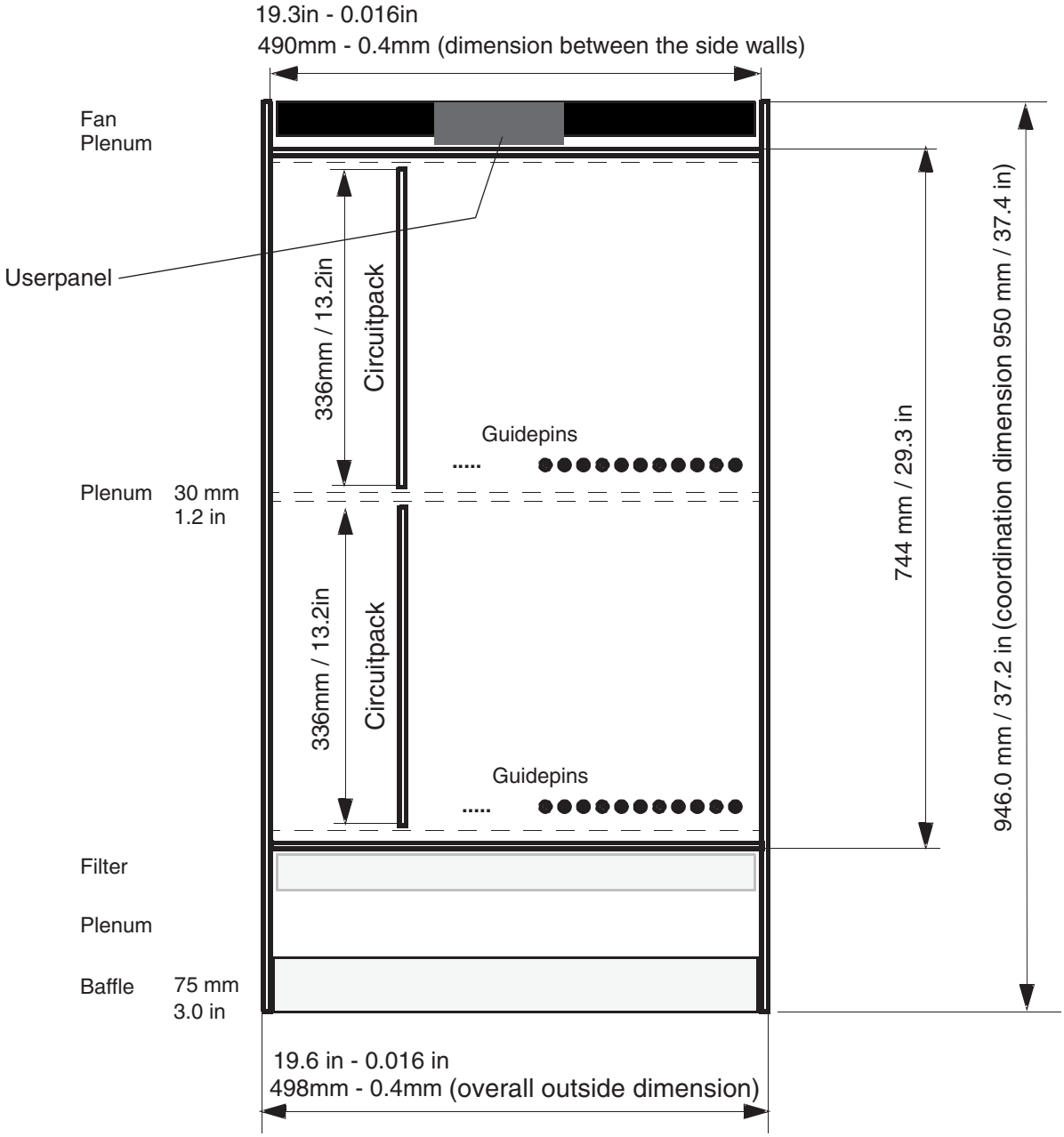


In the following figure a carrier for Dispersion Compensation Modules (DCM) and a passive WDM box have been mounted in the rack. DCMs are required only with 40-Gbit/s applications without DWDM interfacing. The passive WDM box is an OEM product. In this case, only one DUR subrack fits in a 2.2 m (86.6 in) rack (1675 LambdaUnite MSS specific rack). Instead of a DCM unit there is also the possibility to insert a storage box for fiber overlength.



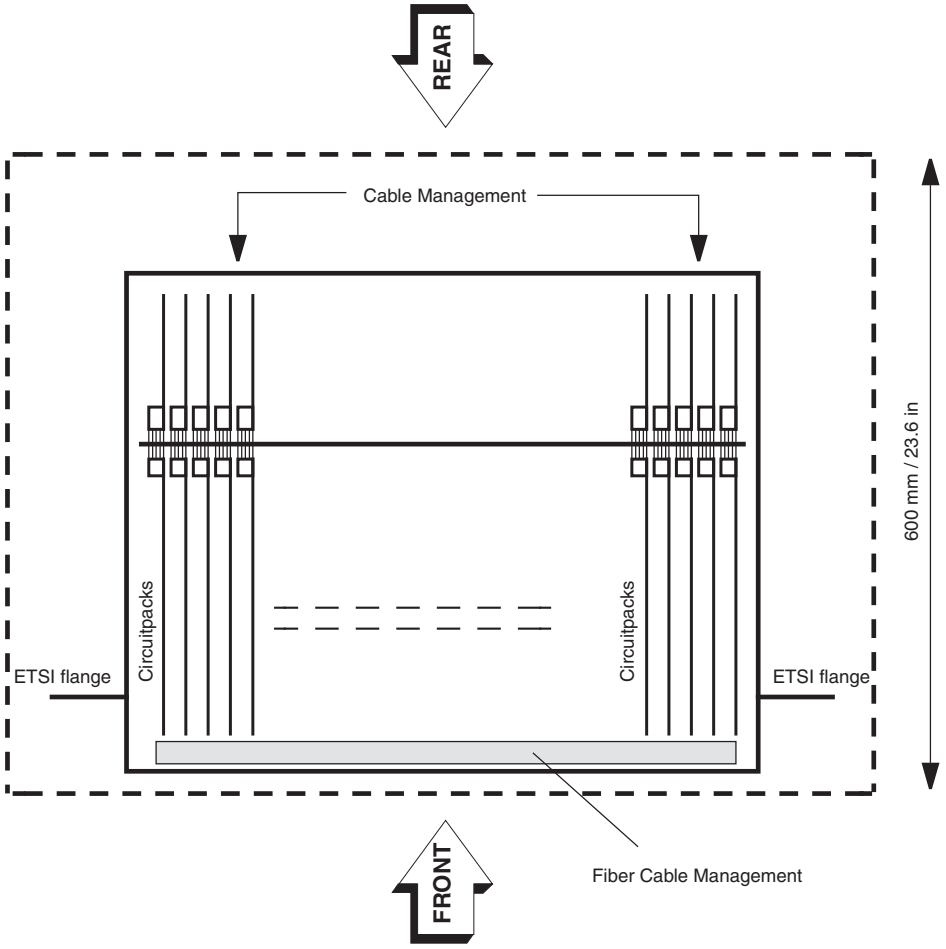
Subrack dimensions

The size of the DUR subrack is 950 mm × 500 mm × 545 mm (37.4 in × 19.7 in × 21.5 in) (height × width × depth). The air flow baffle mounted in the lower part of the subrack is already included in this value.



Front and rear access

The following view-from-above figure illustrates the space required for front and rear access to the system. Front access is required for operations activities and rearrangements of the optical port units. Rear access is required for upgrades that require cable rearrangements and for the arrangements/rearrangements of the electrical transmission cables (STM-1 and DS3/EC1).



Equipment interconnection

This section describes equipment interconnection in 1675 LambdaUnite MSS.

Optical connectors

The optical port units provide optical connections through faceplate-mounted LC connectors. The LC connectors are designed as a duplex configuration that offers a high-density fiber-to-fiber pitch. If 1675 LambdaUnite MSS is mounted in a rack with doors you must use fiber connectors with angled boots.

The following figure illustrates the LC connectors and straight fiber connectors.

LBOs

If required, 1675 LambdaUnite MSS provides optical attenuation using lightguide build-outs (LBOs) on the optical ports. All optical interfaces are factory-equipped with 0-dB LC-type connectors. The optical attenuation can be changed by replacing the LBO.For a complete list of items and comcodes please refer to the 1675 LambdaUnite MSS engineering drawing ED8C948-10, described in “Engineering Drawing” (p. 7-2).

Electrical connectors

The following table shows the types of electrical connectors used for the 1675 LambdaUnite MSS interfaces.

Interface Function	Connector Type
Alarm (station)	D-Sub shielded - filtered
MDI/MDO	D-Sub shielded - filtered
LAN 1 (on User Panel)	RJ 45 crossed - shielded
LAN2, LAN3, LAN4	RJ 45 crossed - shielded
Racktop (alarm lamps)	D-Sub shielded - filtered
FAN Signals (from fan controller board)	D-Sub shielded

Interface Function	Connector Type
UPL internal interface (from user panel)	D-Sub shielded
G703 interface	D-Sub shielded
V11 interface	D-Sub shielded
Station clock interface	D-Sub shielded
STM-1 electrical	1.6 / 5.6 coax connector

7 Ordering

Overview

Purpose

This chapter provides an overview of the ordering process and the current software & licence ordering information for 1675 LambdaUnite MultiService Switch (MSS).

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Ordering information	7-2

Ordering information

1675 LambdaUnite MSS has been carefully engineered and all equipment kitted to simplify the ordering process. In this chapter the current software and licence items are shown, as available on the issue date of this document. For a complete and up-to-date list of all orderable items please refer to the Engineering Drawings described below.

Contact and further information

For all questions concerning ordering of 1675 LambdaUnite MSS, for any information about the marketable items and their comcodes, and for ordering the equipment please contact your Account Executive for 1675 LambdaUnite MSS or your Alcatel-Lucent local customer team.

Engineering Drawing

For a complete list of the orderable items with the respective comcodes please refer to the Engineering Drawing 1675 LambdaUnite MSS "Engineering and Ordering Information" that you can

- find appended at the end of this document, up to date of the printing date (if it was ordered at the Alcatel-Lucent Online Customer Support Site (OLCS))
- order the latest version at the Alcatel-Lucent Online Customer Support Site (OLCS) (https://support.alcatel-lucent.com) with the order code *ED8C948-10*.

Software and license items

The following table lists the ordering information concerning the current software and licence items for 1675 LambdaUnite MultiService Switch (MSS):

Description	Functional name	Item code	Comcode
1675 LambdaUnite MSS Release 10.05.54 NE SW (incl. EMS agent) + CIT SW + license + 3rd party license and full Release Letter	R10.05.54 NE SW	SCAA1051F	109781252
1675 LambdaUnite MSS Upgrade Software License, Upgrade to R10.05.54	Upgrade License → R10.05.54	SBAA1052F	109781260
1675 LambdaUnite MSS Upgrade SW CD to R10.05.54	Upgrade SW CD → R10.05.54	SCAA1052F	109781278
1675 LambdaUnite MSS *WaveStar*® CIT CD, R10.05.54	LambdaUnite CIT CD R10.05.54	SCCA1051F	109781286

Documentation items

For the order codes of the current 1675 LambdaUnite MSS documentation refer to "Related information" (p. xvii).

8 Product support

Overview

Purpose

This chapter provides information about the support for the 1675 LambdaUnite MultiService Switch (MSS).

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Maintenance services	8-7
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Documentation support	8-9
Training support	8-10

Installation services

This section describes the installation services available to support 1675 LambdaUnite MSS.

Alcatel-Lucent offers Installation Services focused on providing the technical support and resources needed to efficiently and cost-effectively install your network equipment. Alcatel-Lucent's Installation Services provide unparalleled network implementation expertise to help install your wire line and wireless networks. We use state-of-the-art tools and technology, and highly skilled technicians to install your equipment and help to ensure the timely and complete implementation of your network solution. By relying on our installation experts, we can rapidly build or expand your network, help manage the complexity of implementing new technologies, reduce operational costs, and help improve your competitive position by enabling your staff to focus on the core aspects of your business rather than focusing on infrastructure details.

Description

Within Alcatel-Lucent's overall Installation Services portfolio, Basic Equipment Installation and Site Supplemental Installation are the two services most closely linked to the initial deployment of Alcatel-Lucent's 1675 LambdaUnite MSS product into your network.

Basic Equipment Installation

Provides the resources, experience and tools necessary to install the 1675 LambdaUnite MSS product into your network. We assemble, cable and wire, and test the 1675 LambdaUnite MSS, helping to ensure it is fully functioning as engineered and specified.

Site Supplemental Installation

Enhances the Basic Equipment Installation service by performing supplemental work that is unique to your specific site location, configuration, or working requirements. Includes installation of material other than the main footprint product (such as earthquake bracing); provision of services unique to your site (such as, hauling and hoisting, multi-floor cabling, rental and local purchases) or as may be required by your operations (such as, overtime to meet your compressed schedules, night work requested by you, abnormal travel expenses, abnormal transportation or warehousing); and any other additional effort or charges associated with your environment.

Benefits

When implementing our Installation Services, Alcatel-Lucent becomes a strategic partner in helping you realize your long-term strategies and achieve your business and technological goals. We combine our state-of-the-art technical background, high-quality processes, expertise in the latest technologies, knowledge of revolutionary equipment breakthroughs, and feature-rich project management tools to get your network up and

running - quickly, efficiently, and reliably. With Alcatel-Lucent, you can concentrate on your core business, while we apply our years of knowledge and experience to installing your network.

Our Installation Services let you:

- *Rapidly expand your network* — by turning hardware into working systems, with the capability to deploy multiple networks in parallel rollouts
- *Reduce operational expense* — of recruiting, training, and retaining skilled installation personnel
- *Leverage Alcatel-Lucent's resources and expertise* — by utilizing our team of knowledgeable and fully equipped experts that implement projects of any size, anywhere around the world
- *Implement quality assurance* — through our total quality management approach
- *Reduce operational expenses* — by avoiding the purchase of the necessary state-of-the-art tools, test equipment, specialized test software, and spare parts that Alcatel-Lucent's Installation Services utilize
- *Ensure high-quality support* — with Alcatel-Lucent's extensive support structure, including proven methods and procedures, mechanized tools, professional training, technical support, and access to Bell Labs.

Reference

For more information about specialized installation services and/or database preparation, please contact your local Account Executive.

Engineering services

This section describes the engineering services available to support 1675 LambdaUnite MSS.

Alcatel-Lucent Worldwide Services (LWS) offers Engineering Services focused on providing the technical support and resources needed to efficiently and cost-effectively engineer your network equipment. We provide the best, most economical equipment solution by ensuring your network equipment is configured correctly, works as specified, and is ready for installation upon delivery. With our proven, end-to-end solutions and experienced network engineering staff, Alcatel-Lucent Worldwide Services is the ideal partner to help service providers engineer and implement the technology that supports their business.

Description

Within Alcatel-Lucent's overall Engineering Services portfolio, Site Survey, Basic Equipment Engineering, Site Engineering, and Site Records are the four services most closely linked to the initial deployment of 1675 LambdaUnite MSS into your network; each is described below.

Site Survey

A Site Survey may be required to collect your site requirements needed for proper equipment engineering. If adequate site requirements and records are not available up front, a site survey would be performed to collect information required for configuration of the equipment and integration of the equipment into the site.

Basic Equipment Engineering

Ensures that the correct footprint hardware is ordered and that the ordered equipment is configured for optimal performance in the network for the customer. Alcatel-Lucent Engineering configures equipment requirements based on inputs from the customer order, completed questionnaires, and/or site survey data. The decisions as to specific equipment needs are based on each component’s functionality and capacity, and the application of engineering rules associated with each component.

Site Engineering

Ensures that the correct site material is ordered and that the optimal equipment layout for the installation of the ordered equipment in the customer’s site is determined. Site Engineering will be used in assisting the customer with determining the necessary site conditions, layout and equipment required to properly install/integrate the footprint hardware components into a specific location.

Site Records

Site Records Service provides detailed record keeping which accurately documents the physical placement and configuration of specified customer equipment. Depending on the customer request, this can involve the initial creation of site records, updating of existing records, or ongoing maintenance of the customer's records.

Benefits

When implementing our Engineering Services, Alcatel-Lucent becomes a strategic partner in helping you realize your long-term strategies and achieve your business and technological goals. Our Engineering Services portfolio delivers quick, responsive support, with state-of-the-art tools, top technicians and end-to-end services to help you engineer an optimal network solution. Whether you are looking to outsource your total engineering effort or simply supplement basic coverage gaps, our portfolio of services provides the flexible level of support you need. With Alcatel-Lucent, you can concentrate on your core business while we apply our years of knowledge and experience in engineering your equipment solutions.

Our Engineering Services let you:

- *Rapidly expand your network* — by turning products into working systems, with the capability to deploy multiple networks in parallel rollouts
- *Reduce costs* — by determining the most cost-effective network configuration and optimal use of office space when planning and providing an equipment solution
- *Reduce operational expense* — of recruiting, training, and retaining skilled engineering personnel
- *Leverage Alcatel-Lucent's resources and expertise* — by utilizing our team of knowledgeable and fully equipped experts that can plan, design, and implement projects of any size, anywhere around the world
- *Implement quality assurance* — through our total quality management approach and use of ISO-certified processes
- *Provide one–stop shopping* with a globally deployed engineering workforce, saving the time, delays and coordination challenges of dealing with multiple equipment vendors and service providers
- *Keep pace with rapidly changing technology* — by supporting the latest technologies and equipment breakthroughs, including Alcatel-Lucent's and other vendor's products
- *Ensure high-quality support* — with Alcatel-Lucent's extensive support structure, including proven methods and procedures, mechanized tools, professional training, technical support, and access to Bell Labs
- *Maintain and track vital office records* — keep track of equipment locations and connections.

Reference

For more information about specialized engineering services, engineering consultations, and/or database preparation, please contact your local Account Executive.

Maintenance services

This section describes the maintenance services available to support 1675 LambdaUnite MSS.

Description

Maintenance Services is composed of three primary services to support your maintenance needs. The services are

- Remote Technical Support Service (RTS)
- On-site Technical Support Service (OTS)
- Repair and Exchange Services (RES)

Remote Technical Support Service (RTS)

RTS provides telephone and web-based access to remote engineers and tools for product information, network diagnostics, and trouble resolution and restoration for all Alcatel-Lucent products.

On-site Technical Support (OTS)

OTS provide network trouble resolution and restoration, at the customer's location, for all Alcatel-Lucent products and selected OEM equipment.

Repair and Exchange Services (RES)

RES provides advanced exchange or return for repair services for defective hardware, eliminating the need for you to purchase and maintain a costly spares inventory.

Contact

For maintenance service contact information please refer to “Technical support” (p. 8-8).

Technical support

This section describes the technical support available for 1675 LambdaUnite MSS.

Services

1675 LambdaUnite MSS is complemented by a full range of services available to support planning, maintaining, and operating your system. Applications testing, network integration, and upgrade/conversion support is also available.

Contacts

You can contact your Alcatel-Lucent Customer Team using the Alcatel-Lucent Customer Support web site (http://www.alcatel-lucent.com/support/) or through your Local Customer Support.

Documentation support

The Alcatel-Lucent documentation organization provides comprehensive product documentation tailored to the needs of the different audiences. An overview of the documentation set can be found at "Related information" (p. xvii).

Customer comment

As customer satisfaction is extremely important to Alcatel-Lucent, every attempt is made to encourage feedback from customers about our information products. Thank you for your feedback.

To comment on this information product, go to the Online Comment Form (*http://infodoc.alcatel-lucent.com/comments/enus*) or e-mail your comments to the Comments Hotline (*comments@alcatel-lucent.com*).

Training support

To complement your product needs, the Alcatel-Lucent University organization offers a formal training package, with the single training courses scheduled regularly at Alcatel-Lucent's corporate training centers or to be arranged as on-site trainings at your facility.

Registering for a course or arranging an on-site training

To review the available courses, to enroll for a training course at one of Alcatel-Lucent' corporate training centers, or to obtain contact information please visit the Alcatel-Lucent University web site (http://www.alcatel-lucent.com/university/index.html).

9 Quality and reliability

Overview

Purpose

This chapter provides information about the quality and reliability of 1675 LambdaUnite MultiService Switch (MSS).

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Quality

Overview

Purpose

This section describes Alcatel-Lucent ´s commitment to quality and reliability and how quality is ensured.

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Alcatel-Lucent's commitment to quality and reliability

Alcatel-Lucent is extremely committed to providing our customers with products of the highest level of quality and reliability in the industry. 1675 LambdaUnite MSS is a prime example of this commitment.

Quality policy

Alcatel-Lucent is committed to achieving sustained business excellence by integrating quality principles and methods into all we do at every level of our company to

- Anticipate and meet customer needs and exceed their expectations, every time
- Relentlessly improve how we work – to deliver the world's best and most innovative communications solutions – faster and more cost-effectively than our competitors

Reliability in the product life-cycle

Each stage of the life cycle of 1675 LambdaUnite MSS relies on people and processes that contribute to the highest product quality and reliability possible. The reliability of a product begins at the earliest planning stage and continues into

- Product architecture
- Design and simulation
- Documentation
- Prototype testing during development
- Design change control
- Manufacturing and product testing (including 100% screening)
- Product quality assurance
- Product field performance
- Product field return management

The R&D community of Alcatel-Lucent is certified by ISO 9001.

Ensuring quality

This section describes the critical elements that ensure product quality and reliability within

- Product development
- Manufacturing

Critical elements of product development

The product development group's strict adherence to the following critical elements ensures the product's reliability

- Design standards
- Design and test practices
- Comprehensive qualification programs
- System-level reliability integration
- Reliability audits and predictions
- Development of quality assurance standards for manufactured products

Critical elements of manufacturing

Note: Independent Quality Representatives are also present at manufacturing locations to ensure shipped product quality.

The manufacturing and field deployment groups' strict adherence to the following critical elements ensures the product's reliability

- Pre-manufacturing
- Qualification
- Accelerated product testing
- Product screening
- Production quality tracking
- Failure mode analysis
- Feedback and corrective actions

Conformity statements

CE conformity

For detailed information about CE conformity, refer to the respective Declaration of Conformity that is available separately ("Related information" (p. xvii)).

Eco-environmental statements

The statements that follow are the eco-environmental statements that apply to the *Waste from Electrical and Electronic Equipment (WEEE) directive*.

Packaging collection and recovery requirements

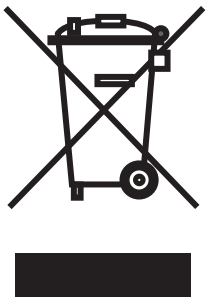
Countries, states, localities, or other jurisdictions may require that systems be established for the return and/or collection of packaging waste from the consumer, or other end user, or from the waste stream. Additionally, reuse, recovery, and/or recycling targets for the return and/or collection of the packaging waste may be established.

For more information regarding collection and recovery of packaging and packaging waste within specific jurisdictions, please contact the Alcatel-Lucent Customer Support organization.

Recycling / take-back / disposal of product

Electronic products bearing or referencing the symbol shown below when put on the market within the European Union, shall be collected and treated at the end of their useful life, in compliance with applicable European Union and local legislation. They shall not be disposed of as part of unsorted municipal waste. Due to materials that may be contained in the product, such as heavy metals or batteries, the environment and human health may be negatively impacted as a result of inappropriate disposal.

For any question, you can email: *takeback@alcatel-lucent.com*.



Note: In the European Union, a solid bar under the crossed-out wheeled bin indicates that the product was put on the market after 13 August 2005.

Moreover, in compliance with legal requirements and contractual agreements, where applicable, Alcatel-Lucent will offer to provide for the collection and treatment of Alcatel-Lucent products at the end of their useful life, or products displaced by Alcatel-Lucent equipment offers.

For information regarding take-back of equipment by Alcatel-Lucent, or for more information regarding the requirements for recycling/disposal of product, please contact your Alcatel-Lucent Account Manager or Alcatel-Lucent Takeback Support at *takeback@alcatel-lucent.com*.

Material content compliance

The European Union (EU) directive 2002/95/EC, “Restriction of the use of certain Hazardous Substances” (RoHS), restricts the use of lead, mercury, cadmium, hexavalent chromium, and certain flame retardants in electrical and electronic equipment. This directive applies to electrical and electronic products placed on the EU market since July 1, 2006. The directive allows various exemptions, including an exemption for lead solder in network infrastructure equipment. Alcatel-Lucent products shipped to the EU since July 1, 2006, for example, 1675 LambdaUnite MSS, comply with the RoHS directive (RoHS5). These products also comply with the corresponding Chinese RoHS directive.

Technical documentation

The technical documentation as required by the Conformity Assessment procedure is kept at Alcatel-Lucent location which is responsible for this product. For more information please contact your local Alcatel-Lucent representative.

Reliability

Overview

Purpose

This section describes how reliability is specified.

Contents

General reliability specifications	9-8
1675 LambdaUnite MSS failure-in-time rates	9-10

General reliability specifications

This section provides general reliability specifications for 1675 LambdaUnite MSS.

Mean Time Between Failures

The Mean Time Between Failures (MTBF) for the whole 1675 LambdaUnite MSS depends on the equipage of the system and on the specific hardware FIT rates, refer to "1675 LambdaUnite MSS failure-in-time rates" (p. 9-10). For further information please contact your Customer Team.

Mean time to repair

The mean time to repair for 1675 LambdaUnite MSS is assumed to be 2 hours. This figure includes dispatch, diagnostic, and repair time.

Infant mortality factor

Note: The steady state failure rate is equal to the failure rate of the system.

The number of failures that a product experiences during the first year of service after turn-up may be greater than the number of subsequent annual steady state failures. This is the early life or infant mortality period. The ratio of the first year failure rate to the steady state failure rate is termed the infant mortality factor (IMF).

The estimation of the 1675 LambdaUnite MSS circuit pack reliability is based on an infant mortality factor (IMF) smaller than 2.5. That means the first year failure rate (or infant mortality rate [IMR]) is assumed to be <2.5 times the steady state failure rate.

Product design life

The product design life for 1675 LambdaUnite MSS is 15 years, except for the fan units and the *CompactFlash*® card. The fan unit design life and the *CompactFlash*® card design life are 7 years.

CompactFlash® cards can be ordered separately and have their own comcode:

- *CompactFlash*® card 512 MB can be ordered using the comcode: 109449710
- *CompactFlash*® card 1 GB can be ordered using the comcode: 109763557 (from software release 10.5.5 on) or 109558544 (for earlier software releases), respectively

Maintainability specifications

Hardware items that need maintenance activities:

- The air filter, located below the subrack, must be replaced or cleaned under regular conditions (e.g. with Eurovent EU6 filters used in the HVAC) once every 3 months to ensure the proper cooling, as described in the *1675 LambdaUnite MSS User Provisioning Guide*, chapter “Supporting procedures”, or as part of a trouble-clearing procedure as described in the *1675 LambdaUnite MSS Maintenance and Trouble-clearing Guide*.
- The *CompactFlash*® card, located in the controller circuit pack, is strongly recommended to be replaced once every seven years; the respective procedure is described in the *1675 LambdaUnite MSS Maintenance and Trouble-clearing Guide* in the chapter “Supporting procedures”.

1675 LambdaUnite MSS does not require periodic electronic equipment maintenance activities, except the *CompactFlash*® card replacement. Continuous performance monitoring enables the system to detect conditions before they become service-affecting.

1675 LambdaUnite MSS failure-in-time rates

This section provides failure-in-time (FIT) rates for 1675 LambdaUnite MSS components, the calculated number of failures in 10^9 hours of operation.

FIT rates of core units

Unit suite	Unit description	Apparatus code	FIT rate
Subrack	Shelf	–	150
	FAN unit	–	2765
	PI (power interface 63A)	PBH1	203
	PI100 (power interface 100A)	PBH3	110
	UPL (user panel)	–	193
	CI-CTL (controller interface)	PBJ1	1366
	TI (E1/DS1 timing interface)	PBI1	100
CTL	Controller CTL/-	KFA1	6000
	Controller CTL/2	KFA531	9545
	Controller CTL/3T	KFA536(B)[1]	6644
	Controller CTL/3S	KFA537(B)[1]	6644
	Controller CTL/4T	KFA538	6114
	Controller CTL/4S	KFA539	6114
XC	Switching unit XC160	KFD3	7523
	Switching unit XC320/B	KFD1B	11289
	Switching unit XC640	KFD2	11063
	Lower-order cross-connection matrix LOXC/1	KFA700	5776
	lower-order cross-connection unit LOXC40G2S/1	KFA702	4493.1
	Lower-order cross-connection unit LOXC40G3S/1	KFA703	4493.1

Notes:

1. The letter “B” indicates variants that are delivered without *CompactFlash*® card.

FIT rates of transmission units

Unit suite	Unit description	Apparatus code	FIT rate
OP10	OP10/1.3IOR1, 600 m	KFA7	4100
	OP10/1.5IR1, 40 km	KFA14	4517
	OP10/1.5LR1, 80 km	KFA6	5517
	OP10/01…80/800G	KFA9, KFA81…*	5347
	OP10/9285…8650	KFA210…*	5674
	OP10/XTTC	KFA361	5621
	OP10/XTTL	KFA362	5621
OP10D	OP10D/PAR2 (20-Gbit/s per slot parent board)	KFA630	6500
	OP10/PAR1XFP (Parent board 1 XFP)	KFA631	3305
	OP10D/PAR2XFP (Parent board for up to 2 XFP)	KFA632	3834
	OM10 (600m slide-in module)	OM10G7	2200
	OM10 (40km slide-in module)	OM10G14	2617
	OM10 (80km slide-in module)	OM10G6	2800
	OMX10/10KM1 (XFP, 10GBASE-LR / I-64.1)	OMX10G10	1086
	OMX10/40KM1 (XFP, 10GBASE-ER / S-64.2b)	OMX10G40	1261
	OMX10/80KM1 (XFP, 10GBASE 8o km /P1L1-2D2/LR-2c))	OMX10G80	836
OP2G5	OP2G5/1.5LR4, 80 km	KFA204	6011
	OP2G5/1.3LR4, 40 km	KFA203	6011
	OP2G5/1.3SR4, 2 km	KFA12	6011
	OP2G5 pWDM (sum of below)	–	4504
	OP2G5 parent	KFA20	2665
	OM2G5/921…*PWDM	OM2G5A921…*	1839
	OM2G5/CL…*CWDM	OM2G5/CL…	
OP2G5D	OP2G5/PAR8 (parent board for eight SFP modules)	KFA620	3875
	OM2G5 SFP module 2 km	OM2G5A12B	373
	OM2G5 SFP module 40 km	OM2G5A203B	373
	OM2G5 SFP module 80 km	OM2G5A204B	373
OP2G5/PAR4	OC48/STM16, Parent board for 4 SFP modules(without SFP modules)	KFA621	3482
OPT	OPT2G5/PAR3 (transparent 2.5-Gbit/s parent board for three SFP modules)	KFA540	4667

Unit suite	Unit description	Apparatus code	FIT rate
GE1	GE1/SX4	KFA13	4270
	GE1/LX4	KFA532	4270
GE10PL1	GE10PL1/1A8	KFA720	5993
	OMGE1/SX1	OMSX1	273
	OMGE1/LX1	OMLX1	201
	OMGE1/ZX1	OMZX1	201
OP622	OP622 w/16 ports, 15 km	KFA17	8737
OP155M	OP155M w/16 ports, 15 km	KFA18	8737
OPLB	OPLB/PAR8 (622/155M parent board for 8 SFPs)	KFA180	4137
	OM622 SFP module, 80 km	OM622A180	235
	OM622 SFP module, 40 km	OM622A181B	200
	OM622 SFP module, 15 km	OM622A182B	200
	OM155 SFP module, 80 km	OM155A185	300
	OM155 SFP module, 40 km	OM115A183B	200
	OM155 SFP module, 15 km	OM115A184B	200
EP 155	EP155/EL8 (electrical STM-1 pack, 8 signals)	KFA533	2139
	ECI/155ME8 (paddle board w/16 ports)	PBK1	147
	ECI/155MP8 (protection paddle board w/8 ports)	PBK2	417
EP 51	EP51/EL36 (electrical DS3/EC1 pack, 36 signals)	KFA535	4406
	EP51/EL36B (electrical DS3/EC1 pack, 36 signals)	KFA535B	4406
	ECI51/MP72 (protection paddle board w/72 ports)	PBK4	925
TMUX2G5/1	Portless TransMUX unit	KFA710	5487

For the fields marked with “n.a.” data was *n*ot *a*vailable on the issue date.

*: For the complete apparatus code list and the respective comcodes please refer to “Engineering Drawing” (p. 7-2).

10 Technical specifications

Overview

Purpose

This chapter provides the technical specifications for 1675 LambdaUnite MultiService Switch (MSS). These data are necessary for planning the application of a 1675 LambdaUnite MSS network element in an existing or new network.

Contents

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Interfaces

Standards compliance

1675 LambdaUnite MSS is compliant with the following standards:

	SONET	SDH	Ethernet
General	ANSI T1.105-1191, T1.1066-1988	ITU-T Rec. G.707, G.703	IEEE 802.3-2000
Equipment		ITU-T Rec. G.781, G.782, G.783, G.784, G.813	
Physical interface	Bellcore GR-253 SR-1	ITU-T Rec. G.957, G.691, G.692, G.693, G.959.1	IEEE 802.3-2000 clause 38
Performance requirements	*Telcordia*™ GR-253-CORE	ITU-T Rec. G.823, G.825, G.826	

Optical interfaces

The detailed specifications of the optical interfaces can be found in “Transmission parameters ” (p. 10-6).

Data interfaces

The following table lists the data interfaces:

Standard External clock interfaces (Input)	2 physical separated interfaces configurable to 2 MHz (G.703.10), 2 Mbit/s (G.703.6), or DS1. The impedance of the interfaces of 75 Ω (coaxial) or 120 Ω (symmetrical) is coded by the pins used
Standard External clock interfaces (Output)	2 physical separated interfaces configurable to 2 MHz (G.703.10), 2 Mbit/s (G.703.6), or DS1. The impedance of the interfaces of 75 Ω (coaxial) or 120 Ω (symmetrical) is coded by the pins used
Orderwire	E1, E2 bytes as 64-kbit/s data channel at G.703
User Channel	F1 byte as 64-kbit/s data channel at V.11

Station alarm interfaces

The station alarm interface offers six isolated contact output pairs: Critical (visual, audible), Major (visual, audible), Minor (visual, audible) which can be used by the customer to extend the alarm signals from the system into the station alarm scheme. The critical contact can be configured to be active without system power. The contacts are able to switch 0.5 A at –72 V and 2 A at –30 V and are ESD safe up to 2 kV.

Miscellaneous discrete interfaces

The system supports 8 MDI and 8 MDO ports. All ports are configurable to be isolated or non isolated. The output ports are capable to switch 0.5 A at –72 VDC and 2 A at –30 VDC and are ESD safe up to 2 kV. The input ports are sensitive to passive switches ($R_{on} \leq 50\ \Omega$, $R_{off} \geq 20\ k\Omega$) or input voltages up to –72 VDC (threshold voltage –3 VDC to –10 VDC) and are ESD safe up to 2 kV.

Orderwire and user bytes

Introduction

Each 1675 LambdaUnite MSS network element provides six user byte interfaces which can be used to access certain overhead bytes of an SDH/SONET transport signal. If desired, an external orderwire equipment can be connected to these interfaces for example.

User bytes can be provisioned by associating or disassociating an SDH/SONET port with a user byte interface.

Important! Orderwire and user byte access is supported by the following port units *only*:

- EP155
- OP155
- OPLB
- OP10

Overhead bytes

Each SDH/SONET port supports three dedicated user bytes:

- The *E1* byte in the SDH Regenerator Section Overhead (RSOH) or SONET Section Overhead (SOH) respectively can be used as a 64-kbit/s orderwire channel.
- The *E2* byte in the SDH Multiplex Section Overhead (MSOH) or SONET Line Overhead (LOH) respectively can be used as a 64-kbit/s orderwire channel.
- The *F1* byte in the SDH Regenerator Section Overhead (RSOH) or SONET Section Overhead (SOH) respectively can be used as a 64-kbit/s user data channel.

Each shelf provides user-provisionable access to up to six of these bytes from among the provisioned SDH/SONET ports in the shelf.

User byte interfaces

The six user byte interfaces are available on the CI-CTL (Connection Interface of the Controller; accessible from the rear side of the shelf).

There are four universal interfaces which can be used as G.703 co-directional or V.11 contra-directional interfaces. Two interfaces can be used as V.11 only.

These interfaces can be used as G.703 or V.11 interfaces:

- User byte interface #1 (USERBIO1)
- User byte interface #2 (USERBIO2)
- User byte interface #3 (USERBIO3)
- User byte interface #4 (USERBIO4)

These interfaces can only be used as V.11 interfaces:

- User byte interface #5 (USERBIO5)
- User byte interface #6 (USERBIO6)

Pin assignment

The pin assignment of the user byte interfaces is described in the *1675 LambdaUnite MSS Installation Guide*.

Transmission parameters

Planning data

Data for planning a transmission route with the signal transmitters and receivers is listed in the following tables. With these data it is possible to determine the maximum link distance between the network elements. The abbreviations used are explained at the end of this section, see "Port unit designation" (p. 10-31) and following.

Connector type

In 1675 LambdaUnite MSS all optical connections are provided by LC connectors. Please refer also to "Equipment interconnection" (p. 6-28).

10-Gbit/s single color circuit packs and modules for SDH/SONET applications

The following table lists the parameters and the end of life power budget of the single channel optical interfaces for 10-Gbit/s signals.

Application code	Unit	VSR600-2R1 (G.693)	S-64.2b/3b IR-2/3	L-64.2b/3 LR-2b/3	P1L1-2D2 (G.959.1)
Functional name (Apparatus code)		OP10/1.3IOR1 (KFA7), OM10/1.3IOR1 (OM10G7), OMX10/10KM1; see also table note 1.	OP10/1.5IR1 (KFA14), OM10/1.5IR1 (OM10G14), OMX10/40KM1; see also table note 1.	OP10/1.5LR1 (KFA6)	OM10/1.5LR1 (OM10G6), OMX10/80KM1; see also table note 1.
SONET level / SDH level		OC-192 / STM-64	OC-192 / STM-64	OC-192 / STM-64	OC-192 / STM-64
Type of plug-in unit		OP101.3Ir	OP101.5SH	OP101.5LH	OM101.5LH
Target distance	km	0.6/2	40	80	80
Transmission rate	kbit/s	9 953 280	9 953 280	9 953 280	9 953 280
Transmission code		NRZ	NRZ	NRZ	NRZ
Wavelength	nm	1260…1360; see also table note 2.	1530…1565; see also table note 3.	1530…1565; see also table note 3.	1530…1565; see also table note 3.
Transmitter at reference point S and MPI-S (acc. G.691 or G.693) respectively					
Source type		MLM	SLM	SLM	SLM
Max. spectral RMS width	nm	3	—	—	—
Min. side mode suppression	dB	—	30	30	30
Mean launched power range	dBm	–6…– 1 (class 1)	–1…+2 (class 1)	+10…+13 (class IIIb/1M)	+0…+4 (class 1)
Minimum Extinction ratio	dB	6	8.2	8.2	9

Application code	Unit	VSR600-2R1 (G.693)	S-64.2b/3b IR-2/3	L-64.2b/3 LR-2b/3	P1L1-2D2 (G.959.1)
Receiver at reference point R and MPI-R (acc.G.691) respectively					
Receiver type		PIN	PIN	PIN	APD
Min. optical sensitivity (BER $=10^{-12}$)	dBm	–11	–14	–14	–24
Max. optical path penalty	dB	1	2 (SSMF), 1 (DSF and NZ-DSF)	2 (SSMF), 1 (DSF and NZ-DSF)	2
Overload limit	dBm	–1	–1	–1	–7
Maximum reflectance of receiver	dB	–14	–27	–27	–27
Optical path between S and R					
Minimum optical return loss of cable at point S (incl. any connectors)	dB	14	24	24	24
Maximum discrete reflectance between S and R	dB	–27	– 27	–27	–27
Maximum chromatic dispersion	ps/nm	3.8	800	1600	1600
Maximum tolerable differential group delay	ps	30	30	30	30
Optical attenuation range (BER $=10^{-12}$)	dB	0…4	3…11	14…22	11…22
Nominal target distance	km	0.6	40	80	80

Notes:

1. The OMX10/10KM1, OMX10/40KM1 and OMX10/80KM1 XFP optical interface modules can be used for SDH/SONET as well as Gigabit Ethernet applications.
2. The receiver also supports 1.5 µ signals with slight span length restrictions; for further information please contact your local customer service.
3. The receiver also supports 1.3 µ signals with slight span length restrictions; for further information please contact your local customer service.

10-Gbit/s WDM direct optics circuit packs

The following table lists the End of Life power budget of the dense and passive WDM compatible optical interfaces for 10-Gbit/s signals. For the spectral parameters please refer to “Engineering Drawing” (p. 7-2).

Application code	Unit	*WaveStar*® OLS 1.6T compatible
Functional name/qualifier		OP10/01...80/800G
Apparatus code		KFA9 / KFA81-159

Application code	Unit	*WaveStar*® OLS 1.6T compatible
SONET level / SDH level		OC-192 / STM-64
Transmission rate	kbit/s	9 953 280 with strong FEC: 10 619 608
Transmission code		NRZ
Frequencies	THz	191.9…195.8 (80 colors); the receiver also supports 1.3 µ signals with slight span length restrictions; for further information please contact your local customer service.
Transmitter at reference point S and MPI-S (acc. G.691) respectively		
Max. spectral width (-20dB)	nm	—
Min. side mode suppression	dB	35
Mean launched power range	dBm	–6.2…–3.8 (class 1)
Minimum Extinction ratio	dB	12
Receiver at reference point R and MPI-R (acc.G.691) respectively		
Receiver type		APD
Input power range (BER =10^{-12})	dBm	–20...–13
Maximum input power	dBm	–13
Max. optical path penalty due to chromatic dispersion	dB	2
Maximum OSNR (BER =10^{-12})	dB	19.5
Maximum reflectance of receiver	dB	–27
Optical path between S and R		
Minimum optical return loss of cable at point S (incl. any connectors)	dB	n/a
Maximum discrete reflectance between S and R	dB	n/a
Maximum chromatic dispersion	ps/nm	1000
Maximum tolerable differential group delay	ps	30
Optical attenuation range (10*12)	dB	n/a
Nominal target distance	km	n/a

10-Gbit/s XFP modules for SDH/SONET or Gigabit Ethernet applications

Please note that the OMX10/10KM1, OMX10/40KM1 and OMX10/80KM1 XFP optical interface modules can be used for SDH/SONET as well as Gigabit Ethernet applications.

The following table lists the parameters and the End of Life power budget of the 10-Gbit/s Extended Form-factor Pluggable (XFP) interface modules.

		OC-192/STM-64 intra office 1.3 µm optics	OC-192/STM-64 intermediate reach / SH 1.5 µm optics - single color interface	OC-192/STM-64 long reach / LH 1.5 µm optics - single color interface.
	Unit	Value	Value	Value
Apparatus code		OMX10/10KM1	OMX10/40KM1	OMX10/80KM1
Application code		I.64-1	S.64-2/3	P1L1-2D2
Fibre type		G.652, G.655	G.652, G.655	G.652, G.655
Source Type		SLM	SLM	SLM
Operating wavelength	nm	1290…1330	1530…1565	1530…1565
Target distance	km	Depending on the application: 2 km, cover also 9.6 km (for SDH/SONET applications)	40	80
Transmission rate	Gbit/s	9.95328	9.95328	9.95328
Transmission code		NRZ	NRZ	NRZ
Minimum side mode suppression ratio	dB	30	30	30
Maximum mean output power:	dBm-	–1	2	4
Minimum mean output power:	dBm	–6	–1	0
Minimum extinction ratio:	dB	6	8.2	9
Maximum attenuation:	dB	4	11	22
Minimum attenuation:	dB	0	3	11
Maximum chromatic dispersion:	ps/nm	6.6	800	1600
Minimum optical return loss at MPI-S	dB	14	24	24
Maximum discrete reflectance between MPI-S and MPI-R	dB	–27	–27	–27
Minimum sensitivity:	dBm	–11	–14	–24
Overload limit	dBm	–1	–1	–7
Maximum optical path penalty:	dB	1	2	2
Maximum reflectance of optical network element:	dB	–14	–27	–27

OP10/XTT Pack

The OP10/XTT is a 1675 LambdaUnite MSS circuit pack with Xtreme compatible optics with tunable laser.

There are two main differences of the functionality of this pack compared to existing OP10 variants:

1. The laser on the pack is tunable, i.e. the wavelength used by the laser should be configurable by command.
2. The transmission traffic terminated on the pack is not traffic according SONET/SDH standards, but according OTN standards.

OP10/XTT has two variants.

- OP10/XTTC
- OP10/XTTL

Tunable wavelength

The tunable wavelength bands depend on the OP10 variants:

- OP10/XTTC Pack
  The possible values for the optical wave length are 1554.537…1568.362 (35 values in C-Band)
- OP10/XTTL Pack
  The possible values for the optical wave length are 1568.773…1607.466 (93 values in L-Band)

Transmission characteristics

The following table lists the optical parameters of the TX module of the Xtreme compatible optics with tunable laser

Parameters	MIN	Typ	MAX	Unit
OPTICAL				
Output Power	–4.0		–1.8	dBm
Peak Frequency (C+L band) (in 50 GHz increments) (λ)	186.5		192.85	THz
Frequency Stability (25 years) (with stabilization)	–2.5		2.5	GHz
Frequency Stability (25 years)	–12.5		12.5	GHz
SSR	35			dB
Extinction Ratio (Filtered)	12			dB
Modulator Chirp Factor (a)	–0.2		0.2	
Total Optical Path Penalty (P_O)			2.5	dB
Residual dispersion			600	ps/nm
Optical Rise/Fall Time (20 to 80%)			25	ps
Optical Zero Crossing Jitter (wideband)			20	ps
Optical Pulse Width	44	47	50	ps

Parameters	MIN	Typ	MAX	Unit
Jitter Generation (50kHz to 80 MHz)			0.1	UI
Optical Isolation (0 to 65°C)	25			dB
ELECTRICAL				
Tone Modulation Index	2.5		2.9	%
TX Module Case Temperature	0		70	°C

Receive characteristics

The following table lists the optical parameters of the RX module of the Xtreme compatible optics with tunable laser

Optical Specification	MIN	Typ	MAX	Unit
Optical input power range to meet Optical Signal to Noise Ratio requirement.				
Standard dynamic range	–11.5	–7	–1.5	dBm
Optical Signal to Noise Ratio for 1E-10 BER Opt. filter 3dB BW=0.6nm and Tx ER > 13dB				
Standard dynamic range			17.2	dB
EOL OSNR for 6E-5 BER Opt. filter 3dB BW=0.6nm and Tx ER > 13dB				
Standard dynamic range			13.1	dB
Transient tolerance for 1dB Optical Signal to Noise Ratio (OSNR) penalty				
Rise and Fall time		10		µs
Received Power divergence		10		dB
Recovery time for brief loss of signal up to 3 ms		200		µs
Jitter Tolerance		GR-253		UI
Wavelength	1520	1580	1610	nm
Optical Return Loss		27		dB
DGD for 1dB OSNR penalty at BER=1e-10		45		ps

10 Gbit/s XFP modules for pWDM applications

The following table lists some parameters and the end of life power budget of the pWDM XFPs for 10-Gbit/s signals.

Application code	Unit	DW100S-2D2(C) DW100S-2D5(C)
Functional name/qualifier		OM10G/59WDM1…OM10G/21WDM1

Application code	Unit	DW100S-2D2(C) DW100S-2D5(C)
Apparatus code		OM10G59…OM10G21
Max. number of channels		16
Fibre type		G.652, G.655
Transmitter at reference point S and MPI-S (acc. G.957) respectively		
Source type		SLM
Min. central frequency	THz	192.1
Max. central frequency	THz	195.9
Channel spacing	GHz	100
Max. center wavelength deviation	pm	±100
Max. spectral width at –15 dB	nm	0.12
Max. spectral excursion at –15 dB	GHz	±20
Min. side mode suppression ratio	dB	30
Max. mean launched channel power	dBm	+3
Min. mean launched channel power	dBm	–1
Min. channel extinction ratio	dB	8.2
Receiver at reference point Rs		
Operating wavelength range	nm	1528…1565
Min. receiver sensitivity	dBm	–24
Max. optical path penalty	dB	2.5
Min. overload	dBm	–7
Min. OSNR with 0 ps/nm for an input power range from –17 dBm to –10 dBm	dB	24
Min. OSNR with 1300 ps/nm for an input power range from –17 dBm to –10 dBm	dB	26
Max. OSNR penalty due to chromatic dispersion and PMD	dB	3
Maximum reflectance of receiver	dB	–27
Optical path between Ss and Rs		
Attenuation range	dB	10…21
Minimum optical return loss of cable at point Ss (incl. any connectors)	dB	24
Maximum differential group delay	ps	10
Maximum chromatic dispersion	ps/nm	+1300
Max. inter-channel crosstalk	dB	–16
Max. interferometric crosstalk	dB	–45

2.5-Gbit/s single color interfaces

The following table lists some parameters and the end of life power budget of the optical interfaces for 2.5-Gbit/s signals.

Application code	Unit	I-16 SR	L-16.1 LR-1	L-16.2 LR-2
Functional name/qualifier		OP2G5/1.3SR4 and OM2G5/1.3SR1	OP2G5/1.3LR4 and OM2G5/1.3LR1	OP2G5/1.5LR4 and OM2G5/1.5LR1
Apparatus code		KFA12 and OM2G5A12	KFA203 and OM2G5A203	KFA204 and OM2G5A204
SONET level / SDH level		OC-48 / STM-16	OC-48 / STM-16	OC-48 / STM-16
Transmission rate	kbit/s	2488,320	2488,320	2488,320
Transmission code		NRZ	NRZ	NRZ
Wavelength	nm	1266…1360	1280…1335	1530…1560; the receiver also supports 1.3 µ signals with slight span length restrictions; for further information please contact your local customer service.
Transmitter at reference point S and MPI-S (acc. G.957) respectively				
Source type		MLM	SLM	SLM
Max. spectral RMS width	nm	4	n/a	n/a
Spectral width at –20 dB	nm	n/a	<1	<1
Min. side mode suppression ratio	dB	n/a	30	30
Mean launched power range	dBm	-10…-3 (class 1)	-1…+2 (class 1)	-1…+2 (class 1)
Minimum Extinction ratio	dB	8.2	8.2	8.2
Receiver at reference point R and MPI-R (acc.G.957 and G.959.1) respectively				
Receiver type		PIN	APD	APD
Min. optical sensitivity (BER $=10^{-10}$)	dBm	–18	–27	–28
Max. optical path penalty	dB	1	1	2 / 1 (L-16.3)
Overload limit	dBm	–3	–9	–9
Maximum reflectance of receiver	dB	–14	–27	–27

Application code	Unit	I-16 SR	L-16.1 LR-1	L-16.2 LR-2
Optical path between S and R				
Minimum optical return loss of cable at point S (incl. any connectors)	dB	24	24	24
Maximum discrete reflectance between S and R	dB	-27	-27	-27
Maximum chromatic dispersion	ps/nm	12	na	1600 / 600 (L-16.3)
Optical attenuation range	dB	0…7	12…24	12…24 / 25 (L-16.3)
Nominal target distance	km	2	40	80

2.5-Gbit/s optical pWDM modules

The following table lists some parameters and the End of Life power budget of the pWDM optical interfaces for 2.5-Gbit/s signals. For the spectral parameters please refer to "Engineering Drawing" (p. 7-2).

Application code	Unit	optical modules for OP2G5/PARENT pWDM board
Functional name/qualifier		OM2G5-921…959
Apparatus code		OM2G5-921…959
SONET level / SDH level		OC-48 / STM-16
Transmission rate	kbit/s	2488320
Transmission code		NRZ
Wavelength	nm	1530...1560 (32 wavelengths); the receiver also supports 1.3 µ signals with slight span length restrictions; for further information please contact your local customer service.
Transmitter at reference point S and MPI-S (acc. G.957) respectively		
Source type		SLM
Max. spectral RMS width	nm	n/a
Spectral width at –20 dB	nm	1
Min. side mode suppression ratio	dB	30
Mean launched power range	dBm	–3…0 (class 1)

Application code	Unit	optical modules for OP2G5/PARENT pWDM board
Minimum Extinction ratio	dB	8.2
Receiver at reference point R and MPI-R (acc.G.957) respectively		
Receiver type		APD
Min. optical sensitivity (BER =10^{-10})	dBm	–28
Max. optical path penalty	dB	2 (G.652 fiber) / 1 (G.655 fiber)
Overload limit	dBm	–8
Maximum reflectance of receiver	dB	–27
Optical path between S and R		
Minimum optical return loss of cable at point S (incl. any connectors)	dB	n/a
Maximum discrete reflectance between S and R	dB	n/a
Maximum chromatic dispersion	ps/nm	2400 (G.652 fiber) / 600 (G.655 fiber)
Optical attenuation range (BER =10^{-12})	dB	8...21.5 (G.652 fiber) / 8...22.5 (G.655 fiber)
Nominal target distance	km	40

2.5-Gbit/s optical CWDM modules

The following table lists some parameters and the End of Life power budget of the Coarse WDM optical interfaces for 2.5-Gbit/s signals. For the spectral parameters please refer to "Engineering Drawing" (p. 7-2).

		OC48/STM16 CWDM compatible optics (40km)	OC48/STM16 CWDM compatible optics (80km)
	Unit	Value	Value
Application code		S-C8S1-1D2 S-C8S1-1D5	S-C8L1-1D2 S-C8L1-1D5
Maximum number of channels		8	8
Fibre type		G.652, G.655	G.652, G.655
Transmitter at reference point S			
Source Type		SLM	SLM
Nominal center wavelength	nm	1471 + 20*m m = 0 to 7	1471 + 20*m m = 0 to 7
Channel spacing	nm	20	20
Maximum center wavelength deviation	nm	±6.5	±6.5
Maximum -20 dB spectral width	nm	1	1

		OC48/STM16 CWDM compatible optics (40km)	OC48/STM16 CWDM compatible optics (80km)
	Unit	Value	Value
Minimum side mode suppression ratio	dB	30	30
Maximum mean launched channel power	dB	5	5
Minimum mean launched channel power	dB	0	0
Minimum channel extinction ratio	dB	8.2	8.2
Eye mask		STM-16 per G.957	STM-16 per G.957
Optical path between point S and R			
Attenuation range	dB	5 -16.5	14 -25.5
Maximum chromatic dispersion	ps/nm	1000	1600
Minimum optical return loss at S	dB	24	24
Maximum differential group delay	ps	120	120
Maximum inter-channel crosstalk	dB	20	20
Maximum interferometric crosstalk	dB	45	45
Receiver at reference point R			
Minimum receiver sensitivity	dBm	-18	-28
Minimum overload	dBm	0	-9
Maximum optical path penalty	dB	1.5	2.5
Maximum reflectance of receiver	dB	-27	-27

2.5-Gbit/s SFP modules for pWDM applications

The following table lists some parameters and the end of life power budget of the pWDM SFPs for 2.5-Gbit/s signals.

Application code	Unit	DW100L-1D2(C) DW100L-1D5(C)
Functional name/qualifier		OM2G5/59WDM1…OM2G5/21WDM1
Apparatus code		OM2G5A59…OM2G5A21
Max. number of channels		16
Fibre type		G.652, G.655
Transmitter at reference point S and MPI-S (acc. G.957) respectively		
Source type		SLM
Min. central frequency	THz	192.1
Max. central frequency	THz	195.9

Application code	Unit	DW100L-1D2(C) DW100L-1D5(C)
Channel spacing	GHz	100
Max. center wavelength deviation	pm	±100
Max. sepctral width at –15 dB	nm	0.18
Max. spectral excursion at –15 dB	GHz	±20
Min. side mode suppression ratio	dB	30
Max. mean launched channel power	dBm	+4
Min. mean launched channel power	dBm	0
Min. channel extinction ratio	dB	8.2
Receiver at reference point Rs		
Min. receiver sensitivity	dBm	–28
Max. optical path penalty	dB	2.5
Min. overload	dBm	–9
Min. OSNR with 0 ps/nm for an input power range from -24 dBm to -10 dBm	dB	16
Min. OSNR with 2400 ps/nm for an input power range from -24 dBm to -10 dBm	dB	19
Max. OSNR penalty due to chromatic dispersion and PMD	dB	3
Maximum reflectance of receiver	dB	–27
Optical path between Ss and Rs		
Attenuation range	dB	13…25.5
Minimum optical return loss of cable at point Ss (incl. any connectors)	dB	24
Maximum differential group delay	ps	40
Maximum chromatic dispersion	ps/nm	+2400
Minimum chromatic dispersion	ps/nm	–600
Max. inter-channel crosstalk	dB	–16
Max. interferometric crosstalk	dB	–45

622-Mbit/s optical interfaces

The following table lists some parameters and the End of Life power budget of the 622-Mbit/s optical interface units.

Application code	Unit	S-4.1 / IR-1	L-4.2/ L-4.3 (LR-2 / LR-3)	L-4.1 / LR-1
Functional name/qualifier		OP622/1.3IR16 and OM622/1.3IR1	OM622/1.5LR1	OM622/1.3LR1
Apparatus code		KFA17 and OM622A182	OM622A180	OM622A181
SONET level / SDH level		OC-12 / STM-4	OC-12 / STM-4	OC-12 / STM-4
Transmission rate	kbit/s	622080	155520	622080
Transmission code		NRZ	NRZ	NRZ
Wavelength	nm	1274…1356	1480 …1580	1280…1335
Transmitter at reference point S and MPI-S (acc. G.957) respectively				
Source type		MLM	SLM	SLM
Max. spectral RMS width	nm	2.5	n/a	n/a
Spectral width at –20 dB	nm	n/a	1	1
Min. side mode suppression ratio	dB	n/a	30	30
Mean launched power range	dBm	–15…–8 (class 1)	–3…+2 (class 1)	–3…+2 (class 1)
Minimum Extinction ratio	dB	8.2	10	10
Receiver at reference point R and MPI-R (acc.G.957) respectively				
Receiver type		n/a	n/a	n/a
Min. optical sensitivity (BER $=10^{-10}$)	dBm	–28	-28	–28
Max. optical path penalty	dB	1	1	1
Overload limit	dBm	–8	-8	–8
Maximum reflectance of receiver	dB	n/a	-25	14
Optical path between S and R				
Minimum optical return loss of cable at point S (incl. any connectors)	dB	n/a	n/a	n/a
Maximum discrete reflectance between S and R	dB	n/a	n/a	n/a
Maximum chromatic dispersion	ps/nm	74	1600	172

Application code	Unit	S-4.1 / IR-1	L-4.2/ L-4.3 (LR-2 / LR-3)	L-4.1 / LR-1
Optical attenuation range	dB	0…12	10…24	10…24
Nominal target distance	km	15	80	40

155-Mbit/s optical interfaces

The following table lists some parameters and the End of Life power budget of the 155-Mbit/s optical interface units.

Application code	Unit	S-1.1 / IR-1	L-1.2/ L-1.3 (LR-2 / LR-3)	L-1.1 / LR-1
Functional name/qualifier		OP155M/1.3IR16, and OM155/1.3IR1	OM155/1.5LR1	OM155/1.3LR1
Apparatus code		OM155A184 and KFA18	OM155A185	OM155A183
SONET level / SDH level		OC-3 / STM-1	OC-3 / STM-1	OC-3 / STM-1
Transmission rate	kbit/s	155520	622080	155520
Transmission code		NRZ	NRZ	NRZ
Wavelength	nm	1261…1360	1480 …1580	1263…1360
Transmitter at reference point S and MPI-S (acc. G.957) respectively				
Source type		MLM	SLM	SLM
Max. spectral RMS width	nm	7.7	n/a	n/a
Spectral width at –20 dB	nm	n/a	1	1
Min. side mode suppression ratio	dB	n/a	30	30
Mean launched power range	dBm	–15…–8 (class 1)	–5…0 (class 1)	–5…0 (class 1)
Minimum Extinction ratio	dB	8.2	10	10
Receiver at reference point R and MPI-R (acc.G.957) respectively				
Receiver type		n/a	n/a	n/a
Min. optical sensitivity (BER =10^{-10})	dBm	–28	-34	–34
Max. optical path penalty	dB	1	1	1
Overload limit	dBm	–8	-10	–10
Maximum reflectance of receiver	dB	n/a	-25	n/a

Application code	Unit	S-1.1 / IR-1	L-1.2/ L-1.3 (LR-2 / LR-3)	L-1.1 / LR-1
Optical path between S and R				
Minimum optical return loss of cable at point S (incl. any connectors)	dB	n/a	20	20
Maximum discrete reflectance between S and R	dB	n/a	–25	n/a
Maximum chromatic dispersion	ps/nm	96	n/a	n/a
Optical attenuation range	dB	0…12	10…28	10…28
Nominal target distance	km	15	80	40

EP155 electrical circuit packs

The following table lists some parameters and the End of Life power budget of the 155-Mbit/s electrical interface units.

Application	Unit	intra-office
Functional name/qualifier		EP155/EL8
Apparatus code		KFA533
SDH Level	type	STM-1
Transmission rate	kbit/s	155,520 ± 20 ppm
Line coding	type	Bipolar with Coded Mark Inversion (CMI, G.703-12)
Return Loss (8…240 MHz.)	dB	15
Maximum cable attenuation (78 MHz)	dB	12.7

EP51 electrical circuit packs

The following table lists some parameters and the End of Life power budget of the EP51 electrical interface units.

Application	Unit	intra-office
Functional name/qualifier		EP51/EL36
Apparatus code		KFA535
SONET/SDH Level	type	DS3/EC1

Application	Unit	intra-office
Transmission rate	kbit/s	44,736 ±20 ppm respectively 51,840 ±20 ppm
Line coding	type	Bipolar with 3 Zero Substitution (B3ZS)
Return Loss	dB	18 in the range 2,24…44,736 kHz 14 in the range 44,736…67,104 kHz
Maximum cable attenuation (10 MHz)	dB/100 ft	0.8 for WE 728/734 cables 1.7 for WE 735 cables

Gigabit Ethernet short reach circuit pack

The GE1/SX4 port unit supports 4 fully independent bidirectional ports. Ethernet frames received from a GE1/SX4 port are mapped into STS-1s or VC-4s using Virtual Concatenation. The number of STS1s/VC-4s per virtual concatenated signal can be user provisioned as ≤ 21 STS1s/7 VC-4s at single STS1/VC-4s intervals. This will offer an effective capacity usage over a network from 50/155 to 1000 Mbit/s in steps of 50/155 Mbit/s.

The GE1/SX4 port unit supports standard BLSR/MS-SPRing and UPSR/SNCP protection schemes on the individual STS1s/VC-4s that are part of the Virtually Concatenated signal.

The GE1/SX4 port unit uses a Low Power Laser (laser class 1/1 according to FDA/CDRH - 21 CFR 1010 & 1040 / IEC 60825).

The GE1/SX4 port unit complies with IEEE 802.3-2000 Clause 38.

The table below describes the various operating ranges for the GE1/SX4 port unit over each optical fiber type.

Fiber Type	Modal Bandwidth @ 850 nm (min. overfilled launch) (MHz*km)	Minimum range (meters)
62.5 µm MMF	160	2 to 220
62.5 µm MMF	200	2 to 275
50 µm MMF	400	2 to 500
50 µm MMF	500	2 to 550
10 µm MMF	N/A	Not supported

The following table lists the specific transmission characteristics for a GE1/SX4 port unit.

Description	Unit	
Apparatus code	KFA13	

Description	Unit	
Transmitter type		Shortwave Laser
Signaling speed (range)	GBd	1.25 ± 100 ppm
Wavelength (range)0	nm	770 to 860
T_{rise}/T_{fall} (max, 20–80%, $\lambda > 830$ nm)	ns	0.26
T_{rise}/T_{fall} (max, 20–80%, $\lambda \leq 830$ nm)	ns	0.21
RMS spectral width (max)	nm	0.85
Average launch power (max)	dBm	-1.1 (limit for class 1).
Average launch power (min)	dBm	–9.5
Average launch power of OFF transmitter (max)	dBm	–30 (During all conditions when the PMA is powered in the OFF mode, the AC signal (data) into the transmit port will be valid encoded 8B/10B patterns except for short durations during system power-on-reset or diagnostics when the PMA is placed in a loopback mode.)
Extinction ratio (min)	dB	9
RIN (max)	dB/Hz	–117
Coupled Power Ratio (CPR) (radial overfilled launches, while they meet CPR ranges, should be avoided)	dB	9 < CPR

The following table lists the specific receive characteristics for a GE1/SX4 port unit.

Description	Unit	62.5 µm MMF	50 µm MMF
Signaling speed (range)	GBd	1.25 ± 100 ppm	
Wavelength (range)	nm	770 to 860	
Average receive power (max)	dBm	0	
Receive sensitivity	dBm	–17	
Return loss (min)	dB	12	
Stressed receive sensitivity (measured with conformance test signal at TP3 for BER = 10^{-12} at the eye center) (measured with a transmit signal having a 9 dB extinction ratio; if another extinction ratio is used, the stressed received sensitivity should be corrected for the extinction ratio penalty)	dBm	–12.5	–13.5

The following table lists the worst-case power budget and link penalties for a GE1/SX4 port unit. Link penalties are used for link budget calculations.

Description	Unit	62.5 µm MMF		50 µm MMF	
Modal bandwidth as measured at 850 nm (minimum, overfilled launch)	MHz*km	160	200	400	500
Link power budget	dB	7.5	7.5	7.5	7.5
Operating distance	m	220	275	500	550
Channel insertion loss (a wavelength of 830 nm is used to calculate the values)	dB	2.38	2.60	3.37	3.56
Link power penalties (a wavelength of 830 nm is used to calculate the values)	dB	4.27	4.29	4.07	3.57
Unallocated margin in link power budget (a wavelength of 830 nm is used to calculate the values)	dB	0.84	0.60	0.05	0.37

Gigabit Ethernet long reach circuit pack

The GE1/LX4 port unit supports 4 fully independent bidirectional ports. Ethernet frames received from a GE1/LX4 port are mapped into STS-1s or VC-4s using Virtual Concatenation. The number of STS1s/VC-4s per virtual concatenated signal can be user provisioned as ≤ 21 STS1s/7 VC-4s at single STS1/VC-4s intervals. This will offer an effective capacity usage over a network from 50/155 to 1000 Mbit/s in steps of 50/155 Mbit/s.

The GE1/LX4 port unit supports standard BLSR/MS-SPRing and UPSR/SNCP protection schemes on the individual STS1s/VC-4s that are part of the Virtually Concatenated signal.

The GE1/LX4 port unit uses a Low Power Laser (laser class 1/1 according to FDA/CDRH - 21 CFR 1010 & 1040 / IEC 60825).

The GE1/LX4 port unit complies with IEEE 802.3-2000 Clause 38.

The table below describes the various operating ranges for the GE1/LX4 port unit over each optical fiber type.

Fiber Type	Modal Bandwidth @ 1300 nm (min. overfilled launch) (MHz*km)	Minimum range (meters)
10 µm SMF	N/A	2 to 5000

The following table lists the specific transmission characteristics for a GE1/LX4 port unit.

Description	Unit	
Apparatus code		KFA532
Transmitter type		Longwave Laser
Signaling speed (range)	GBd	1.25 ± 100 ppm

Description	Unit	
Wavelength (range)	nm	1270 to 1355
T_{rise}/T_{fall} (max, 20–80%)	ns	0.26
RMS spectral width (max)	nm	4
Average launch power (max)	dBm	-3
Average launch power (min)	dBm	-11 (10 µm SMF)
Average launch power of OFF transmitter (max)	dBm	-30 (During all conditions when the PMA is powered in the OFF mode, the AC signal (data) into the transmit port will be valid encoded 8B/10B patterns except for short durations during system power-on-reset or diagnostics when the PMA is placed in a loopback mode.)
Extinction ratio (min)	dB	9
RIN (max)	dB/Hz	-120

The following table lists the specific receive characteristics for a GE1/LX4 port unit.

Description	Unit	
Signaling speed (range)	GBd	1.25 ± 100 ppm
Wavelength (range)	nm	1270 to 1335
Average receive power (max)	dBm	-3
Receive sensitivity	dBm	-19
Return loss (min)	dB	12
Stressed receive sensitivity (measured with conformance test signal at TP3 for BER = 10^{-12} at the eye center) (measured with a transmit signal having a 9 dB extinction ratio; if another extinction ratio is used, the stressed received sensitivity should be corrected for the extinction ratio penalty)	dBm	-14.4

The following table lists the worst-case power budget and link penalties for a GE1/LX4 port unit. Link penalties are used for link budget calculations.

Description	Unit	10 µm SMF
Modal bandwidth as measured at 1300 nm (minimum, overfilled launch)	MHz*km	N/A
Link power budget	dB	9
Operating distance	m	5000

Description	Unit	10 µm SMF
Channel insertion loss (a wavelength of 1270 nm is used to calculate the values)	dB	4.57
Link power penalties (a wavelength of 1270 nm is used to calculate the values)	dB	3.27
Unallocated margin in link power budget (a wavelength of 1270 nm is used to calculate the values)	dB	1.16

Gigabit Ethernet transmission unit GE10PL1/1A8

The GE10PL1/1A8 port unit supports optical pluggable 1 Gigabit Ethernet ports (SFPs) plus one optical pluggable 10 Gigabit Ethernet Transceiver (XFP). Various types of transceivers with different reach are supported (250m... 70 km). One bi-color LED (red and green) and one yellow LED will be used per port for status and activity signaling. It is only possible to use either up to eight 1 Gigabit ports or the 10 Gigabit port, a parallel use is not possible.

The following SFPs are supported for GbE:

- 1GbE 70km 1550nm
- 1GbE 0,5km 780nm

  The 1000BASE-SX optical interface is designed to work primarily on multi-mode fiber according to ITU-T Rec. G.651 and IEC 60793 Type A1a and A1b with the exceptions noted in Table 38-12 of IEEE 802.3-2002.
- 1GbE 5km 1310nm -

The following XFPs are supported

- 10GbE 10km 1310nm and OC192/STM64 I-64.1/SR-1
- 10GbE 40km 1550nm and OC192/STM64 S-64.2b/IR-2
- 10GbE 80km 1550nm and OC192/STM64 P1L1-2D2/LR-2c

1000BASE-SX optical interface

The following table lists the specific transmission characteristics for a 1000BASE-SX optical interface SFP.

OMGE1/SX1		
Description	**Unit**	
Application code		1000BASE-SX
Operating wavelength range	nm	770 -860
Transmitter at reference point TP2		
Source Type		MLM
Spectral Characteristics		

OMGE1/SX1		
Description	**Unit**	
maximum rms width	nm	0.85
Mean Launched power		
maximum	dBm	0
minimum	dBm	-9.5
Max. Mean Power in disabled state	dBm	-30
Minimum Extinction Ratio	dB	9.0
Maximum rise / fall time (20%-80%, ? > 830 nm)	ns	0.26
Maximum rise / fall time (20% -80%, ? = 830 nm)	ns	0.21
Maximum Relative Intensity Noise	dB/Hz	-117
Maximum transmit jitter at TP2	ps	345
Optical path between TP2 and TP3		
Attenuation range (BER=1*10-12)	dB	0 -7.5
Maximum link power penalty	dB	4.3
Receiver at reference point TP3		
Minimum sensitivity (BER=1*10E-12)	dBm	-17
Minimum stressed receive sensitivity	dBm	-12.5 / -13.5
Overload limit	dBm	0
Minimum acceptable jitter	ps	408
Minimum return loss of receiver	dB	12

1Gb Ethernet long reach interface 1000BASE-LX (5 km SMF, 1310 nm)

The following table lists the specific transmission characteristics for a 1000BASE-LX optical interface SFP.

OMGE1/LX1		
Description	**Unit**	
Application code		1000BASE-LX
Operating wavelength range	nm	1270 -1355
Transmitter at reference point TP2		
Source Type		MLM
Spectral Characteristics		
maximum rms width	nm	4
Mean Launched power		
maximum	dBm	–3

OMGE1/LX1		
Description	**Unit**	
minimum	dBm	–11 / –11.5
Max. Mean Power in disabled state	dBm	–30
Minimum Extinction Ratio	dB	9.0
Maximum rise / fall time (20%-80%, ? > 830 nm)	ns	0.26
Maximum rise / fall time (20% -80%,)	ns	0.21
Maximum Relative Intensity Noise	dB/Hz	–120
Maximum transmit jitter at TP2	ps	345
Optical path between TP2 and TP3		
Attenuation range (BER=1*10-12) on SMF	dB	0 -8.0
Maximum link power penalty	dB	3.3
Receiver at reference point TP3		
Minimum sensitivity (BER=1*10E-12)	dBm	–19
Minimum stressed receive sensitivity	dBm	–14,4
Overload limit	dBm	–3
Minimum acceptable jitter	ps	408
Minimum return loss of receiver	dB	12

1Gb Ethernet long reach interface 1000BASE-ZX (70 km SMF, 1550 nm)

The following table lists the specific transmission characteristics for a 1000BASE-ZX optical interface SFP.

OMGE1/ZX1		
Description	**Unit**	
Application code		1000BASE-ZX
Fiber type		SMF acc. to G.652
Operating wavelength range	nm	1500 -1580
Transmitter at reference point TP2		
Source Type		SLM
Spectral Characteristics		
-maximum -20 dB width	nm	1
minimum side mode suppression ratio	dB	30
Mean Launched power		
maximum	dBm	5
minimum	dBm	0

OMGE1/ZX1		
Description	**Unit**	
Max. Mean Power in disabled state	dBm	–40
Minimum Extinction Ratio	dB	9
Maximum rise / fall time (20%-80%)	ns	0.26
Maximum Relative Intensity Noise	dB/Hz	–120
Maximum transmit jitter at TP2	ps	345
Optical path between TP2 and TP3		
Attenuation range (BER = 1*10E-12)	dB	5 - 21
Maximum dispersion	ps/nm	1600
Receiver at reference point TP3		
Minimum sensitivity (BER = 1*10E-12)	dBm	–22.5
Minimum overload	dBm	0
Maximum optical path penalty	dB	1.5
Minimum acceptable jitter	ps	408
Minimum return loss of receiver (at TP3)	dB	12

10 Gigabit Ethernet short reach LANPHY interface

The following table lists the specific transmission characteristics for a 10 Gigabit Ethernet short reach LANPHY interface.

Description	**Unit**	
Apparatus code		
Target distance	km	10
Signaling speed (range)	GBd	10.3125 ± 100 ppm
Wavelength (range)	nm	1260 to 1355
Average launch power (max)	dBm	+0.5
Average launch power (min)	dBm	–8.2
Average launch power of OFF transmitter (max)	dBm	–30
Extinction ratio (min)	dB	3.5
Side mode suppression	dB	30
Dispersion penalty	dB	3,2
Relative Intensity Noise (RIN)	dB/Hz	–128
Maximum reflectance	dBm	–12
Return loss tolerance	dB	12

The following table lists the specific receive characteristics for a 10GBASE-LR XFP optical interface module:

OMX10/10KM1		
Description	**Unit**	
Application code		10GBASE-LR
Target distance	km	10
Signaling speed (range)	GBd	10.3125 ± 100 ppm
Wavelength (range)	nm	1260 to 1355
Overload limit	dBm	–1
Receive sensitivity	dBm	–12.6
Return loss (min)	dB	14
Stressed receive sensitivity (measured with conformance test signal at TP3 for BER = 10^{-12} at the eye center)	dBm	–10.3

Notes:

1. The OMX10/10KM1 XFP optical interface module can be used for SDH/SONET as well as Gigabit Ethernet applications.

10 Gigabit Ethernet extended-reach LANPHY interface

The following table lists the specific transmission characteristics for a 10 Gigabit Ethernet extended reach LANPHY interface.

Description	Unit	
Apparatus code		
Target distance	km	40
Signaling speed (range)	GBd	10.3125 ± 100 ppm
Wavelength (range)	nm	1530 to 1565
Average launch power (max)	dBm	+4.0
Average launch power (min)	dBm	-4.7
Average launch power of OFF transmitter (max)	dBm	-30
Extinction ratio (min)	dB	3
Side mode suppression	dB	30
Dispersion penalty	dB	3,0
Relative Intensity Noise (RIN)	dB/Hz	-128
Maximum reflectance	dBm	-27
Return loss tolerance	dB	21

The following table lists the specific receive characteristics for a 10GBASE-ER XFP optical interface module:

OMX10/40KM1		
Description	**Unit**	
Application code		10GBASE-ER
Target distance	km	40
Signaling speed (range)	GBd	10.3125 ± 100 ppm
Wavelength (range)	nm	1530 to 1565
Overload limit	dBm	–1
Receive sensitivity	dBm	–14.1
Maximum receive reflectance	dBm	–27
Stressed receive sensitivity (measured with conformance test signal at TP3 for BER = 10^{-12} at the eye center)	dBm	–11.3

Notes:

1. The OMX10/40KM1 XFP optical interface module can be used for SDH/SONET as well as Gigabit Ethernet applications.

10 Gigabit Ethernet 80 km LANPHY interface

The following table lists the specific transmission characteristics for a 10 Gigabit Ethernet 80 km LANPHY interface.

Description	**Unit**	
Apparatus code		
Target distance	km	80
Signaling speed (range)	GBd	10.3125 ± 100 ppm
Wavelength (range)	nm	1530 to 1565
Average launch power (max)	dBm	+4.0
Average launch power (min)	dBm	0
Average launch power of OFF transmitter (max)	dBm	-40
Extinction ratio (min)	dB	9.0
Side mode suppression	dB	30
Dispersion penalty	dB	3,0
Relative Intensity Noise (RIN)	dB/Hz	-128

Description	Unit	
Maximum reflectance	dBm	-27
Return loss tolerance	dB	21

The following table lists the specific receive characteristics for a 10GBASE-ZR XFP optical interface module:

OMX10/80KM1		
Description	**Unit**	
Application code		10GBASE-ZR
Target distance	km	80
Signaling speed (range)	GBd	10.3125 ± 100 ppm
Wavelength (range)	nm	1530 to 1565
Overload limit	dBm	–7
Receive sensitivity	dBm	–24
Maximum receive reflectance	dBm	–27
Return loss (min)	dB	14

Notes:

1. The OMX10/80KM1 XFP optical interface module can be used for SDH/SONET as well as Gigabit Ethernet applications.

Port unit designation

The designation of the various types of optical port units reflects their application and functional characteristics:

- *SH* stands for *s*hort-*h*aul
- *LH* stands for *l*ong-*h*aul
- *VLH* stands for *v*ery *l*ong-*h*aul
- *ULH* stands for *u*ltra *l*ong-*h*aul

Application code

The application code used in the tables is as follows:

application-[STM level.]suffix

Please note that in SONET applications the STM level is not part of the application code.

Application (SDH)

In the applicable SDH standards, the following abbreviations are available for designating the application: I, S, L, V, U.

- *I* stands for *i*ntra-office
- *S* stands for *s*hort-haul
- *L* stands for *l*ong-haul
- *V* stands for *v*ery long-haul
- *U* stands for *u*ltra long-haul

I, S, L, V and U are internationally standardized designations.

Application (SONET)

In the applicable SONET standards, the following abbreviations are available for designating the application: SR, IR, LR, VR.

- *VSR* stands for *v*ery *s*hort *r*each
- *SR* stands for *s*hort *r*each
- *IR* stands for *i*ntermediate *r*each
- *LR* stands for *l*ong *r*each
- *VR* stands for *v*ery *l*ong *r*each

VSR, SR, IR, LR and VR are internationally standardized designations.

OC / STM level

The OC level can be 3, 12, 48 and 192. The STM level can be 1, 4, 16 and 64.

Suffix

The fibre-optic type and the nominal wavelength of the laser used are denoted by a suffix number.

- “1” denotes the use of nominally 1310 nm laser sources on standard fibres as per ITU-T Rec. G.652
- “2” denotes the use of nominally 1550 nm laser sources on standard single mode fibres as per ITU-T Rec. G.652 / G.691
- “3” denotes the use of nominally 1550 nm laser sources on dispersion-shifted fibres as per ITU-T Rec. G. 653.
- “5” denotes the use of NZ-DSF fibre applications with G. 655 fibres.

For STM-64 interfaces, an appendix of a, b, or c to the suffix refers to the dispersion accommodation techniques used. For I-64 codes an “r” is added after the suffix number to indicate a reduced target distance.

Bandwidth management

Specifications

The following specifications apply to 1675 LambdaUnite MSS Release 10.05.54 with regard to bandwidth management:

- System switching capacity: 160 Gbit/s, 320 Gbit/s, or 640 Gbit/s in total (for details please refer to Chapter 4, “Product description”)
- VT1.5/VC-12 cross-connection granularity base with LOXC; STS-1/VC-3 (higher-order) without LOXC
- Uni- & bi-directional cross-connecting
- 1:2 broadcast connections for all cross-connection rates
- STS-12c/VC-4-4c, STS-48c/VC-4-16c and STS-192c/VC-4-64c contiguous concatenations
- Unidirectional and bidirectional virtual concatenated cross-connections STS-1-Kv (K=1...21), VC-4-Kv (K=1...7) for Gigabit Ethernet applications
- Unidirectional and bidirectional virtual concatenated cross-connections STS-3c-17v / VC-4-17v for transparent applications
- STS-3c, STS-12c, STS-48c unidirectional and bidirectional pipe mode cross-connections
- Uni-directional drop & continue
- Switching matrix capacity: 3072 × 3072 STS-1 /1024 × 1024 VC-4s (XC160), 6144 × 6144 STS-1s / 2048 × 2048 VC-4s (XC320), or 12288 × 12288 STS-1 / 4096 × 4096 VC-4 (XC640)
- Bridging and rolling commands for in-service rearrangement of circuits.

Performance requirements

Specifications

The following specifications apply to 1675 LambdaUnite MSS with regard to performance requirements:

	SDH	SONET
Jitter on STM-N / STS-N interfaces	G.813, G.825	*Telcordia*™ GR-253
Jitter on PDH interfaces	G.823, G.783	*Telcordia*™ GR-253
Performance monitoring	G.784, G.826	*Telcordia*™ GR-253

Supervision and alarms

Specifications

The following specifications apply to 1675 LambdaUnite MSS with regard to supervision and alarms:

- Plug-in circuit pack indication: red fault and green service/active LED per circuit pack
- System Controller indicators/buttons:
  - User Panel LED indicators: Prompt, Deferred and Info alarm, Abnormal, Near-End Activity, Far-End Activity, Power On, Alarm Cut-off (ACO)
  - Push-buttons: ACO button to acknowledge office alarms, LED test button
- Station Alarm Interface: Offers six isolated contact output pairs: Critical (visual, audible), Major (visual, audible), Minor (visual, audible), which can be used to extend the alarm signals from the system into the station alarm scheme.
- Rack Top Alarm Lamps: Two red and one yellow lamp are present in top of the rack to signal a Critical, Major and Minor alarm, respectively.
- Q-LAN interface to connect to *WaveStar*® CIT
- Q-LAN interfaces to connect to other Network Elements
- Floating station alarm interface outputs
- Miscellaneous discrete inputs and outputs

Timing and synchronization

The following specifications apply to 1675 LambdaUnite MSS with regard to timing and synchronization.

Clock

The clock has the following specifications:

Clock	Specification
Built-in oscillator Stratum-3	Accuracy 4.6 ppm acc. to G.813 option 1, Stability 0.37 ppm/ first 24 hours

Timing modes

The timing modes are specified as follows:

Timing mode	Specification
Free running mode	Accuracy 20 ppm over 15 years
Hold-over mode	Accuracy 4.6 ppm of the frequency of the last source in two weeks
Locked mode with reference to	• one of the external sync. inputs • one of the optical inputs
Automatic ref. signal switching	compliant with ETSI ETS 300 417–6
Support of Sync. Status Message (SSM)	OC-M / STM-N ports

OAM & P

Specifications

The following specifications apply to 1675 LambdaUnite MSS with regard to operation, administration, maintenance, and provisioning:

- Testing
  - Power On Self Test after start up and recovery
  - LAN interface self test
  - LED self test
  - Facility loopbacks and cross-connection loopbacks for interface testing
- Recovery
  Auto recovery after input power failure
- Local O & M via faceplate LEDs, buttons on User Panel, *WaveStar*® CIT LAN interface
- Centralized O & M via LAN interface, DCC link
- SW-downloading via LAN interface, DCC link
- Alarms
  - Categories for indication of alarm severity
  - Station alarm interfaces
  - Rack alarms
- Miscellaneous Discrete in- and outputs
- Self-diagnostics
- Local workstation (*WaveStar*® CIT)
- Auto-provisioning by the insertion of a circuit pack

Network management

Specifications

The following specifications apply to 1675 LambdaUnite MSS with regard to network management:

- Fully manageable by OMS
- Integration into path management OMS
- Access to Embedded Communication Channels
- Via in-station OMS interface: TL1 message protocol / 100BaseT interface
- *WaveStar*® CIT for small network management: RJ-45 CIT interface / 100BaseT interface

Physical design

Specifications overview

The following specifications apply to 1675 LambdaUnite MSS with regard to physical design:

Subrack dimensions	DUR subrack: 950 × 498 × 438 mm (37.4 × 19.6 × 17.2 in) (H × W × D) in accordance with ETSI Standard ETS 300 119-4
DUR subrack weight	27 kg (without: Fan unit, UPL, internal cabling, blanks). For more detailed weight information, please refer to "Weight and power consumption" (p. 10-40).
Rack types	NEBS-2000 or ETSI-2 rack
Rack weight	NEBS-2000: 90.5 kg (rack with PDP and rack cabling + 2x door-set) ETSI-2 rack: 92.3 kg (rack with PDP and rack cabling + 2x door-set). For more detailed weight information, please refer to "Weight and power consumption" (p. 10-40).
Connectors optical	LC connectors on all optical interfaces
Connectors electrical	1.6/5.6 coax on STM-1 electrical interface SUB-D on Alarm, Timing, User Byte IF Western RJ45 on LAN interfaces
Station power input (battery)	–48 V / –60 VDC (max. range: –40…–72 VDC)
Power consumption	1500 W for a typical configuration, 3500 W maximum. For more detailed power consumption information, please refer to "System power consumption" (p. 6-4) and to "Weight and power consumption" (p. 10-40).

Transmission fibers

1675 LambdaUnite MSS supports the following transmission fiber types:

- Standard single-mode fiber acc. to ITU-T Rec. G. 652
- Dispersion shifted fiber acc. to ITU-T Rec. G.653
- Non-zero dispersion shifted fiber acc. to ITU-T Rec. G.655
- Multimode fiber (MMF) for Gigabit Ethernet acc. to IEEE 1802.3.

Weight and power consumption

Weight and power consumption specifications

The following specifications apply to 1675 LambdaUnite MSS with regard to weight and typical power consumption of the individual parts/circuit packs. The values for the worst case power consumption are roughly 20% higher.

Component	App.-Code	Weight [kg]	Typical power consumption [W]
UNITE Rack ETSI-2 (incl. rack top lamps, doors, and side plates)	-	120	-
ETSI-2 rack extension (width), for both sides	-	12.5	-
UNITE Rack NEBS-2000 (incl. rack top lamps, doors and side plates)	-	116	-
NEBS-2000 rack extension (width), for both sides	-	12.2	-
Rack extension (height), 600 mm	-	27.4	-
Rack extension (height), 750 mm	-	27.6	-
DUR/2 with rear faceplates and complete mounting brackets +	-	51	307
incl. 1 × Controller Interface (CI-CTL)	PBJ1		
incl. 2 × Power Interface (PI/100)	PBH3		
incl. 1 × Fan Unit			
incl. 1 × User Panel (UPL)			
incl. 2 × Timing Interface (TI)	PBI1		
Controller (CTL/-)	KFA1	1.47	26
Controller (CTL/2)	KFA531	1.47	26
Controller (CTL/3S, CTL/3T)	KFA536/7(B)[1]	1.4	28
Controller (CTL/4S, CTL4T)	KFA538/9	1.39	18
XC640 (switching unit 640G)	KFD2	7.1	325
XC320/B (switching unit 320G)	KFD1B	6.38	200
XC160 (switching unit 160G)	KFD3	5.49	99
LOXC/1 (low order switching unit)	KFA700	1.52	64
LOXC40G2S (low order switching unit)	KFA702	2.34	101
LOXC40G3S (low order switching unit)	KFA703	2.34	101
OP10D/PAR2 with Slide In OM10/xxx	KFA630 +	2.38	51
OP10D/PAR2 without Slide In OM10/xxx	KFA630	1.29	34
OM10/1.3IOR1	OM10G7	0.55	8

Component	App.-Code	Weight [kg]	Typical power consumption [W]
OM10/1.5IR1	OM10G14	0.55	9
OM10/1.5LR1	OM10G6	0.55	9
OP10/1.5LR1	KFA6	1.95	47
OP10/1.3IOR1	KFA7	1.6	43
OP10/1.5IR1	KFA14	1.7	40
OP10/01-80/800G	KFA9	2.22	56
	KFA81-159		
OP10/XTTC	KFA361	2.06	42
OP10/XTTL	KFA362	2.06	42
OP10/PAR1XFP (Parent board 1 XFP)	KFA631	1.6	24
OP10D/PAR2XFP (Parent board for up to 2 XFP)	KFA632	1.6	35
OP2G5D/PAR8 without SFP	KFA620	1.56	31
OP2G5/PAR4 without SFP	KFA621	1.245	21
OM2G5/1.3SR1	OM2G5A12	0.02	0.9
OM2G5/1.3LR1	OM2G5A203	0.02	0.9
OM2G5/1.5LR1	OM2G5A204	0.02	0.9
OP2G5D/PAR8 (with 8 modules OM2G5/xxx)	KFA620 +	1.72	38
OP2G5/1.3SR4	KFA12	1.35	22
OP2G5/1.3LR4	KFA203	1.63	39
OP2G5/1.5LR4	KFA204	1.69	39
OP2G5/921-959/PWDM	KFA20 + OM2G5-921...-959	1.96	41
OM2G5/CL…*CWDM	OM2G5/CL…		1
OPT2G5/PAR3 (without SFPs)	KFA540	1.68	48
OPLB/PAR8 (without SFPs)	KFA180	1.48	28
OM622/1.3IR1	OM622A182	0.02	0.8
OM622/1.3LR1	OM622A181	0.02	0.8
OM622/1.5LR1	OM622A180	0.02	0.8
OM155/1.3IR1	OM155A184	0.02	0.8
OM155/1.3LR1	OM155A183	0.02	0.8
OM155/1.5LR1	OM155A185	0.02	0.8
OPLB/PAR8 (with 8 modules OM622/xxx or OM155/xxx)	KFA180+	1.64	34
OP622/1.3IR16	KFA17	1.6	39

Component	App.-Code	Weight [kg]	Typical power consumption [W]
OP155M/1.3IR16	KFA18	1.67	39
GE1/SX4	KFA13	1.46	76
GE1/LX4	KFA532	1.46	76
GE10PL1/1A8	KFA720	1.54	41
OMGE1/SX1	OMSX1		1
OMGE1/LX1	OMLX1		1
OMGE1/ZX1	OMZX1		1.1
OMX10/10KM1	OMX10G10		2.6
OMX10/40KM1	OMX10G40		2.6
OMX10/80KM1	OMX10G80		3.6
EP155/EL8	KFA533	1.25	23
ECI/E8	PBK1	0.44	1.1
ECI/P8	PBK2	0.35	1.1
EP51/EL36	KFA535	1.52	36
EP51/EL36B	KFA535B	1.52	36
ECI/51MP72	PBK4	0.75	n.a.
DCM CARRIER CPL (ETSI - 2/NEBS - 2000 RACK)	-	8.84	-
DCM/LC CPL DK-S (15 km SSMF)	-	2.47	-
DCM/LC CPL DK-30,0	-	2.06	-
Front blank (faceplate)	-	0.52	-
TMUX2G5/1	KFA710	1.5	52.3

Notes:

1. The letter “B” indicates variants that are delivered without *CompactFlash*® card.

For the fields marked with “n.a.”, data was not available on the issue date.

Spare part information

Recommended spare parts

The following table indicates how many plug-in units, paddle boards, and sub-racks are required for the customer's substitution spare stock; the calculation for all parts is based on a lead time of 26 week days, except for the 10-Gbit/s colored optics transmission units which have longer lead times. For more specific information please contact your Alcatel-Lucent local customer team.

Type	Apparatus code	1 pack used	up to 10 packs used	up to 100 packs used	up to 1000 packs used	up to 10000 packs used
Subrack DUR	-	1	1	1	2	5
Fan unit	-	1	1	2	5	21
PI/100 power interface	PBH3	1	1	1	2	4
User panel	-	1	1	1	2	6
TI/E1/DS1 timing interface	PBI1	1	1	1	2	4
CI controller interface	PBJ1	1	1	2	5	19
CTL/- controller	KFA1	1	1	3	11	58
CTL/2 controller	KFA531	1	2	4	15	85
CTL/3 controller (/3T and /3S)	KFA536/KFA537(B)[1]	1	1	3	12	63
CTL/4 controller (/4T and /4S)	KFA538/KFA539	1	1	3	11	59
XC160 switching unit	KFD3	1	2	4	13	69
XC320 switching unit	KFD1B	1	2	4	17	101
XC640 switching unit	KFD2	1	2	4	16	96
LOXC/1 switching unit	KFA700	1	1	3	11	56
LOXC40G2S/1 switching unit	KFA702	1	1	3	9	46
LOXC40G3S/1 switching unit	KFA703	1	1	3	9	46
OP10 intra-office 1.3 µm	KFA7	1	2	4	15	88
OP10 colored optics *WaveStar*® OLS 1.6T compatible) (preferred / not preferred colors)	KFA9, KFA81…*	1 / 1	2 / 2	6 / 7	24 / 29	n.a.
OP10 long reach 1.5 µm	KFA6	1	2	4	17	101
OP10 intermediate reach 1.5 µm	KFA14	1	2	4	16	91
OP10D OC-192/STM-64 double density parent board for two optical modules	KFA630	1	1	3	12	62

Type	Apparatus code	1 pack used	up to 10 packs used	up to 100 packs used	up to 1000 packs used	up to 10000 packs used
OM10 (OC-192/STM-64 hot pluggable optical LR/LH modules) 80 km	OM10G6	1	1	2	7	34
OM10 (OC-192/STM-64 hot pluggable optical IR/SH modules) 40 km	OM10G14	1	1	2	7	30
OM10 (OC-192/STM-64 hot pluggable optical IOR module) 600 m	OM10G7	1	1	2	6	26
OMX10/10KM1 (XFP, 10GBASE-LR / I-64.1)	OMX10G10	0	1	2	5	18
OMX10/40KM1 (XFP, 10GBASE-ER / S-64.2b)	OMX10G40	0	1	2	5	20
OMX10/80KM1 (XFP, 10GBASE 8o km /P1L1-2D2/LR-2c)	OMX10G80	0	1	2	5	20
OP10/XTTC	KFA361	1	1	3	11	55
OP10/XTTL	KFA362	1	1	3	11	55
OP10/PAR1XFP (Parent board 1 XFP)	KFA631	1	1	3	9	43
OP10D/PAR2XFP (Parent board for up to 2 XFP)	KFA632	1	1	3	10	48
OP2G5 long reach 1.5 µm / 1.3 µm	KFA204 / KFA203	1	1	3	11	59
OP2G5 short reach 1.3 µm	KFA12	1	1	3	12	64
OP2G5D/PAR8 (SFP parent board)	KFA620	1	1	3	8	41
OP2G5/PAR4 (SFP parent board)	KFA621	1	1	3	8	37
OPT2G5/PAR3 (transparent SFP parent board)	KFA540	1	1	3	9	47
OM2G5/1.5LR SFP optical modules	OM2G5A204	0	1	1	3	8
OM2G5/1.3LR SFP optical modules	OM2G5A203	0	1.	1	3	8
OM2G5/1.3SR SFP optical modules	OM2G5A12	0	1	1	3	8
OP2G5 pWDM parent board	KFA20	1	1	3	8	36
OM2G5 pWDM optical modules	OM2G5A291…*	1	1	3	8	35
OM2G5/CL…*CWDM optical modules	OM2G5/CL…					
OP622 intermediate reach 1.3 µm	KFA17	1	2	4	14	79
OP155M intermediate reach 1.3 µm	KFA18	1	2	4	14	79

Type	Apparatus code	1 pack used	up to 10 packs used	up to 100 packs used	up to 1000 packs used	up to 10000 packs used
OPLB (OC-3&OC-12 /STM-1&STM-4 parent board for eight SFP modules)	KFA180	1	1	3	9	43
OM622 (OC-12/STM-4 SFP modules for LR/LH applications) 40 km	OM622A181	1	1	1	2	7
OM622 (OC-12/STM-4 SFP modules for IR/SH applications) 15 km	OM622A182	1	1	1	2	7
OM622 (OC-12/STM-4 SFP modules for LR/LH applications) 80 km	OM622A180	1	1	1	2	6
OM155 (OC-3/STM-1 SFP modules for LR/LH applications) 40 km	OM155A183	1	1	1	2	7
OM155 (OC-3/STM-1 SFP modules for IR/SH applications) 15 km	OM155A184	1	1	1	2	7
OM155 (OC-3/STM-1 SFP modules for LR/LH applications) 80 km	OM155A185	1	1	1	2	6
EP155 electrical STM-1 interface unit	KFA533	1	1	3	8	40
ECI/155ME8 electrical comm. interface with 16 ports	PBK1	1	1	1	2	4
ECI/155MP8 electrical comm. interface with protection	PBK2	1	1	1	2	6
EP51 electrical DS3 interface unit	KFA535	1	1	3	9	45
EP51 electrical DS3 interface unit	KFA535B	1	1	3	9	45
ECI51/MP72 electrical comm. interface with protection	PBK4	1	1	2	4	14
GE1/LX4 Ethernet Interface	KFA532	1	1	3	10	51
GE1/SX4 Ethernet Interface	KFA13	1	1	3	10	51
GE10PL1/1A8	KFA720	1	1	3	11	58
OMGE1/SX1	OMSX1	0	1	1	3	8
OMGE1/LX1	OMLX1	0	1	1	3	8
OMGE1/ZX1	OMZX1	0	1	2	4	12
TMUX2G5/1	KFA710	1	1	3	10	54

Notes:

1. The letter “B” indicates variants that are delivered without *CompactFlash*® card.

For the fields marked with “n.a.” data was not available on the issue date.

*: For the complete apparatus code list and the respective comcodes please refer to “Engineering Drawing” (p. 7-2).

Appendix A: An SDH overview

Overview

Purpose

This chapter briefly describes the Synchronous Digital Hierarchy (SDH).

Synchronous Digital Hierarchy

In 1988, the ITU-T (formerly CCITT) came to an agreement on the Synchronous Digital Hierarchy (SDH). The corresponding ITU-T Recommendation G.707 forms the basis of a global, uniform optical transmission network. SDH can operate with plesiochronous networks and therefore allows the continuous evolution of existing digital transmission networks.

The major features and advantages of SDH are:

- Compatibility of transmission equipment and networks on a worldwide basis
- Uniform physical interfaces
- Easy cross connection of signals in the network nodes
- Possibility of transmitting PDH (Plesiochronous Digital Hierarchy) tributary signals at bit rates commonly used at present
- Simple adding and dropping of individual channels without special multiplexers (add/drop facility)
- Easy transition to higher transmission rates
- Due to the standardization of the network element functions SDH supports a superordinate network management and new monitoring functions and provides transport capacity and protocols (Telecommunication Management Network, TMN) for this purpose in the overheads of the multiplex signals.
- High flexibility and user-friendly monitoring possibilities, e.g. end-to-end monitoring of the bit error ratio.

Purpose of SDH

The basic purpose of SDH is to provide a standard synchronous optical hierarchy with sufficient flexibility to accommodate digital signals that currently exist in today's network, as well as those planned for the future.

SDH currently defines standard rates and formats and optical interfaces. Today, mid-span meet is possible at the optical transmission level. These and other related issues continue to evolve through the ITU-T committees.

ITU-T addressed issues

The set of ITU-T Recommendations defines

- Optical parameters
- Multiplexing schemes to map existing digital signals (PDH) into SDH payload signals
- Overhead channels to support standard operation, administration, maintenance, and provisioning (OAM&P) functions
- Criteria for optical line Automatic Protection Switch (APS)

References

For more detailed information on SDH, refer to

- ITU-T Recommendation G.703, "Physical/electrical characteristics of hierarchical digital interfaces", October 1996
- ITU-T Recommendation G.707, "Network Node Interface For The Synchronous Digital Hierarchy (SDH)", March 1996
- ITU-T Recommendation G.780, "Vocabulary of terms for synchronous digital hierarchy (SDH) networks and equipment" , November 1993
- ITU-T Recommendation G.783, "Characteristics of Synchronous Digital Hierarchy (SDH) Multiplexing Equipment Functional Blocks ", April 1997
- ITU-T Recommendation G.784, "Synchronous Digital Hierarchy (SDH) Management ", January 1994
- ITU-T Recommendation G.785, "Characteristics of a flexible multiplexer in a synchronous digital hierarchy environment ", November 1996
- ITU-T Recommendation G.813, "Timing characteristics of SDH equipment slave clocks (SEC)", August 1996
- ITU-T Recommendation G.823, "The control of jitter and wander within digital networks which are based on the 2048-kbit/s hierarchy", March 1993
- ITU-T Recommendation G.825, "The control of jitter and wander within digital networks which are based on the synchronous digital hierarchy (SDH)", March 1993

- ITU-T Recommendation G.826, " Error performance Parameters and Objectives for International, Constant Bit Rate Digital Paths at or Above the Primary Rate", February 1999
- ITU-T Recommendation G.957, "Optical interfaces for equipments and systems relating to the synchronous digital hierarchy", July 1995

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SDH signal hierarchy

This section describes the basics of the SDH hierarchy.

STM-1 Frame

The SDH signal hierarchy is based on a basic “building block” frame called the Synchronous Transport Module 1 (STM-1), as shown in “SDH STM-1 frame” (p. A-5).

The STM-1 frame has a rate of 8000 frames per second and a duration of 125 microseconds

The STM-1 frame consists of 270 columns and 9 rows.

Each cell in the matrix represents an 8-bit byte.

Transmitting Signals

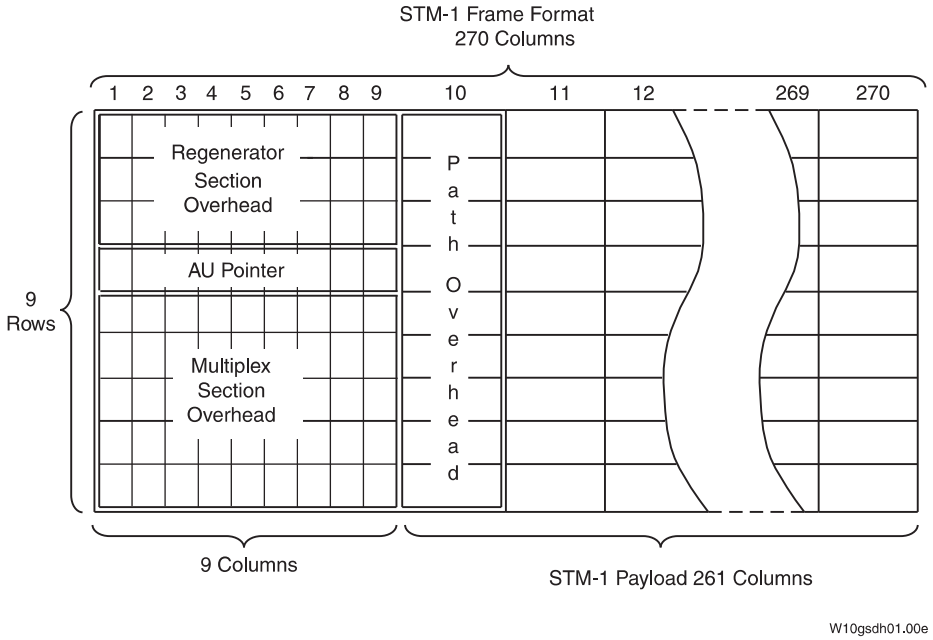
The STM-1 frame (STM = Synchronous Transport Module) is transmitted serially starting from the left with row 1 column 1 through column 270, then row 2 column 1 through 270, continuing on, row-by-row, until all 2430 bytes (9x270) of the STM-1 frame have been transmitted. Because each STM-1 frame consists of 2430 bytes and each byte has 8 bits, the frame contains 19440 bits a frame. There are 8000 STM-1 frames a second, at the STM-1 signal rate of 155.520.000 (19440 × 8000) kbit/s.

Three higher bit rates are also defined:

- 622.080 Mbit/s (STM-4)
- 2488.320 Mbit/s (STM-16)
- 9953.280 Mbit/s (STM-64)

The bit rates of the higher-order hierarchy levels are integer multiples of the STM-1 transmission rate.

SDH STM-1 frame



Section overhead (SOH)

The first nine bytes of each row with exception of the fourth row are part of the SOH (Section OverHead). The first nine byte of the fourth row contain the AU pointer (AU = Administrative Unit).

STM-1 payload

Columns 10 through 270 (the remainder of the frame), are reserved for payload signals.

SDH path and line sections

This section describes and illustrates the SDH path and line sections.

SDH layers

SDH divides its processing functions into the following three path and line sections:

- Regenerator section
- Multiplex section
- Path

These three path and line sections are associated with

- Equipment that reflects the natural divisions in network spans
- Overhead bytes that carry information used by various network elements

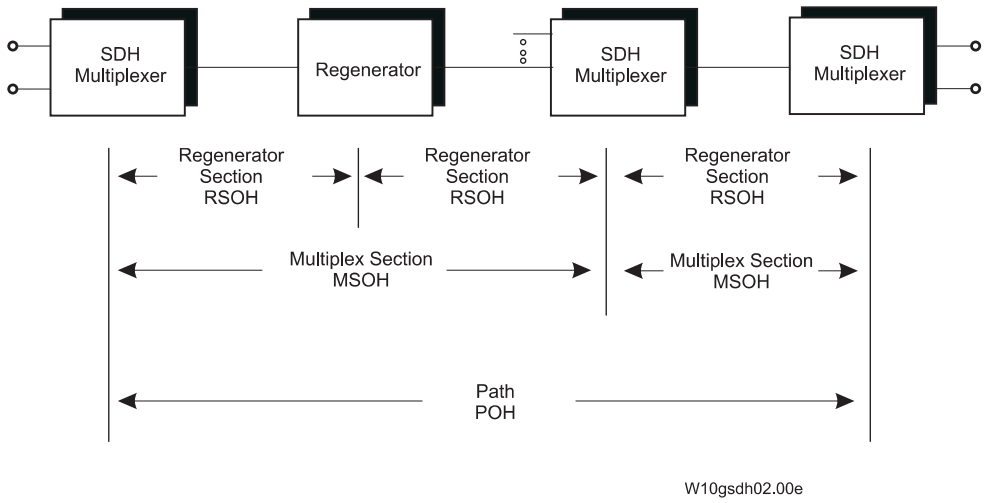
Equipment layers

The following table lists and defines each SDH equipment path and line section.

Path and line sections	Definition
Regenerator section	A regenerator section describes the section between two network elements. The network elements, however, do not necessarily have to be regenerators.
Multiplex section	A multiplex section is the section between two multiplexers. A multiplex section is defined as that part of a path where no multiplexing or demultiplexing of the STM-N frame takes place.
Path	A path is the logical signal connection between two termination points. A path can be composed of a number of multiplex sections which themselves can consist of several regenerator sections.

Path, MS and RS

The following figure illustrates the equipment path, multiplex sections and regenerator sections in a signal path.



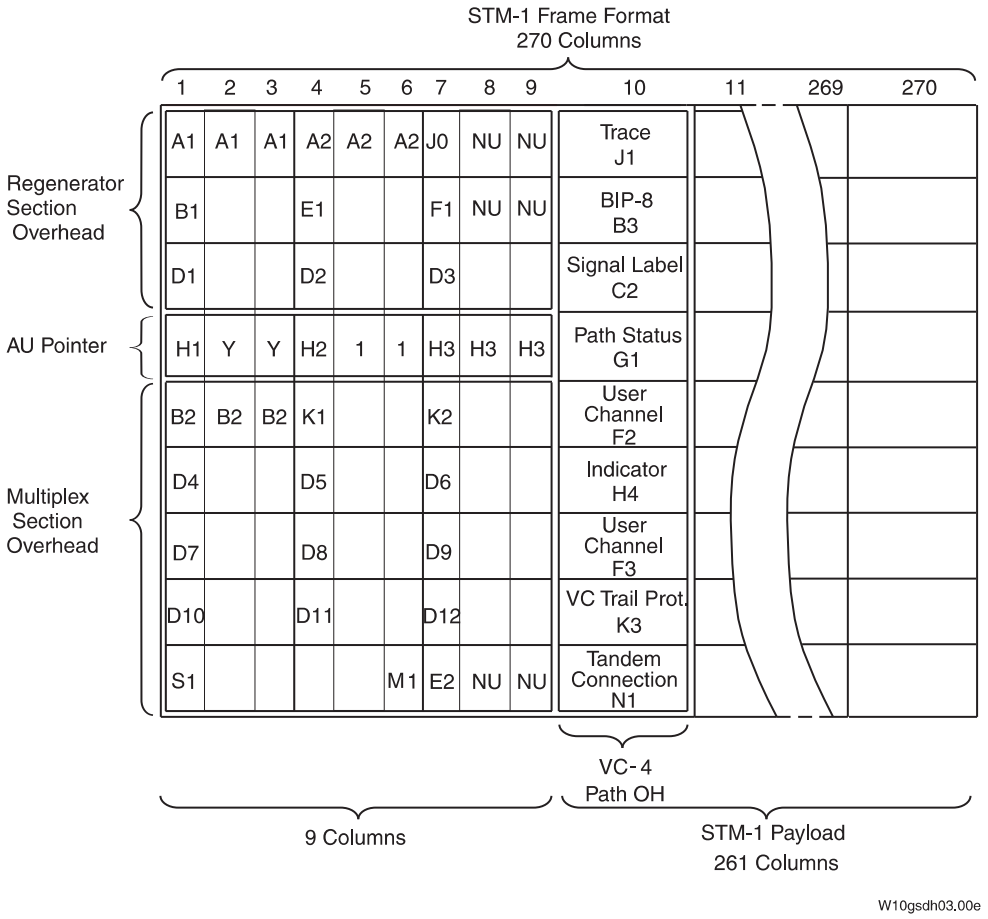
Overhead bytes

The following table lists and defines the overhead associated with each SDH path and line section.

Overhead byte section	Definition
Regenerator section	Contains information that is used by all SDH equipment including repeaters.
Multiplex section	Used by all SDH equipment except repeaters.
Path	The POH contains all the additional signals of the respective hierarchy level so that a VC can be transmitted and switched through independently of its contents.

SDH frame

The following figure illustrates the SDH frame sections and its set of overhead bytes.



SDH frame structure

This section provides detailed information on the locations and functions of various overhead bytes for each of the following SDH path and line sections:

- Regenerator Section
- Multiplex Section
- Path

RS/MS overhead

The following table identifies the location and function of each Regenerator Section and Multiplex Section overhead byte.

Bytes	Function
A1, A2	Frame alignment A1 = 1111 0110 ; A2 = 0010 1000 ; These fixed-value bytes are used for synchronization.
B1	BIP-8 parity test Regenerator section error monitoring; BIP-8 : Computed over all bits of the previous frame after scrambling; B1 is placed into the SOH before scrambling; BIP-X: (Bit Interleaved Parity X bits) Even parity, X-bit code; first bit of code = even parity over first bit of all X-bit sequences;
B2	Multiplex section error monitoring; BIP-24 : B2 is computed over all bits of the previous STM-1 frame except for row 1 to 3 of the SOH (RSOH); B2 is computed after and placed before scrambling;
Z0	Spare bytes
D1 - D3 (= DCC_R) D4 - D12 (= DCC_M)	Data Communication Channel (network management information exchange)
E1	Orderwire channel
E2	Orderwire channel
F1	User channel
K1, K2	Automatic protection switch
K2	MS-AIS/RDI indicator
S1	Synchronization Status Message
M1	REI (Remote Error Indication) byte
NU	National Usage

Path overhead

The Path Overhead (POH) is generated for all plesiochronous tributary signals in accordance with ITU-T Rec. G.709. The POH provides for integrity of communication between the point of assembly of a Virtual Container VC and its point of disassembly.

The following table shows the higher-order POH bytes and their functions.

Byte	Function
J1	Path trace identifier
B3	Path Bit Interleaved Parity (BIP-8) Provides each path performance monitoring. This byte is calculated over all bits of the previous payload before scrambling.
C2	Signal label All "0" means unequipped; other and "00000001" means equipped
G1	Path status Conveys the STM-1 path terminating status, performance, and remote defect indication (RDI) signal conditions back to an originating path terminating equipment.
F2, F3	User data channel Reserved for user communication.
H4	Multiframe indicator Provides a general multiframe indicator for VC-structured payloads.
K3	VC Trail protection.
N1	Tandem Connection Monitoring (TCM)

The following table shows the lower-order POH bytes and their functions.

Byte	Function
V5	Error checking (b1 + b2 = BIP2), signal label (bit 5…7), and path status (b3 = REI, b4 = RFI, b8 = RDI)
J2	Path trace identifier
N2	Network operator byte (for TCM)
K4	higher-order APS (b1…b4) and optional (b5…b7).

AU pointer

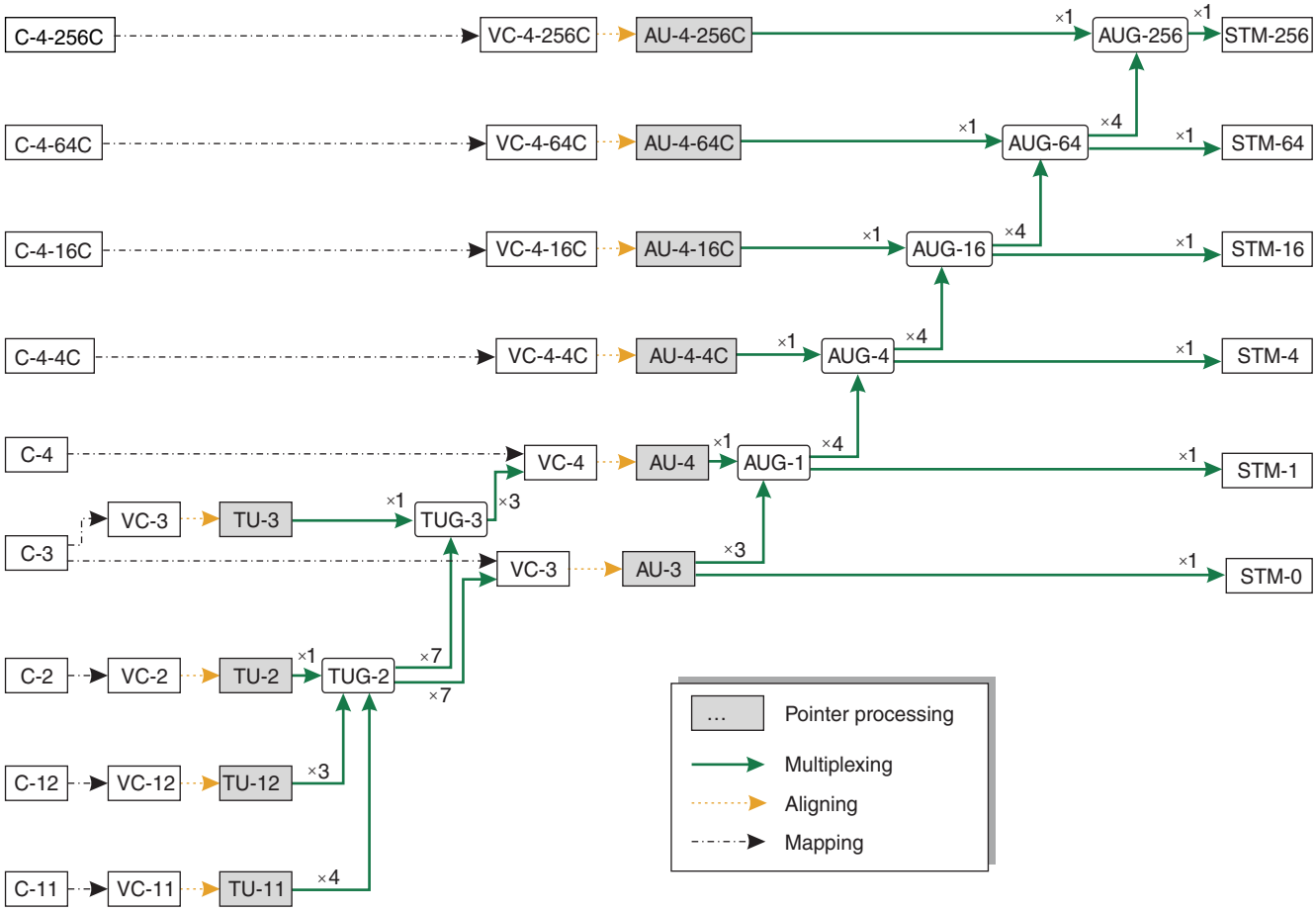
The AU pointer together with the last 261 columns of the STM-1 frame forms an AUG (Administrative Unit Group). An AUG may contain one AU-4 or three byte-multiplexed AU-3s (an AU-3 is exactly one third of the size of an AU-4). AU-3s are also compatible with the SONET standard (Synchronous Optical NETwork) which is the predecessor of SDH (and still the prevailing technology within the USA). Three byte-multiplexed STS frames (SONET frame), each containing one AU-3 can be mapped into one STM-1.

SDH digital multiplexing

Digital multiplexing is SDH's method of byte mapping tributary signals to a higher signal rate, which permits economical extraction of a single tributary signal without the need to demultiplex the entire STM-1 payload. In addition, SDH provides overhead channels for use by OAM&P groups.

SDH digital multiplexing

The following figure illustrates the SDH technique of mapping tributary signals into the STM frames.



Transporting SDH payloads

Tributary signals are mapped into a digital signal called a virtual container (VC). The VC is a structure designed for the transport and switching of STM payloads. There are various sizes of VCs: VC-11, VC-12, VC-2, VC-3, VC-4, VC-4-4C, VC-4-16C, and VC-4-64C.

Table

The following table shows the mapping possibilities of some digital signals into SDH payloads.

Input tributary	Voice Channels	Rate	Mapped Into
1.5 Mbit/s	24	1.544 Mbit/s	VC-11
2 Mbit/s	32	2.048 Mbit/s	VC-12
6 Mbit/s	96	6.312 Mbit/s	VC-2
34 Mbit/s	672	34.368 Mbit/s	VC-3
45 Mbit/s	672	44.736 Mbit/s	VC-3
140 Mbit/s	2016	139.264 Mbit/s	VC-4

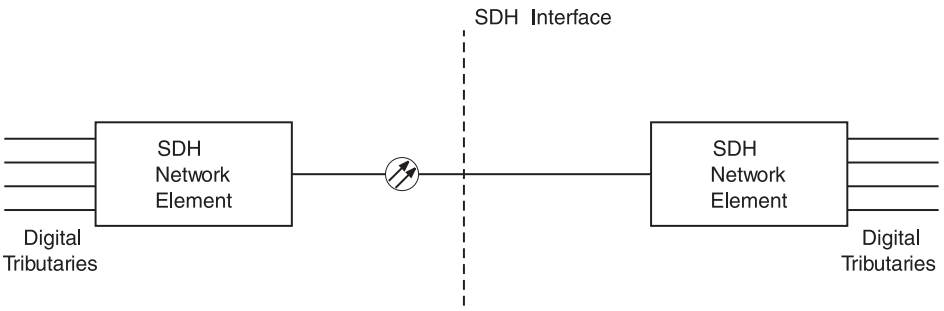
SDH interface

This section describes the SDH interface.

Description

The SDH interface provides the optical mid-span meet between SDH network elements. An SDH network element is the hardware and software that affects the termination or repeating of an SDH standard signal.

SDH interface



Standard optical interconnect at SDH interface

Family of standard rates at N x 155.52 Mbit/s
[Synchronous Transport Module (STM-1)]

Overhead channels defined for interoffice operations
and maintenance functions

W10gsdh06.00e

SDH multiplexing process

SDH provides for multiplexing of 2-Mbit/s (C-12) and 34-Mbit/s (C-3) signals into an STM-1 frame.

Furthermore, multiplexing paths also exist for the SONET specific 1.5-Mbit/s, 6-Mbit/s and 45-Mbit/s signals.

Process

The following describes the process for multiplexing a 2-Mbit/s signal. The "SDH digital multiplexing" (p. A-12) illustrates the multiplexing process.

1 Input 2-Mbit/s tributary is mapped

- Each VC-12 carries a single 2-Mbit/s payload.
- The VC-12 is aligned into a Tributary Unit TU-2 using a TU pointer.
- Three TU-2 are then multiplexed into a Tributary Unit Group TUG-2.
- Seven TUG-2 are multiplexed into an TUG-3.
- Three TUG-3 are multiplexed into an VC-4.
- The VC-4 is aligned into an Administrative Unit AU-4 using a AU pointer.
- The AU-4 is mapped into an AUG which is then mapped into an STM-1 frame.

2 After VCs are multiplexed into the STM-1 payload, the section overhead is added.

3 Scrambled STM-1 signal is transported to the optical stage.

SDH demultiplexing process

Demultiplexing is the inverse of multiplexing. This topic describes how to demultiplex a signal.

Process

The following describes the process for demultiplexing an STM-1 signal to a 2 Mbit/s signal. The “SDH digital multiplexing” (p. A-12) illustrates the demultiplexing process.

1. The unscrambled STM-1 signal from the optical conversion stages is processed to extract the path overhead and accurately locate the payload.

2. The STM-1 path overhead is processed to locate the VCs. The individual VCs are then processed to extract VC overhead and, via the VC pointer, accurately locate the 2-Mbit/s signal.

3. The 2-Mbit/s signal is desynchronized, providing a standard 2-Mbit/s signal to the asynchronous network.

Key points

SDH STM pointers are used to locate the payload relative to the transport overhead.

Remember the following key points about signal demultiplexing:

- The SDH frame is a fixed time (125 µs) and no bit-stuffing is used.
- The synchronous payload can float within the frame. This is to permit compensation for small variations in frequency between the clocks of the two systems that may occur if the systems are independently timed (plesiochronous timing).

SDH transport rates

Higher rate STM-N frames are built through byte-multiplexing of N STM-1 signals.

Creating higher rate signals

A STM-N signal can only be multiplexed out of N STM-1 frames with their first A1 byte at the same position (i.e. the first A1 byte arriving at the same time).

STM-N frames are built through byte-multiplexing of N STM-1 signals. Not all bytes of the multiplexed SOH (size = N × SOH of STM-1) are relevant in an STM-4/16.

For example there is only one B1 byte in an STM-4/16 frame which is computed the same way as for an STM-1. Generally the SOH of the first STM-1 inside the STM-N is used for SOH bytes that are needed only once. The valid bytes are given in ITU-T G.707.

SDH transport rates

Designation	Line rate (Mbit/s)	Capacity
STM-1	155.520	1 AU-4 or 3 AU-3
STM-4	622.080	4 AU-4 or 12 AU-3
STM-16	2488.320	16 AU-4 or 48 AU-3
STM-64	9953.280	64 AU-4 or 192 AU-3
STM-256	39813.120	256 AU-4 or 768 AU-3

Appendix B: A SONET overview

Overview

Purpose

This chapter briefly describes the Synchronous Optical Network (SONET).

History of the SONET name

The American National Standards Institute (ANSI) recognized the need for an optical signal standard for future broadband transmission, and a committee began working on optical signal and interface standards in 1984.

In 1985, Bellcore proposed a network approach to fiber system standardization to T1X1. In the proposal, Bellcore suggested the following:

- Hierarchical family of signals whose rates would be integer multiples of a basic modular signal
- Synchronous multiplexing technique, leading to the coining of the term *Synchronous Optical Network* (SONET)

CCITT interest in SONET

The International Telegraph and Telephone Consultative Committee (CCITT) was interested in SONET and held conferences in 1987 and 1988 which resulted in coordinated specifications and approval of both the American National Standard (SONET) and the CCITT-International Standard, Synchronous Digital Hierarchy (SDH) in 1988.

Important! The CCITT is now named International Telecommunication Union, Telecommunication Standardization Sector (ITU-T). For more information refer to the "Standards: Their Global Impact" in the *IEEE Communications Magazine*, Vol. 32, No. 1, January 1994.

Purpose

The basic purpose of SONET is to provide a standard synchronous optical hierarchy with sufficient flexibility to accommodate digital signals that currently exist in the networks of today, as well as those planned for the future.

SONET currently defines standard rates and formats and optical interfaces. Today, mid-span meet is possible at the optical transmission level. These and other related issues continue to evolve through the ANSI committees.

ANSI addressed issues

The set of American National Standards defines:

- Optical parameters
- Multiplexing schemes to map existing digital signals (that is, DS1 and DS3) into SONET payload signals
- Overhead channels to support standard operation, administration, maintenance, and provisioning (OAM&P) functions
- Criteria for optical line automatic protection switch (APS)

References

For more detailed information on SONET, refer to:

- ANSI T1.105 – 1995 American National Standard for Telecommunications, Synchronous Optical Network (SONET)
- ANSI T1.106-1988 American National Standard for Telecommunications – Digital Hierarchy Optical Interface Specifications, Single Mode
- ITU Recommendations G.707, G.708, G.709
- R. Ballart and Y. C. Ching, SONET: Now It's the Standard Optical Network, *IEEE Communications Magazine*, Vol. 27, No. 3 (March 1989): 8-15

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SONET signal hierarchy

Introduction

This section describes the basics of the SONET hierarchy.

STS-1 frame

The SONET signal hierarchy is based on a basic "building block" frame called the synchronous transport signal-level 1 (STS-1), as shown in "Figure of SONET STS-1 frame" (p. B-4).

The STS-1 frame has:

- A recurring rate of 8000 frames a second
- The frame rate of 125 microseconds

The STS-1 frame consists of:

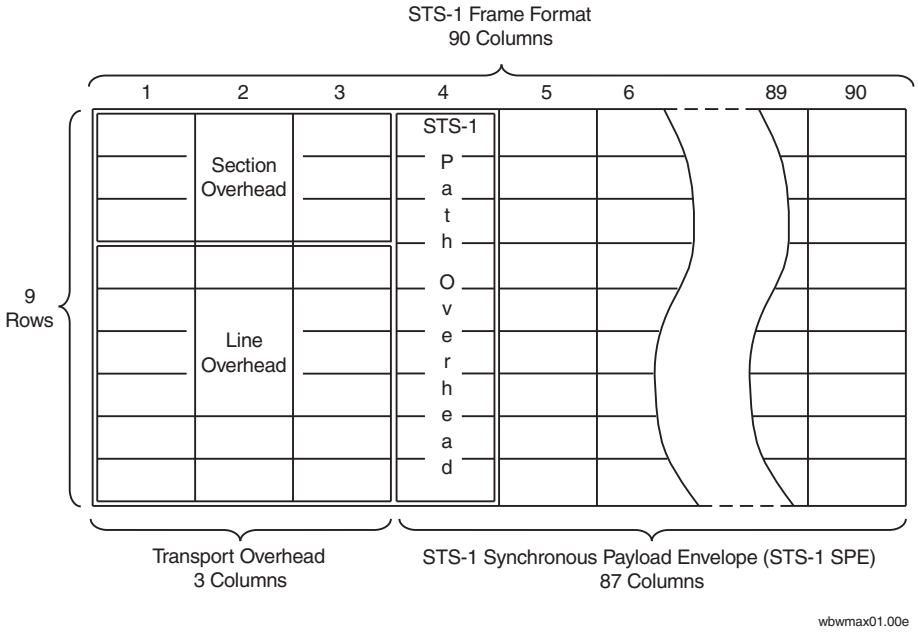
- 90 columns
- 9 rows

Important! Each cell in the matrix represents an 8-bit byte.

Transmitting signals

The STS-1 frame is transmitted serially starting from the left with row 1 column 1 through column 90, then row 2 column 1 through 90, continuing on, row-by-row, until all 810 bytes (9 × 90) of the STS-1 frame have been transmitted. Because each STS-1 frame consists of 810 bytes and each byte has 8 bits, the frame contains 6480 bits a frame. There are 8000 STS-1 frames a second, at the STS-1 signal rate of 51,840,000 (6480 × 8000) bits a second.

Figure of SONET STS-1 frame



Transport overhead

The first three columns in each of the nine rows carry the section and line overhead bytes. Collectively, these 27 bytes are referred to as transport overhead.

Synchronous payload envelope

Columns 4 through 90 (the remainder of the frame), are reserved for payload signals (for example, DS1 and DS3) and is referred to as the STS-1 synchronous payload envelope (STS-1 SPE). The optical counterpart of the STS-1 is the optical carrier level 1 signal (OC-1), which is the result of a direct optical conversion after scrambling.

SONET layers

SONET layers

SONET divides its processing functions into the following three layers:

- Section
- Line
- Path

These three layers are associated with:

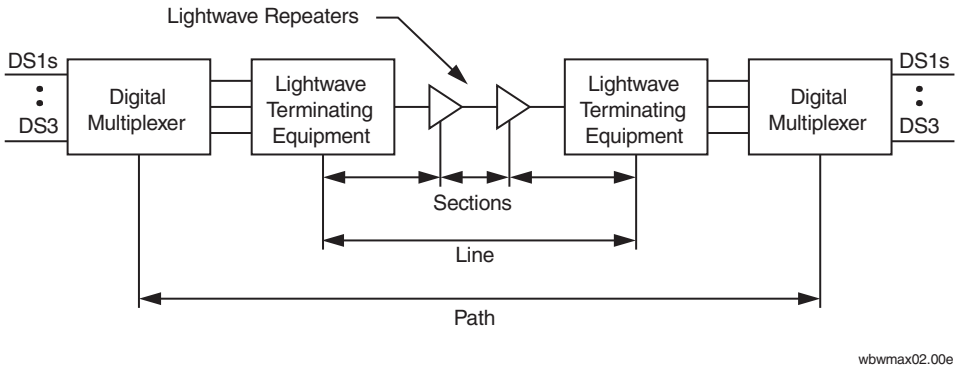
- Equipment that reflects the natural divisions in network spans
- Bytes that carry information used by various network elements

Equipment layers

The following table lists and defines each SONET equipment layer:

Layer	Definition
Section and Section Terminating Equipment	The transmission spans (Spans between regenerators are also referred to as sections.) between lightwave terminating equipment and the regenerators. This equipment provides regenerator functions which terminate the section overhead to provide single-ended operations and section performance monitoring.
Line and Line Terminating Equipment	The transmission span between terminating equipment (STS-1 cross-connects) that provides line performance monitoring.
STS-1 and Virtual Tributary (VT) Path Terminating Equipment	The SONET portion of the transmission span for an end-to-end tributary (DS1 or DS3) signal that provides signal labeling and path performance monitoring for signals as they are transported through a SONET network. STS-1 path terminating equipment also provides cross-connections for lower-rate, (that is, DS1) signals. A VT is a sub-DS3 payload and is described later in more detail.

The following figure illustrates the equipment layers (section, line, and path) in a signal path:



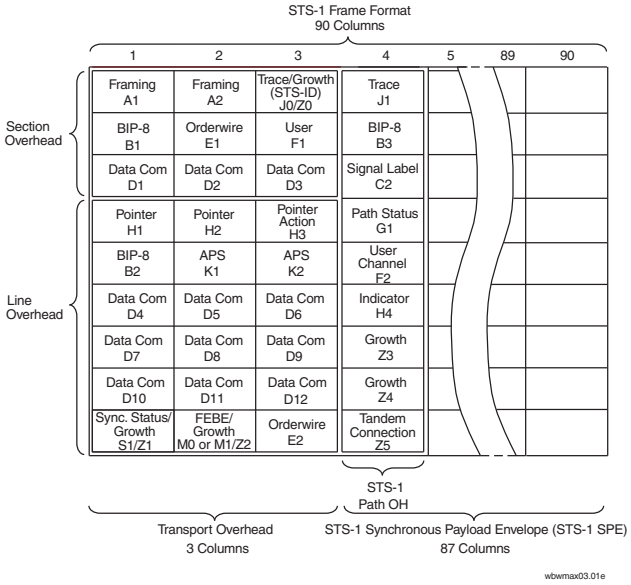
Overhead byte layers

The following table lists and defines the overhead associated with each SONET layer:

Overhead Byte Layer	Definition
Section	Contains information that is used by all SONET equipment including repeaters.
Line	Used by all SONET equipment except repeaters.
Path	Carried within the payload envelope across the end-to-end path with: • STS-1 remaining with the STS-1 SPE until its payload is demultiplexed • VTN (N = 1.5, 2, 3, or 6) remaining with the VTN until it is demultiplexed to its asynchronous signal

Figure of SONET frame format

The following figure illustrates each SONET layer and its set of overhead bytes:



SONET frame structure

Introduction

This section provides detailed information on the locations and functions of various overhead bytes for each of the following SONET layers:

- Section
- Line
- Path (STS-1 and VT)

Section overhead

The following table identifies the location and function of each section overhead byte:

Byte	Location and Function
Framing (A1 & A2)	Provides framing for each STS-1.
Trace/Growth (J0/Z0)	The Section Trace and Section Growth bytes replace STS-1 ID (C1). J0/Z0 are for future use and the locations are as follows: • J0 byte is in the first STS-1 of an STS-*N*. • Z0 byte is in the second through *N*th STS-1 of the STS-*N*.
Section Bit Interleaved Parity (BIP-8) (B1)*	Provides section performance monitoring and is calculated over all bits of the previous STS-N frame.
Section Orderwire (E1)*	Provides a local orderwire for voice communication channel between regenerators.
Section User Channel (F1)*	Set aside for the purpose of the user.
Section Data Communications Channel (D1, D2, D3)*	A 192-kbit/s message-based channel that is used for alarms, maintenance, control, monitoring, and other communication needs between section terminating equipment.

Notes:

1. * Defined only for STS-1 #1 of an STS-*N* signal.

Line overhead

The following table identifies the location and function of each line overhead byte:

Byte	Location and Function
Pointer (H1, H2)	Two bytes indicating the offset in bytes between the pointer action byte (H3) and the first byte (J1) of the STS-1 synchronous payload envelope (SPE).

Byte	Location and Function
Pointer Action (H3)	Allocated for frequency justification.
Line Bit Interleaved Parity (BIP-8) (B2)	Provided for line performance monitoring in all STS-1 signals within an STS-*N* signal.
Automatic Protection Switching (APS) (K1, K2)*	Two bytes used for APS signaling between line level entities. In addition, bits 6, 7, and 8 of K2 are used for line alarm indication signal (AIS) and line far-end receive failure (FERF).
Line Data Communications Channel (D4 - D12)	This is a 576-kbit/s message-based channel.
Synchronization Status (S1)	• Located in the first STS-1 of an STS-*N*. • Conveys the synchronization status of the Network Element.
Growth (Z1)	• Located in the second through Nth STS-1 of an STS-N. • Reserved for future growth.
Line Orderwire (E2)*	Allocated to be used as an express orderwire between line entities.

Notes:

1. * Defined only for STS-1 #1 of an STS-N signal.

STS-1 path overhead

The STS-1 path overhead is assigned to and remains with the STS-1 SPE until the payload is demultiplexed and is used for functions that are necessary to transport all synchronous payload envelopes.

Use the following table to determine the location and function of each STS-1 path overhead byte:

Byte	Location and Function
STS-1 Path Trace (J1)	Repetitively transmits a 64 byte, fixed length string so that an STS-1 path receiving terminal can verify its continued connection to the intended transmitter.
STS-1 Path Bit Interleaved Parity (BIP-8) (B3)	Provides each STS-1 path performance monitoring. This byte is calculated over all bits of the previous STS-1 SPE before scrambling.
STS-1 Path Signal Label (C2)	Indicates the construction of the STS-1 synchronous payload envelope (SPE).
Path Status (G1)	Conveys the STS-1 path terminating status, performance, and remote defect indication (RDI) signal conditions back to an originating STS-1 path terminating equipment.

Byte	Location and Function
Path User Channel (F2)	Reserved for user communication.
Indicator (H4)	Provides a general multiframe indicator for VT-structured payloads.
Path Growth (Z3 - Z4)	Reserved for future growth.
Tandem Connection (Z5)	Allocated for Tandem Connection Maintenance and the Path Data Channel, as specified by ANSI T1.105.05.

SPE values

The following table lists the types of STS-1 synchronous payload envelope values and their meanings. The system can generate 00, 01, or 04 and can carry any of the other values within the path layer overhead.

Hexadecimal Code	STS-1 SPE
00	Unequipped
01	Equipped nonspecific payload
02	VT-Structured STS-1 SPE
04	Asynchronous mapping for DS3
12	DS4NA Asynchronous mapping
13	Mapping for ATM
14	Mapping for DQDB
15	Asynchronous mapping FDDI

VT path overhead

Virtual tributary (VT) path overhead provides important functions for managing sub-STS-1 payloads; such as, error checking, path status, and signal label. These functions are similar to those provided for STS-1 paths.

SONET digital multiplexing

Introduction

SONET provides the following two multiplexing schemes:

- Asynchronous
- Synchronous

Asynchronous multiplexing

When fiber optic facilities are used to carry DS3 signals, the signal consists of a combination of the following payload signals:

- 28 DS1s
- 14 DS1s
- 7 DS2s

M23 format

Typically, 28 DS1 signals are multiplexed into a DS3 signal, using the M23 format. The M23 format involves bit interleaving of four DS1 signals into a DS2 signal and then bit interleaving of seven DS2 signals into a DS3. In addition, the DS3 rate is not a direct multiple of the DS1 or the DS2 rates due to the bit-stuffing synchronization technique used in asynchronous multiplexing.

Disadvantages of M23 format

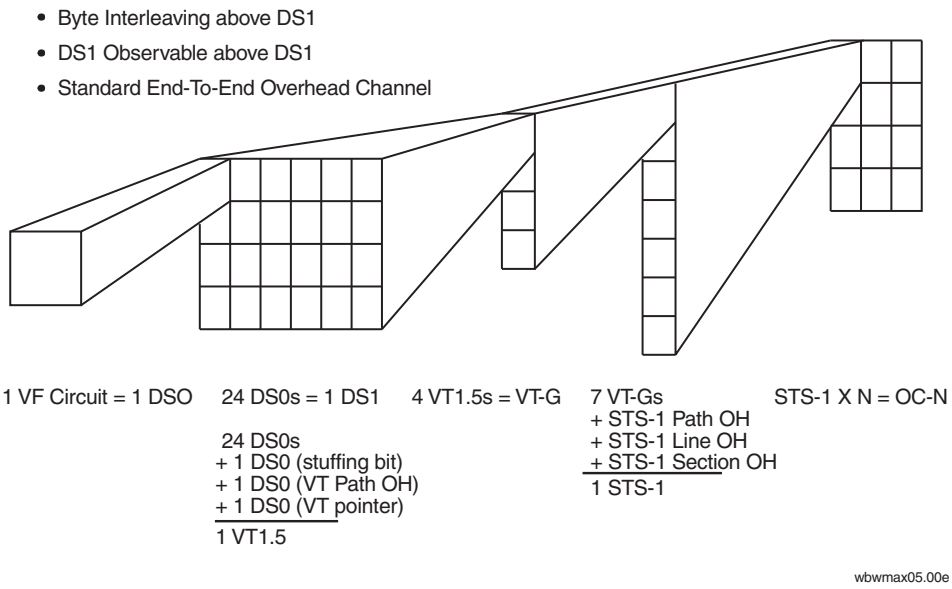
When using an M23 format, identification of DS0s contained in any DS-N signal is complex, and DS0s cannot be directly extracted. An asynchronous DS3 signal must be demultiplexed down to the DS1 level to access and cross-connect DS0 and DS1 signals. In addition, the M23 format does not provide an end-to-end overhead channel for use by OAM&P groups.

Synchronous multiplexing

Synchronous multiplexing is the SONET method of byte interleaving DS1s to a higher signal rate, which permits economical extraction of a single DS1 without the need to demultiplex the entire STS-1 SPE. In addition, SONET provides overhead channels for use by OAM&P groups.

Figure of synchronous multiplexing

The following figure illustrates the SONET technique of mapping a single asynchronous DS1 signal into an STS-1 SPE:



Transporting SONET payloads

Sub-DS3 asynchronous signals (DS1, DS1C, DS2, and E1) are *byte interleaved* into a digital signal called a virtual tributary (VT). The VT is a structure designed for the transport and switching of sub-DS3 payloads. There are four sizes of VTs: 1.5, 2, 3, and 6.

Table

Digital signals DS1 and DS3 are the most important asynchronous signals in the current network. Broadband payloads, such as ATM, are also of great importance.

Input Tributary	Voice Channels (DS0s)	Rate	SONET Signal	Rate
DS1	24 DS0s	1.544 Mbit/s	VT1.5	1.728 Mbit/s
E1 (CEPT)	32 DS0s	2.048 Mbit/s	VT2	2.304 Mbit/s
DS1C	48 DS0s	3.152 Mbit/s	VT3	3.456 Mbit/s
DS2	96 DS0s	6.312 Mbit/s	VT6	6.912 Mbit/s
DS3	672 DS0s	44.736 Mbit/s	STS-1	51.840 Mbit/s
DS4NA	2016 DS0s	139.264 Mbit/s	STS-3c	155.520 Mbit/s

Input Tributary	Voice Channels (DS0s)	Rate	SONET Signal	Rate
ATM	2016 DS0s	149.760 Mbit/s	STS-3c	155.520 Mbit/s
FDDI	2016 DS0s	125.000 Mbit/s	STS-3c	155.520 Mbit/s

SONET interface

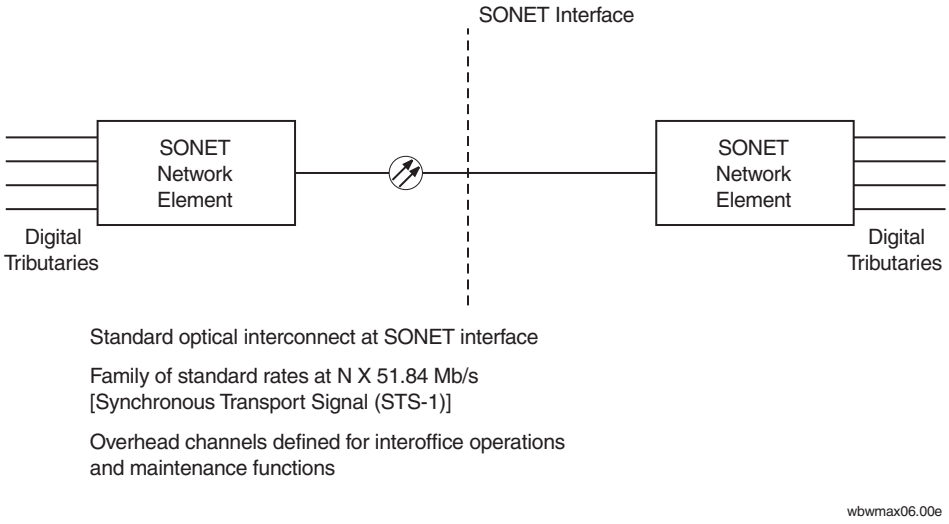
Introduction

This section describes the SONET interface.

Description

The SONET interface provides the optical mid-span meet between SONET network elements. A SONET network element is the hardware and software that affects the termination or repeating of a SONET standard signal.

Figure of SONET interface



SONET multiplexing process

Introduction

SONET provides for multiplexing of asynchronous DS1s, synchronous DS1s, and asynchronous DS3s.

Multiplexing process

The following describes the process for multiplexing a signal.

1 Input DS1 or DS3 tributary is mapped.

In the case of DS1 inputs, three time slots (DS0s) are added to the incoming signal, becoming a VT1.5.

An asynchronous DS1 that fully meets the specified rate is mapped into the VT1.5 SPE as clear channel input since no framing is needed.

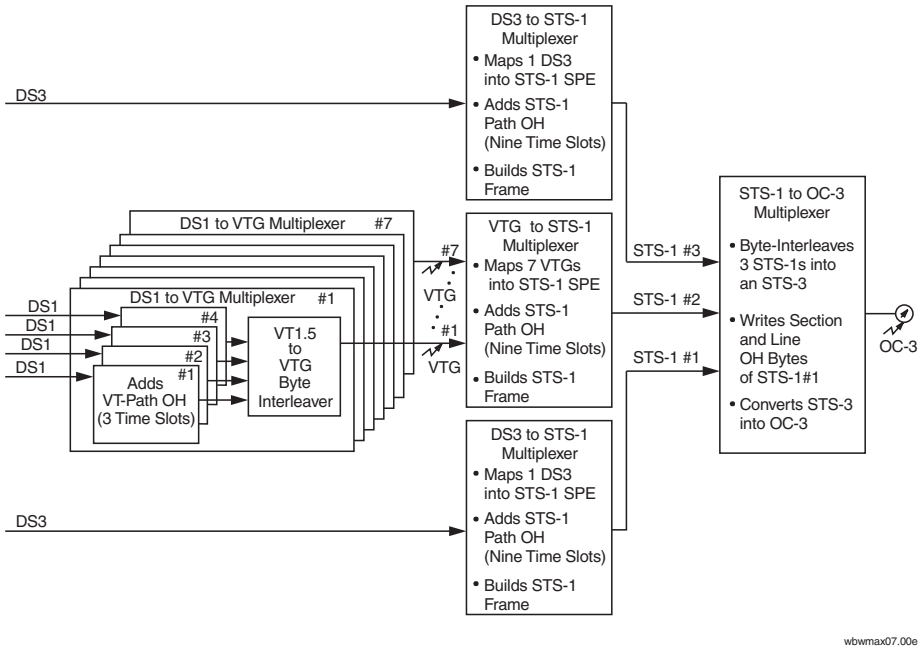
- Each VT1.5 carries a single DS1 payload.
- Four VT1.5s are bundled into a VT group (VT-G).
- Seven VT-Gs are byte interleaved into an STS-1 frame.

  Important! The VT-G to-STS-1 multiplex is a simple byte interleaving process, so individual VT signals are easily observable within the STS-1. Thus, cross-connections and add/drop can be accomplished without the back-to-back mux/demux steps required by asynchronous signal formats.

2 After VTs are multiplexed into the STS-1 SPE, the path, line, and section overhead is added.

3 Scrambled STS-N signal is transported to the optical stage.

Figure of SONET multiplexing process



SONET demultiplexing process

Introduction

Demultiplexing is the inverse of multiplexing. This topic describes how to demultiplex a signal.

Demultiplexing process

The following describes the process for demultiplexing an STS-1 signal to a DS1 signal.

1. The unscrambled STS-1 signal from the optical conversion stages is processed to extract the section and line overhead and accurately locate the SPE.
2. The STS-1 path overhead is processed to locate the VTs. The individual VTs are then processed to extract VT overhead and, via the VT pointer, accurately locate the DS1.
3. The DS1 is desynchronized, providing a standard DS1 signal to the asynchronous network.

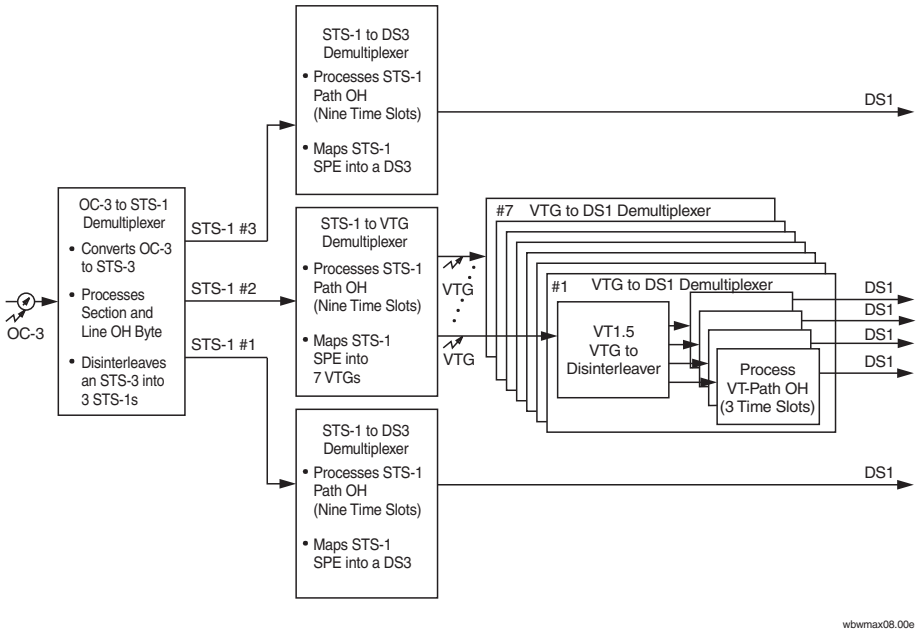
Key points

Remember the following key points when demultiplexing a signal:

- The SONET frame is a fixed time (125 *m*s) and no bit-stuffing is used.
- The synchronous payload envelope (SPE) can *float* within the frame. This is to permit compensation for small variations in frequency between the clocks of the two systems that may occur if the systems are independently timed (plesiochronous timing). The SPE can also *drift* across the 125-*m*s frame boundary.

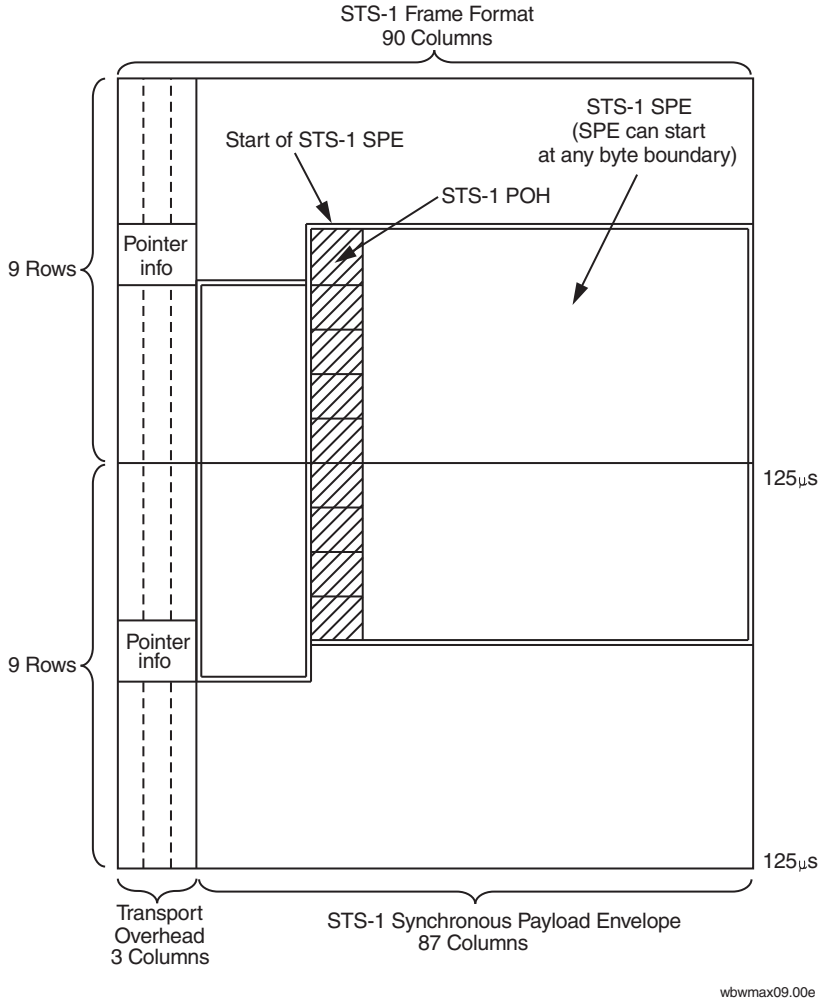
  Important! SONET STS pointers are used to locate the SPE relative to the transport overhead.

Figure of SONET demultiplexing process



SPE figure

The following figure illustrates the SPE floating within an STS-1 frame:



SONET transport rates

Introduction

Higher rate SONET signals are created by byte-interleaving N STS-1s to form an N STS-1 signal.

Creating higher rate signals

The desired N STS-1s are created by:

- Adjusting all payload pointers and regenerating the section and line overhead bytes to be in phase with each other and the outgoing multiplexed signal
- Scrambling and converting the N STS-1 to an optical carrier – level N (OC-*N*) signal

SONET transport rates

OC Level	Line Rate (Mbit/s)	Capacity
OC-1	51.84	28 DS1s or 1 DS3
OC-3	155.52	84 DS1s or 3 DS3s
OC-12	622.08	336 DS1s or 12 DS3s
OC-48	2488.32	1344 DS1s or 48 DS3s
OC-192	9953.28	5376 DS1s or 192 DS3s
OC-768	39813.12	21504 DS1s or 768 DS3s

Appendix C: Remote test access

Overview

Purpose

This section provides descriptive information about the concepts of the 1675 LambdaUnite MSS remote test access feature.

Contents

Remote test access – general description	C-2
Remote test access modes	C-10
Emulated Selector Bridge (ESB) topologies	C-21
DS3 test access port	C-25

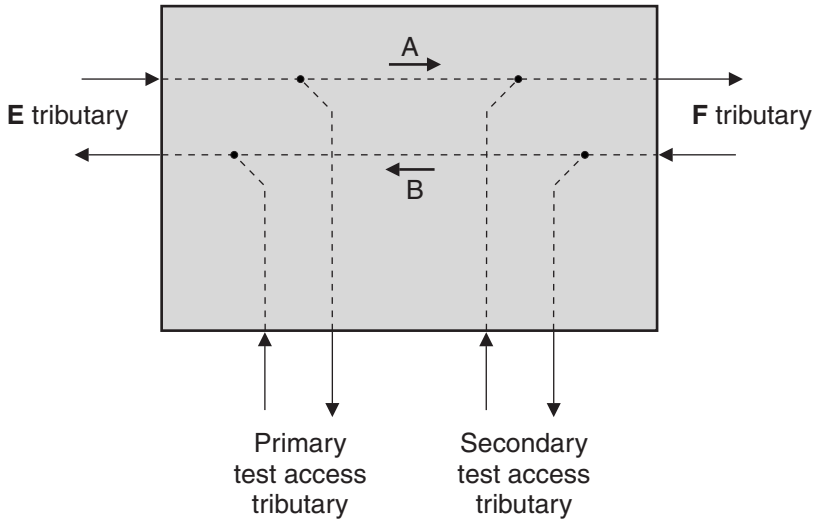
Remote test access – general description

Introduction

The 1675 LambdaUnite MSS test access feature makes it possible to access individual tributaries for testing purposes.

Naming conventions for tributaries and directions

These naming conventions for tributaries and directions are used:



E tributary and F tributary

The E tributary and F tributary are the tributaries under test. The E and F tributaries are the ends of a unidirectional or bidirectional cross-connection, with the dashed lines indicating the signal traveling through the system.

If an idle (not cross-connected) tributary is the focus of test access, then it is always the E tributary.

Note that the arrows indicate individual tributaries, even if a port, such as an OC-48 port for example, supports multiple tributaries.

A and B direction

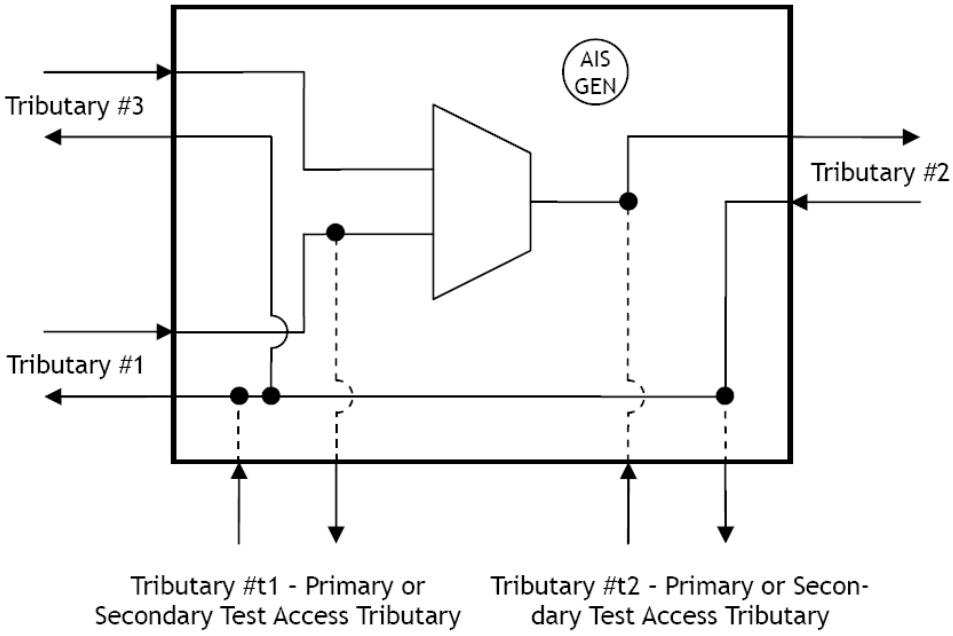
The signal path from the E-end to the F-end is referred to as the “A” direction; the opposite direction as the “B” direction. If there is only a one-way cross-connection, the signal travels in the “A” direction.

Primary and secondary test access tributary

The primary and secondary test access tributaries are the tributaries that can be connected to the test equipment for measurement and evaluation.

Model for path-protected and bridged cross-connections

The following figure shows the basic setup of test access for path-protected cross-connections:



In this setup the tributaries can take the following roles in a remote test access session depending on the test access mode:

Test access mode	E tributary	F-tributary	Primary test access tributary	Secondary test access tributary
MONE/SPLTE	#1	#2 or none	#t1	None
	#2	#1 or none	#t2	None
MONF/SPLTF	#1	#2	#t2	None
	#2	#1	#t1	None
MONEF/ SPLTEF	#1	#2	#t1	#t2
	#2	#1	#t2	#t1

Note: Please observe the following general rules:

Test access to a tributary used as input to a path selector
When creating a test session where the tributary under test is an input to a path selector the input signal is connected to the test access tributary in front of the selector. This means that always the input signal to a path selector is monitored and never the output signal.
When creating a SPLT-mode test session where the tributary under test is an input to a path selector the signal is always split at the output of the path selector. As a consequence the whole path-protected signal is affected and replaced by AIS or test traffic.

Test access to a tributary used as input to a path bridge
When creating a test session where the tributary under test is an input to a path bridge the input signal is connected towards the test access tributary in front of the bridge.
When creating a SPLT-mode test session where the tributary under test is an input to a path bridge only the leg identified by the E-tributary and F-tributary is split, the other leg of the path bridge remains unaffected. The insertion of AIS or test traffic from a test access tributary is always done on the leg being split only, the other leg remains unaffected.

The position of the path selector may change during a test session
The position of the path selector may change during a test session and may be in a different position after terminating a test session compared to the position when creating the test session. The actual position is notified using the normal mechanisms.

Behavior of the wait-to-restore timer and hold-off timer in combination with a SPLT-mode test session
When a SPLT-mode test session is created which disconnects the output of the path selector from the E-tributary or F-tributary, and a wait-ro-restore timer or a hold-off timer is currently running, the expiry of the timer has no effect. When removing this SPLT-mode test session again (thus reconnecting the output of the path selector again), the current state of the input signals is evaluated without applying the hold-off timer or the wait-to-restore timer.

Clearing of `Path Switch Denial` when creating a SPLT-mode test session

When a `Path Switch Denial` indication for a path selector was sent to the management interface (e.g. due to a lockout request and the worker leg having a signal fail condition or a Forced Switch request and the selected leg having a signal fail condition) and now a SPLT-mode test session is created which disconnects the output of the path selector from the E-tributary or F-tributary, the `Path Switch Denial` indication may get cleared with this action and is not re-created again when removing this SPLT-mode test session (thus reconnecting the output of the path selector again).

Unidirectional and bidirectional topologies

For the remote test access, path protection topologies must be considered either unidirectional or bidirectional. There can be a path selector in done direction while in the opposite direction there might be no connection, an unprotected connection, or a path bridge.

A topology is a bidirectional topology, if

- there is a cross-connection from tributary #1 to tributary #2 *and*
- there is also a cross-connection in opposite direction from tributary #2 to tributary #1.

This is applicable regardless whether the connections between tributary #1 and #2 are unprotected, bridged, path-protected are any combinations of both.

A topology is a unidirectional topology, if

- there is a cross-connection from tributary #1 to tributary #2 *and*
- there is *no* cross-connection in opposite direction from tributary #2 to tributary #1.

This is applicable regardless whether the connections between tributary #1 and #2 are unprotected, bridged, path-protected are any combinations of both. There might be other cross-connections from any other tributary with tributary #1 as output tributary.

Restrictions

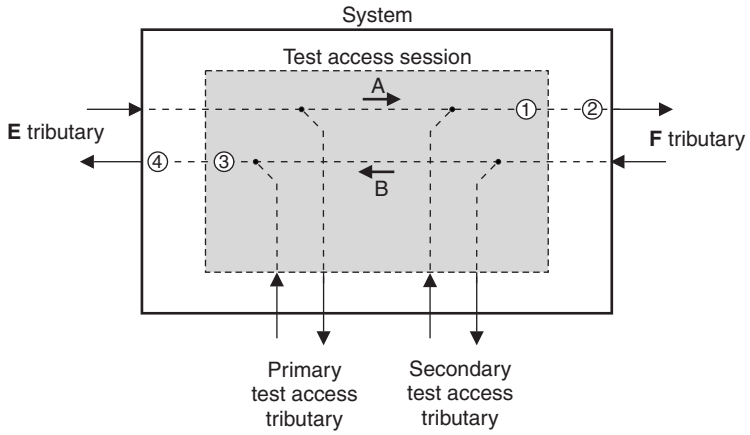
The following restrictions have to be considered

- Path bridge input tributaries may be associated with an E tributary or an F tributary depending on the topology (definitions see above)
- Only one of the path selector inputs or of the broadcast legs can be examined by test access at a time
- The bridge to the test access tributary is not counted as a leg for the 1:N broadcast
- Only the leg which has a test access function is reflected n responses of test access retrievals
- Combinations of loopback, roll, and topology conversion with remote test access are not allowed. Only exception: It is possible to create a loopback at the X tributary or Y tributary not involved directly in the test access session (see figures below)

The following figures illustrate how test access modes can be applied for path protected cross-connections.

Naming conventions for output modes

These naming conventions are used for cross-connection output modes and test access output modes:



Legend:

1 The F output mode of the test access session (E tributary → F tributary, A direction).

2 The cross-connection output mode (source → destination).

3 The cross-connection return output mode (destination → source).

4 The E output mode of the test access session (F tributary → E tributary, B direction).

Supported test access tributary rates

These test access tributaries and tributaries under test are supported, depending on the type of interface (cf. "Naming conventions for tributaries and directions" (p. C-2)):

Interface	Tributaries under test (E and F tributaries)	Test access tributaries
OC-3	VT1.5[1], STS-1, STS-3c	VT1.5[1], STS-1, STS-3c
STM-1 / STM-1E	VC-3[2], VC-4	VC-3[2], VC-4
OC-12	VT1.5[1], STS-1, STS-3c, STS-12c	VT1.5[1], STS-1, STS-3c, STS-12c
STM-4	VC-3[2], VC-4, VC-4-4C	VC-3[2], VC-4, VC-4-4C

Interface	Tributaries under test (E and F tributaries)	Test access tributaries
OC-48	VT1.5[1], STS-1, STS-3c, STS-12c, STS-48c	VT1.5[1], STS-1, STS-3c, STS-12c, STS-48c
OC-48 (fully transparent services)	STS-3c	-
STM-16	VC-3[2], VC-4, VC-4-4C, VC-4-16C	VC-3[2], VC-4, VC-4-4C, VC-4-16C
STM-16 (fully transparent services)	VC-4	-
OC-192	VT1.5[1], STS-1, STS-3c, STS-12c, STS-48c, STS-192c	VT1.5[1], STS-1, STS-3c, STS-12c, STS-48c, STS-192c
STM-64	VC-3[2], VC-4, VC-4-4C, VC-4-16C, VC-4-64C	VC-3[2], VC-4, VC-4-4C, VC-4-16C, VC-4-64C
1 GbE	STS-1, VC-4	-
10 GbE (10GEPL)	STS-1, STS-3c, VC-4	-
DS3 (T3)	STS-1	STS-1
EC-1	STS-1	STS-1
TMA port of a TransMUX unit	STS-1	–
TMS port of a TransMUX unit	VT1.5[1]	VT1.5[1]
DS3 test access port on a TransMUX unit[3]	VT1.5 carrying DS1	VT1.5 carrying DS1

Notes:

1. Remote test access for VT1.5 lower-order tributaries is only supported if a lower-order cross-connection unit of type LOXC40G2S/1 or LOXC40G3S/1 is used.
2. VC-3 represents higher-order VC-3 only.
3. Also refer to the section "DS3 test access port" (p. C-25).

Important! Remote test access is *not* supported for SDH lower-order tributaries (VC-12 and lower-order VC-3).

Important! Remote test access is *not* supported on 1+1 MSP/APS protection schemes. For MS-SPRing/BLSR protection schemes, the restrictions mentioned below apply.

Remote test access modes

The possible test access modes are shown in the following list. Please refer to "Remote test access modes " (p. C-10) for a more detailed description of the individual modes.

1. Single test tributary modes
   - "Monitor E" mode (MONE)
   - "Monitor F" mode (MONF)
   - "Split E" mode (SPLTE)
   - "Split F" mode (SPLTF)
2. Dual test tributary modes
   - "Monitor E and F" mode (MONEF)
   - "Split E and F" mode (SPLTEF)

Specific considerations

On add/drop legs of BLSR/MS-SPRing interfaces, all test access modes are supported without any special considerations.

On BLSR/MS-SPRing intra-ring through cross-connections, only the test access modes MONE, MONF, and MONEF are supported.

Remote test access mode changes

Each user with a user privilege code of at least *T4* can change the test access mode:

- among the supported single test tributary modes, or
- among the supported dual test tributary modes.

Changing the test access mode between a single test tributary mode and a dual test tributary mode is *not* possible.

Cross-connection output mode changes

For any of the test access modes, it is possible to change the cross connection output mode of the E and/or F tributaries during a test access session.

Remote test access sessions

Up to a maximum number of 1000 test access sessions (test sessions) can be set up, as long as tributaries are available to be used as test access tributaries or E or F tributaries.

Each user with a user privilege code of at least *T4* can initiate, modify, or terminate a test session at any time, while the system is in normal operation mode. Initiating, modifying, or terminating a test session is *not* possible when the system is in maintenance condition. Changing the test access mode or cross connection output mode is *not* supported for lower-order tributaries.

The remote test access feature can be used in conjunction with the following test equipment:

- Spirent *REACT 2001*™ Remote Access and Test System (Broadband Remote Test Unit (BRTU): OC-12 Module)
- Acterna CT-6000 *CenTest*® Broadband Test Unit

Required tributaries

These are the tributaries which need to be specified depending on the test access mode:

Mode	E tributary	F tributary	Primary test access tributary	Secondary test access tributary
MONE	●	●[1]	●	–
MONF	●	●[1]	●	–
MONEF	●	●	●	●
SPLTE	●	●[1]	●	–
SPLTF	●	●[1]	●	–
SPLTEF	●	●	●	●

Notes:

1. In the case of an idle (not cross-connected) E tributary, the F tributary must *not* be specified.

Monitor before split

The initial test access mode in a test session is restricted to one of the not traffic affecting monitoring modes (“Monitor E” (MONE), “Monitor F” (MONF), or “Monitor E and F” (MONEF)).

This means that you initially have to establish a test session with one of the above mentioned modes. Afterwards, other test access modes can then be realized by modifying the existing test session.

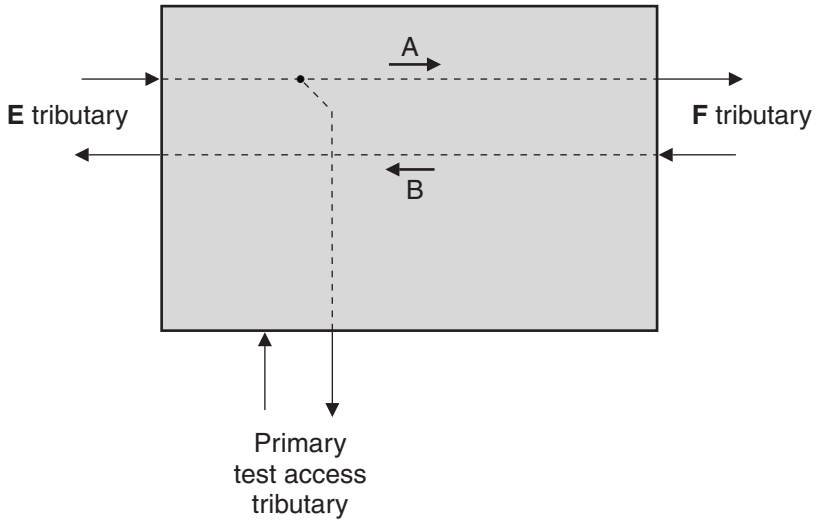
This principle is referred to as “monitor before split”.

Remote test access modes

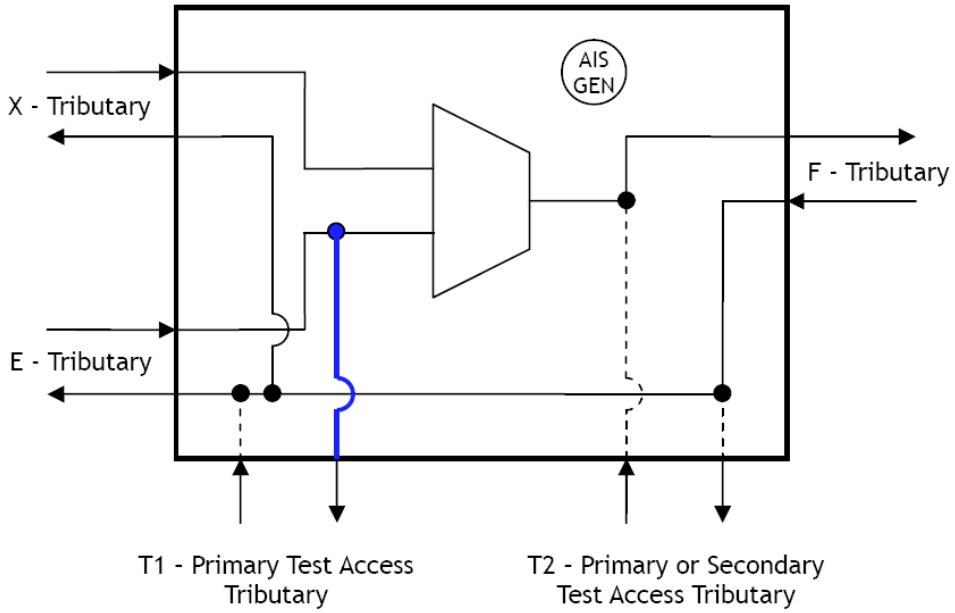
“Monitor E” mode (MONE)

The MONE mode is supported on unidirectional and bidirectional cross-connections, and on not cross-connected (idle) tributaries.

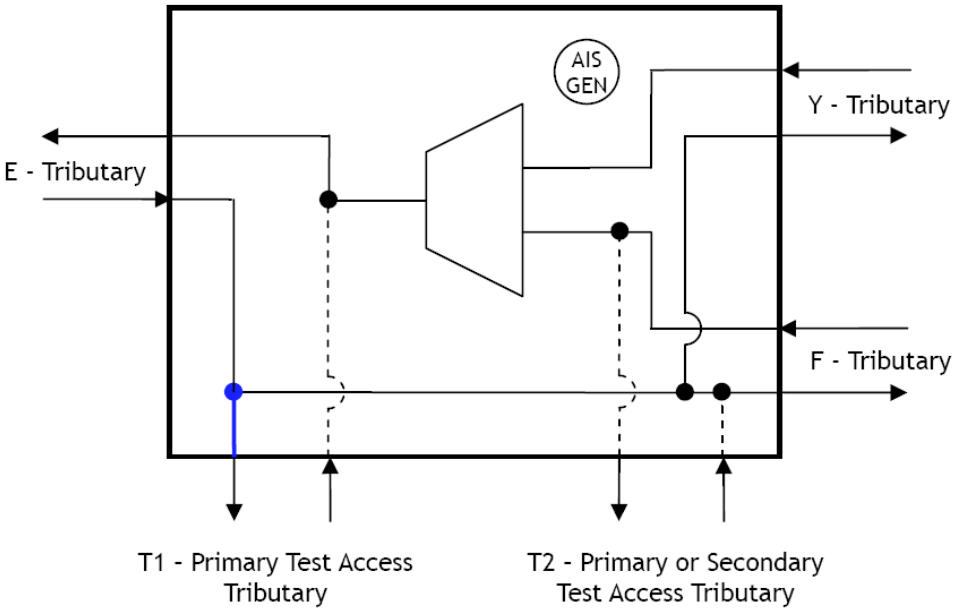
The E tributary is system-internally broadcasted to the original destination, and to the primary test access tributary. If there is no existing cross-connection for the E tributary, then a cross-connection to the primary test access tributary is generated system-internally.



The MONE mode allows to monitor the working or the protection selector input of a path protection:



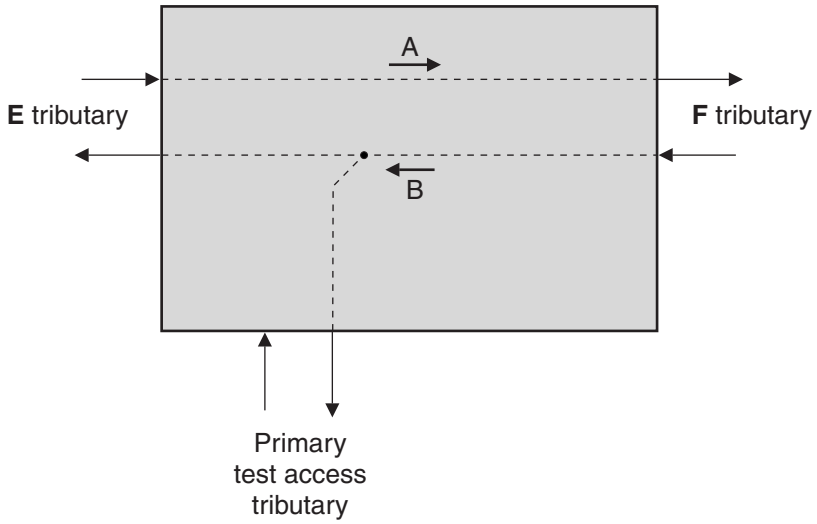
The MONE mode also allows to monitor the input of a path bridge:



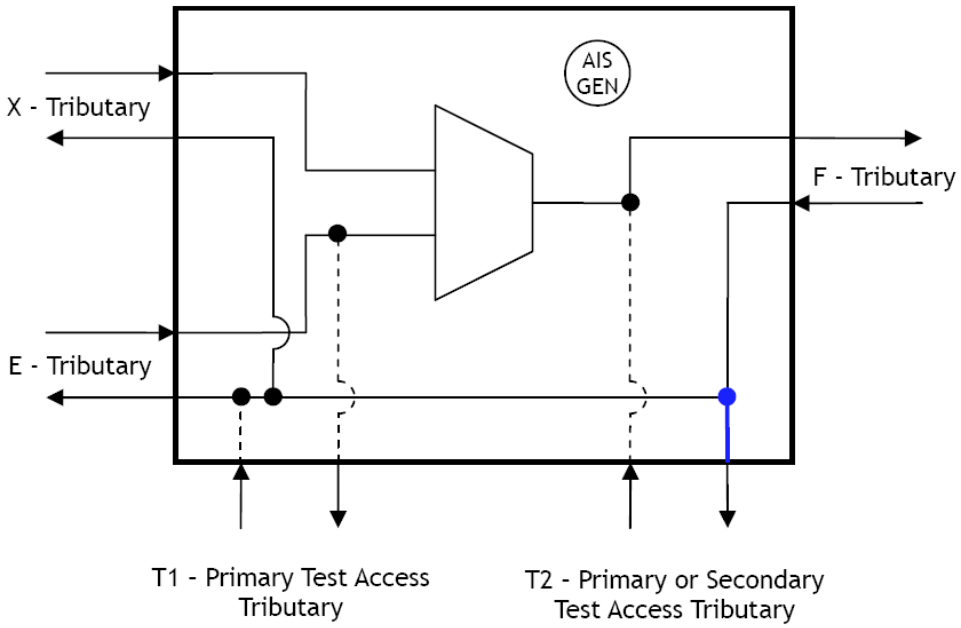
"Monitor F" mode (MONF)

The MONF mode is supported on bidirectional cross-connections only.

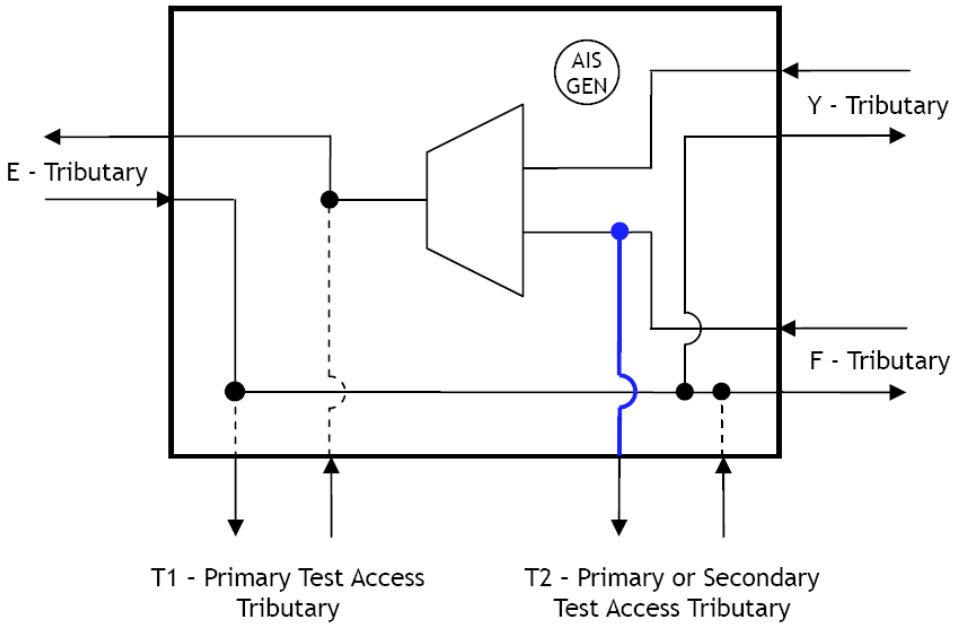
The F tributary is system-internally broadcasted to the original destination, and to the primary test access tributary.



The MONF mode allows to monitor the input of a path bridge:



The MONF mode also allows monitoring the input of a path selector:

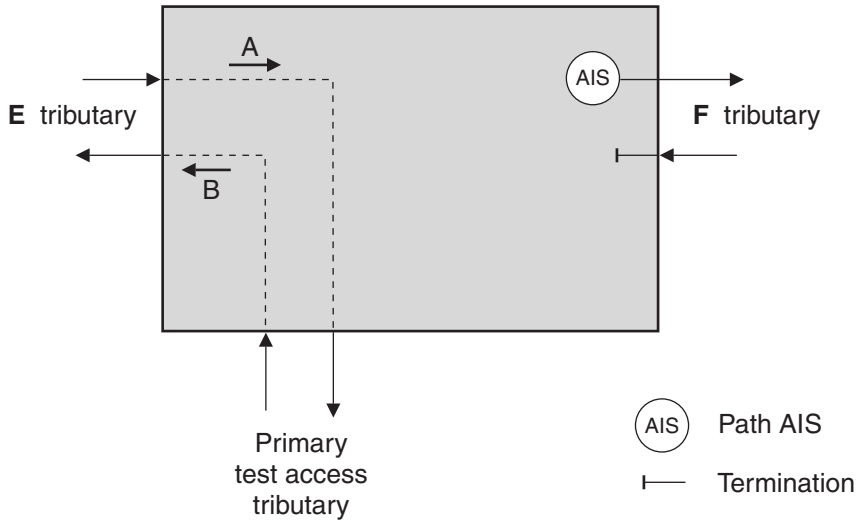


"Split E" mode (SPLTE)

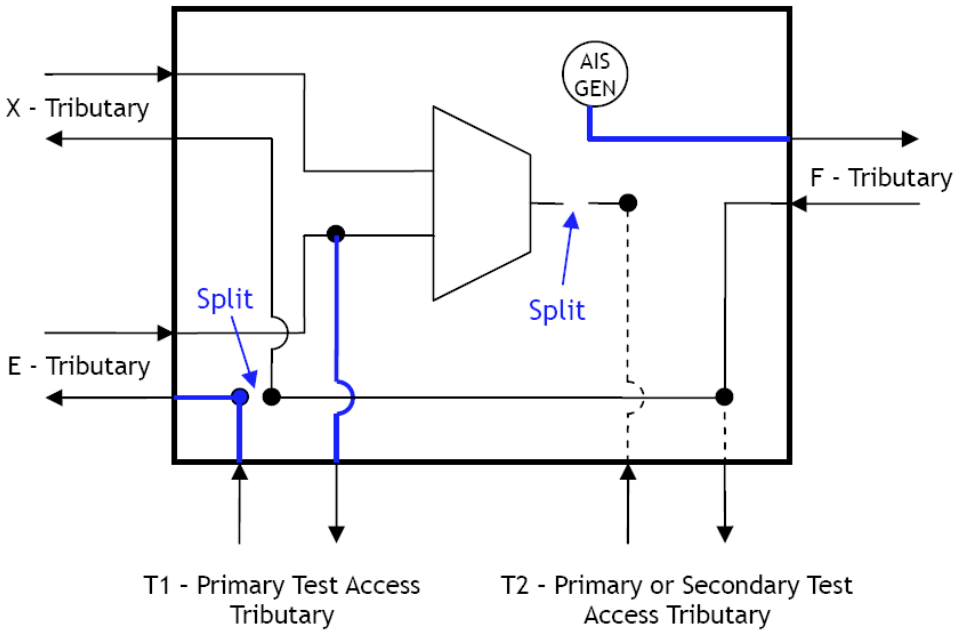
The SPLTE mode is supported on unidirectional and bidirectional cross-connections, and on not cross-connected (idle) tributaries.

The existing unidirectional or bidirectional cross-connection between the E and F tributaries is split off, and the E tributary is cross-connected to the primary test access tributary.

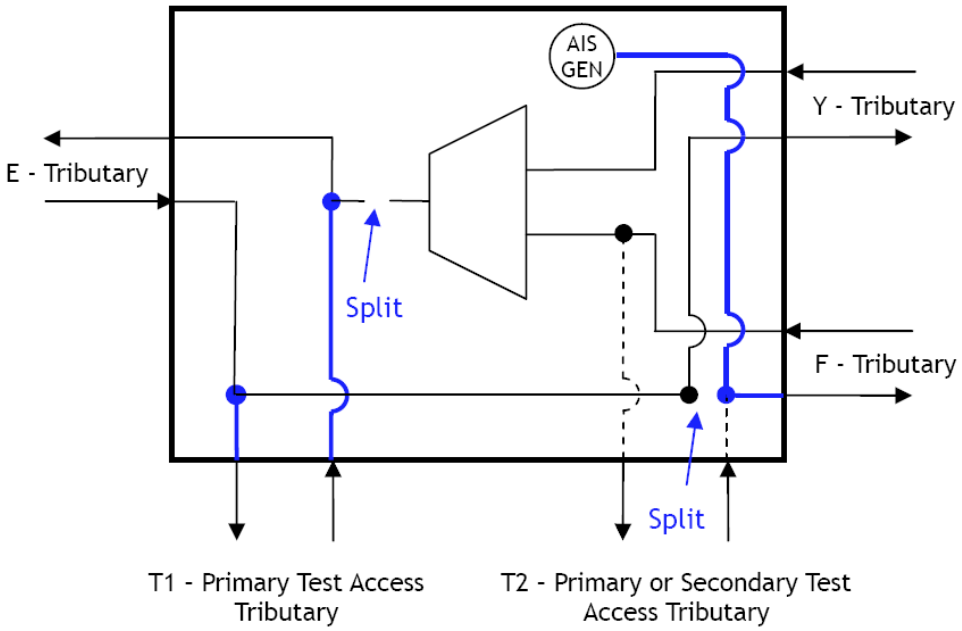
In the case of a unidirectional or bidirectional cross-connection, path AIS is inserted at the F tributary in the A direction during the split. In the B direction, the F tributary is terminated in the case of a bidirectional cross-connection. In the case of a unidirectional cross-connection, there is no B direction. No action is performed at the F tributary in the case of an idle E tributary.



The SPLTE mode allows to monitor the working or the protection selector input of a path protection as well as to inject test traffic in the output direction of the E tributary only. The input of the F tributary only remains connected to the X tributary and the output of the F tributary gets provided by an AIS signal:



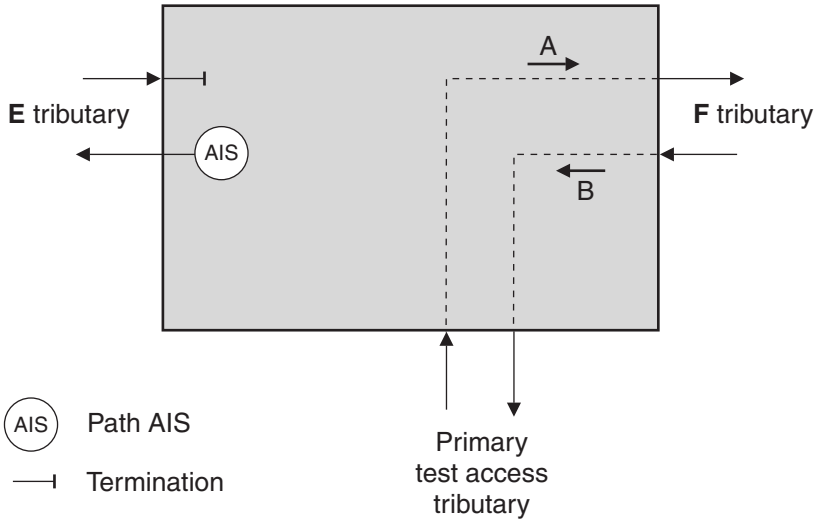
The SPLTE mode also allows to monitor the input of a path bridge as well as to inject test traffic for the output of the E tributary. As side effect the input of the E tributary only remains connected to the Y tributary, the output of the F tributary gets provided by an AIS signal:



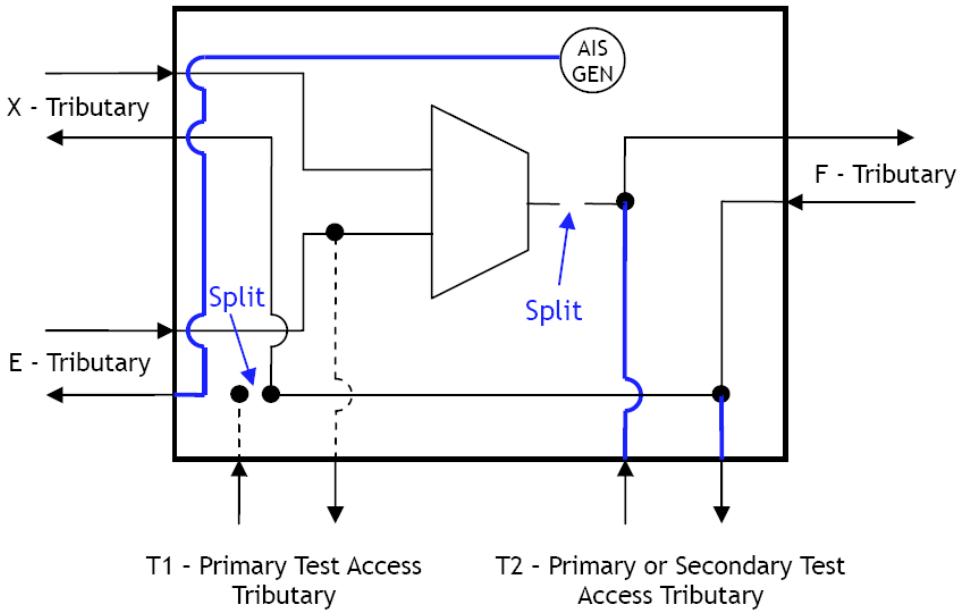
"Split F" mode (SPLTF)

The SPLTF mode is supported on bidirectional cross-connections only.

The existing bidirectional cross-connection between the E and F tributaries is split off, and the F tributary is cross-connected to the primary test access tributary. During the split, path AIS is inserted at the E tributary in the B direction, and in the A direction, the E tributary is terminated.



The SPLTF mode allows to monitor the input of a path bridge as well as to inject test traffic for the output of the F tributary.



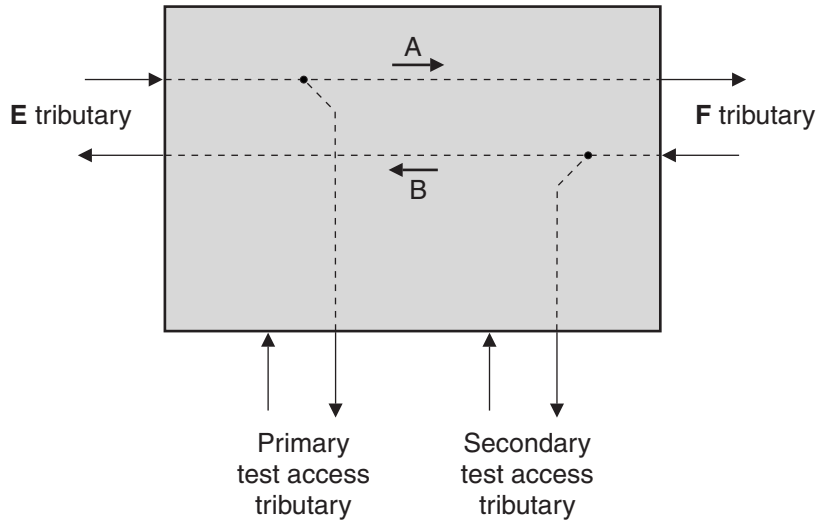
The SPLTF mode also allows to monitor the input of a path selector as well as to inject test traffic for the output of the F tributary.

"Monitor E and F" mode (MONEF)

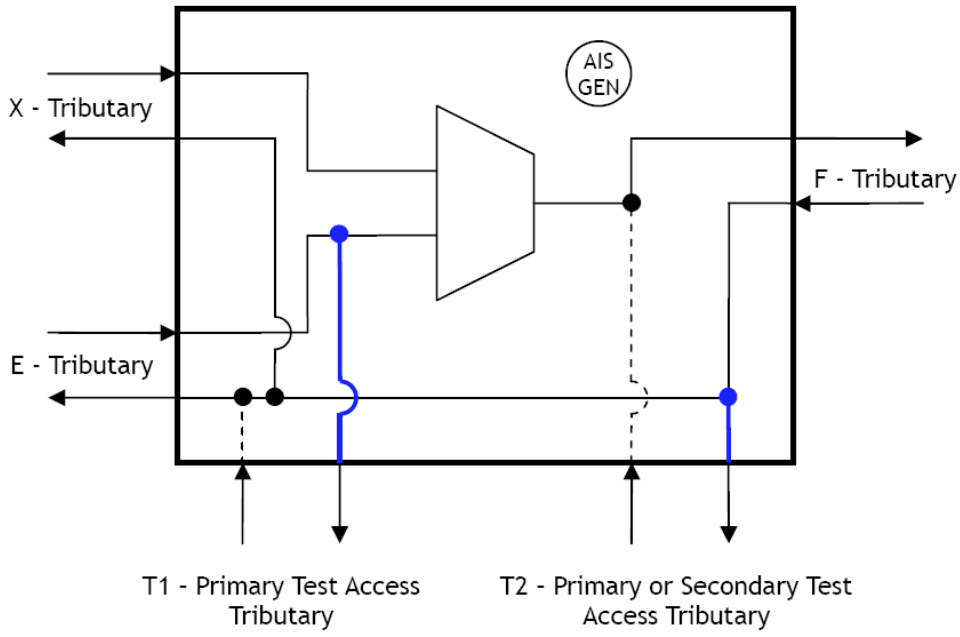
The MONEF mode is supported on bidirectional cross-connections only.

This mode is a combination of MONE and MONF. The E tributary is system-internally broadcasted to the original destination, and to the primary test access tributary.

The F tributary is system-internally broadcasted to the original destination, and to the secondary test access tributary.



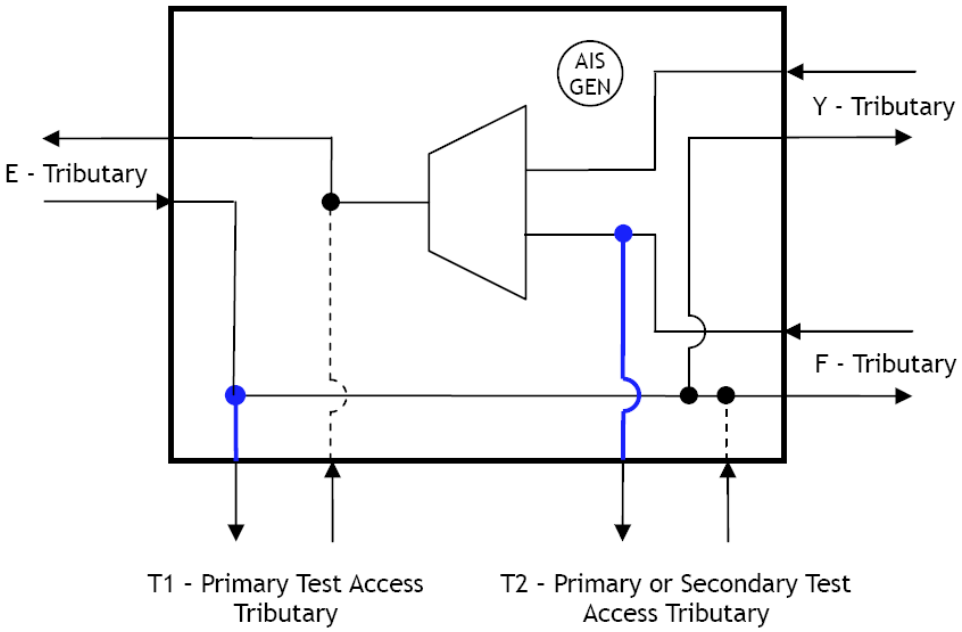
The MONEF mode can be applied to a topology where the E tributary is an input tributary of a path selector or output of a bridge:



The previous figure also applies to other bidirectional path-protected or bridged topologies, but not for unidirectional or idle topologies. This means that, for example:

- only a normal, unprotected connection from E to F is present
- only a single point-to-point connection from F to E is present

The MONEF mode can also be applied to the mirrored topology where the E tributary is input to a bridge or is the output of a path selector:



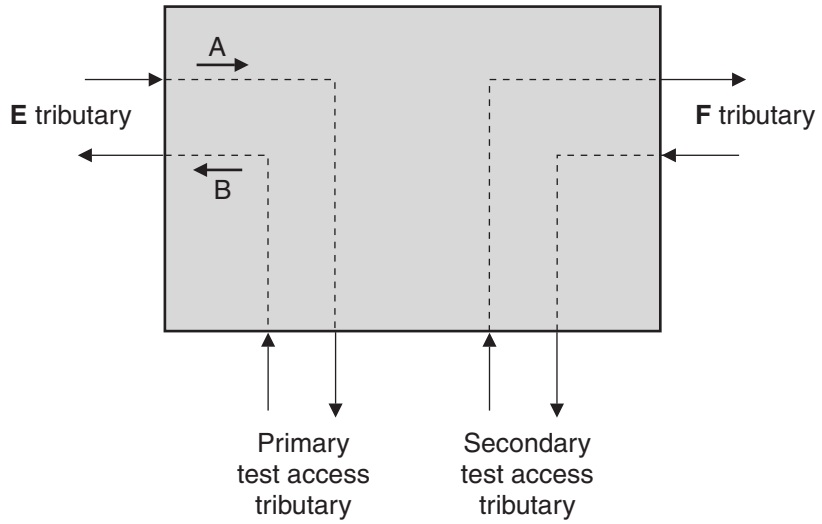
The previous figure also applies to other bidirectional path-protected or bridged topologies, but not for unidirectional or idle topologies. This means that, for example:

- only a normal, unprotected connection from F to E is present
- only a single point-to-point connection from E to F is present

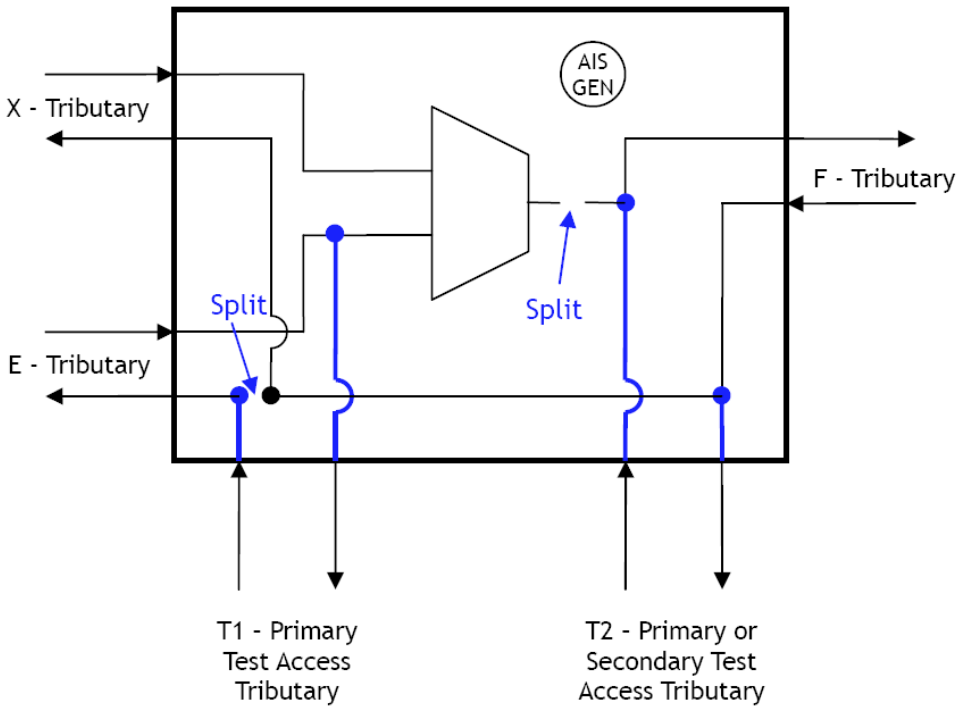
"Split E and F" mode (SPLTEF)

The SPLTEF mode is supported on bidirectional cross-connections only.

This mode is a combination of SPLTE and SPLTF. The bidirectional cross-connection between the E and F tributaries is split off. The E tributary is cross-connected system-internally to the primary test access tributary. The F tributary is cross-connected system-internally to the secondary test access tributary.



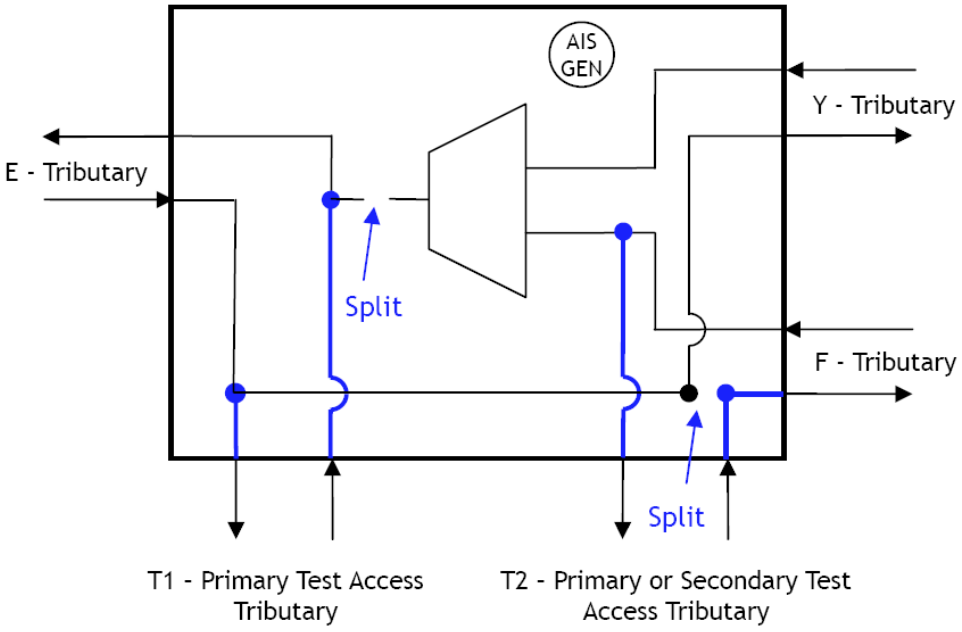
The SPLTEF mode can be applied in a path-protected topology where the E tributary is the input of the path selector or output of a bridge:



The previous figure also applies to other bidirectional path-protected or bridged topologies, but not for unidirectional or idle topologies. This means that, for example:

- only a normal, unprotected connection from E to F is present
- only a single point-to-point connection from F to E is present

SPLTEF can also be applied to the topology where the E tributary is the input to a bridge or the output of a path selector:



The previous figure also applies to other bidirectional path-protected or bridged topologies, but not for unidirectional or idle topologies. This means that, for example:

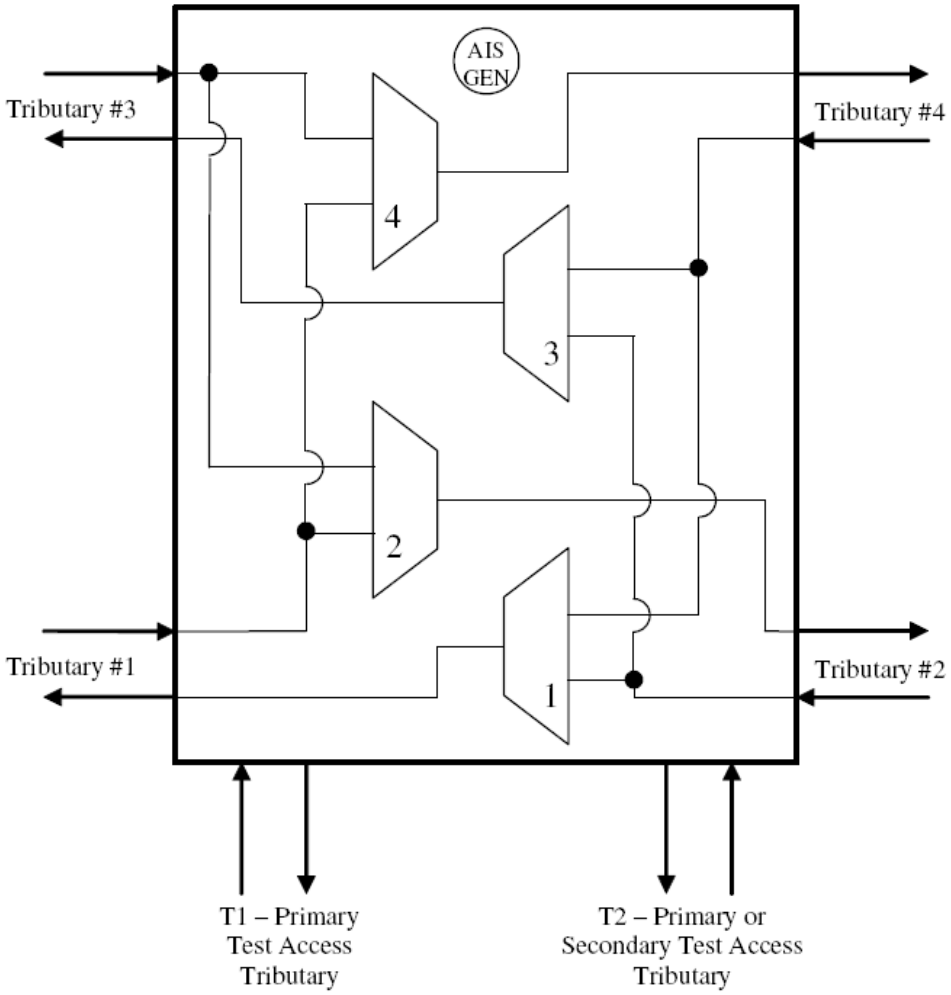
- only a normal, unprotected connection from F to E is present
- only a single point-to-point connection from E to F is present

Emulated Selector Bridge (ESB) topologies

Emulated Selector Bridge (ESB)

The so-called Emulated Selector Bridge (ESB) construct is needed to support back-to-back SNCP/UPSR topologies.

In general, a bidirectional ESB looks as follows:



To examine the inputs of any of the path selectors in this topology the tributaries under test have to be chosen as follows:

Selector	E tributary	F tributary
1	#2 or #4	#1
2	#1 or #3	#2

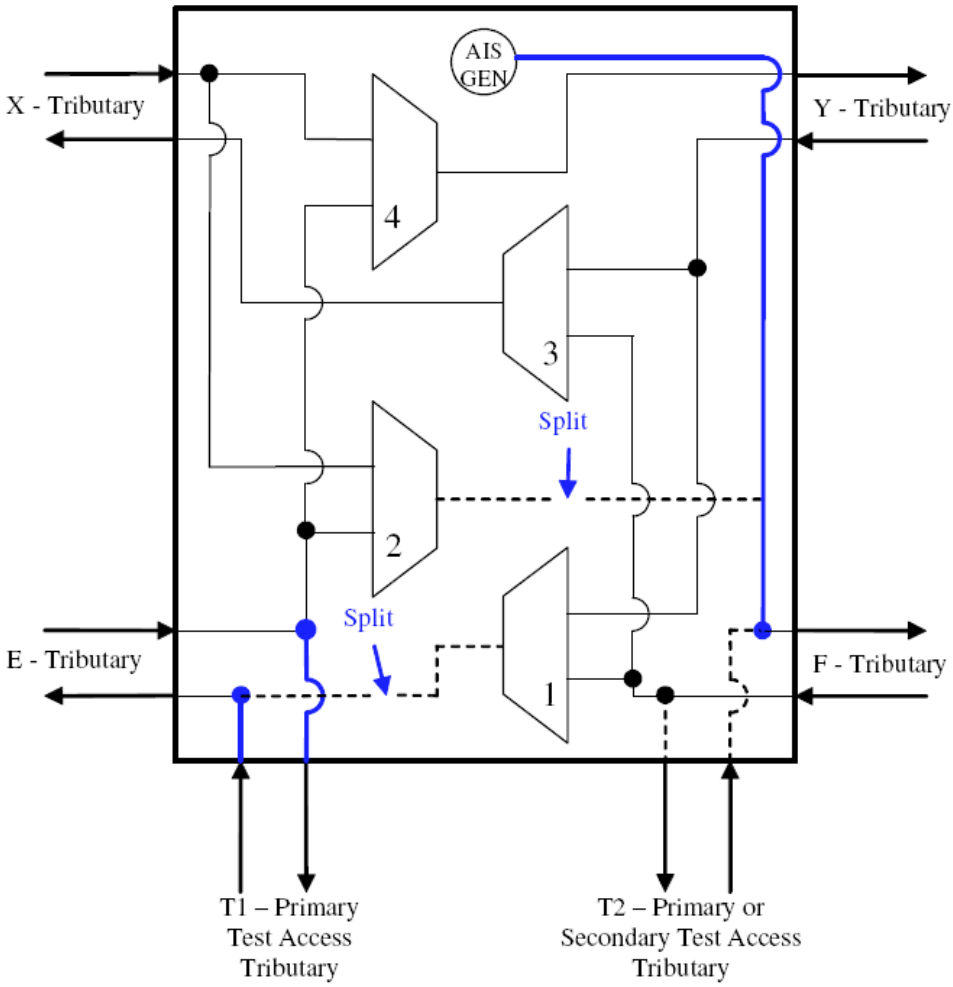
Selector	E tributary	F tributary
3	#2 or #4	#3
4	#1 or #3	#4

MONE, MONF, MONEF

When creating a test session of type MONE, MONF or MONEF on an ESB topology the same mechanisms apply as for normal bridged and/or path protected connections, i.e. a signal is always monitored in front of a selector.

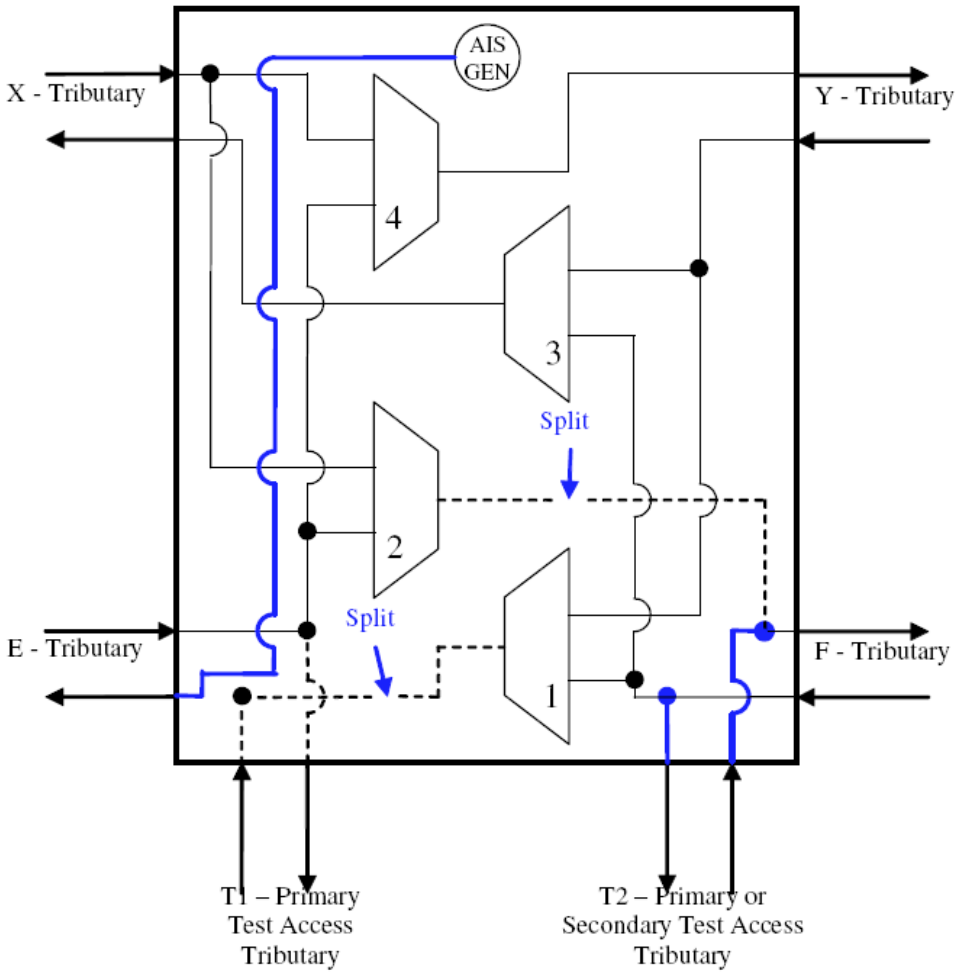
SPLTE

When creating a test session of type SPLTE on an ESB topology the same mechanisms apply as for normal bridged and/or path-protected connections, according to the following figure:



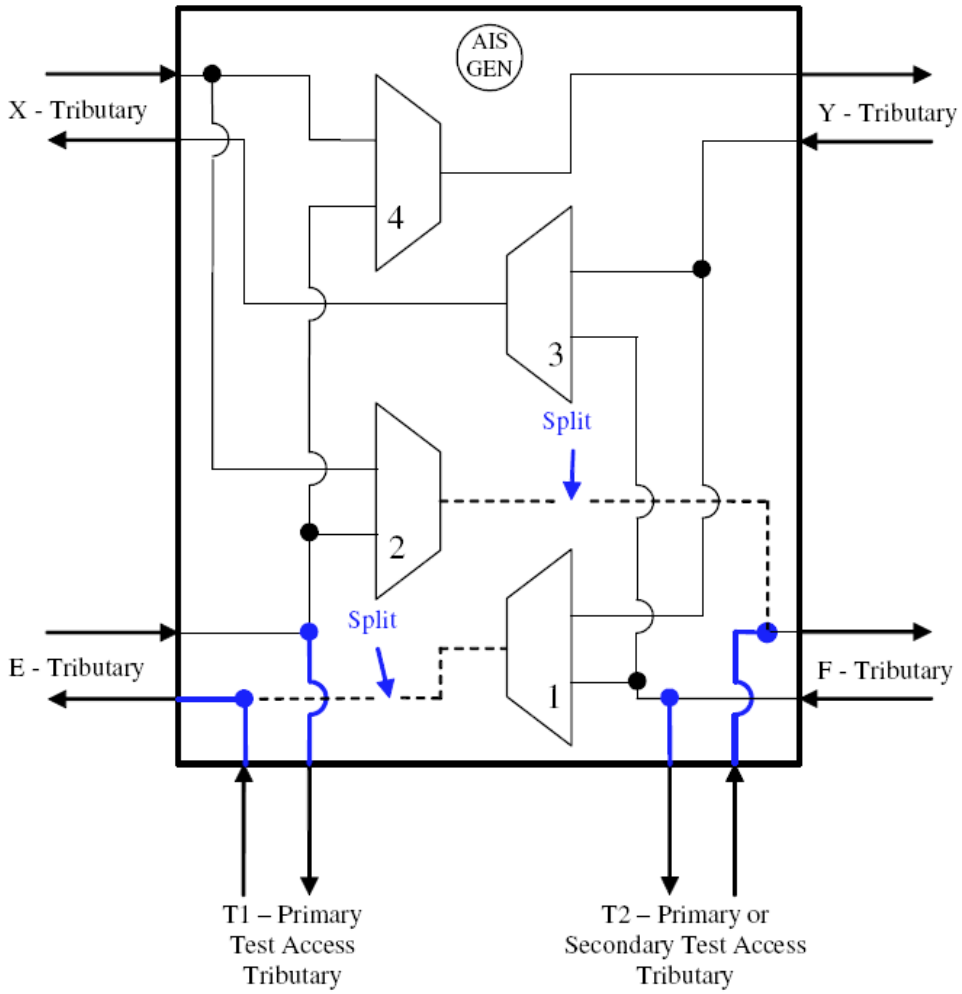
SPLTF

When creating a test session of type SPLTF on an ESB topology the same mechanisms apply as for normal bridged and/or path-protected connections, according to the following figure:



SPLTEF

When creating a Test Session of type SPLTEF on an ESB topology the same mechanisms apply as for normal bridged and/or path-protected connections, according to the following figure:



Change of input conditions while a SPLT-mode test session is active

When a test session of type SPLTE, SPLTF or SPLTEF is set up on an ESB topology and the input conditions change while the test session is active it may happen that:

- a different state of the two selectors having the same inputs is visible/reported at the TL1 interface.
- in case of non-revertive protection groups the two selectors might end up in different positions when a failure of one input leading to a protection switch happens while a SPLT-mode test session exists and the failure clears before deleting the test session again.

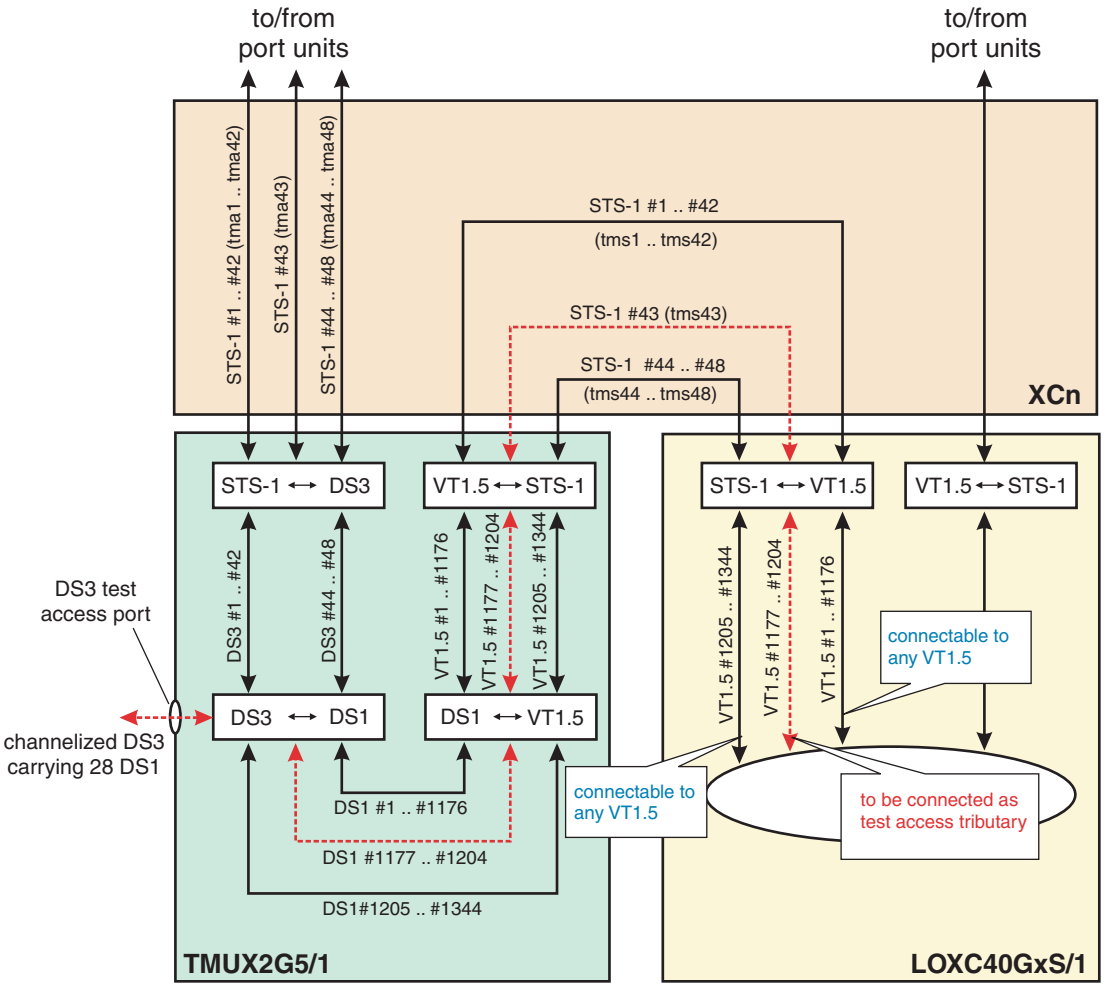
DS3 test access port

Connectivity

The system provides a dedicated DS3 (T3) test access port located at the front panel of the TransMUX unit (see "Portless TransMUX unit (TMUX2G5/1)" (p. 4-27)).

The DS3 test access port provides access to a channelized DS3 signal carrying 28 DS1 signals. These DS1 signals are transmultiplexed into/from VT1.5 signals connected to a lower-order cross-connection unit of type LOXC40G2S/1 or LOXC40G3S/1. The bandwidth used to connect the 28 DS1 signals to the LOXC is a specific portion of the overall bandwidth of the TransMUX unit, namely the STS-1 #43. This bandwidth is not usable for transmultiplexing of user traffic cross-connected to the corresponding asynchronous port (tma43) when the test port is enabled.

The following diagram shows the connectivity of the test port:



The connection between the DS3 test access port and the lower-order cross-connection unit (LOXC) is fix once the test port is activated.

To make a DS1 signal available at the DS3 test access port, you need to create a lower order remote test access session where the primary and/or secondary test access tributary is one of those VT1.5 tributaries shown as “to be connected as test access tributary” in the diagram above.

DS3 test access port activation

The DS3 test access port can be activated (enabled) and deactivated (disabled) using the *WaveStar*® CIT or through TL1 commands.

When the DS3 test access port is enabled, then the internal connection between the STS-1 termination and the DS3 termination at the asynchronous side of the TransMUX unit is deleted. Instead the DS3 termination is connected to the DS3 test access port.

Activating or deactivating the DS3 test access port does not have any side effects on traffic running over the unaffected bandwidth, that is traffic running over the STS-1 equivalents #1 through #42 and #44 through #48.

The STS-1 termination which will be internally disconnected from the DS3 termination should *not* be involved in any cross-connection. Otherwise, activating the DS3 test access port will be rejected.

DS3 test access port and equipment protection

A TransMUX unit can be 1+1 equipment protected. However, the DS3 test access ports of the two equipment-protected TransMUX units are *not* protected.

> **Important!** *Only the DS3 test access port of the active unit provides full functionality and connectivity.*

Note furthermore that the behavior regarding the DS3 test access port of the TransMUX unit in the protection slot is characterized as follows:

- The DS3 test access port of the protection unit is *not* visible as a separate entity at the management interface. The consequence is that no alarms can be raised for this (invisible) port.
- Alarms related to the DS3 test access port of the protection unit, while the protection unit is active, are reported using the AID of the test port of the worker unit.

Configuration rules

Observe the following configuration rules:

- Always use the DS3 test access port of the *TransMUX unit in the worker slot* to connect a tester.
- Ensure that the TransMUX unit in the worker slot is the *active* unit, for example by applying a “Forced Switch to Worker”.

Note: If these rules are not followed then connectivity might be restricted. For example, when connecting a tester to the standby TransMUX unit, it is still possible to monitor all tapped signals because they are broadcasted from the LOXC towards both TransMUX units.

However, it is not possible to inject a signal from the tester, because the LOXC selects the incoming signal from the *active* TransMUX unit. This also applies when an equipment protection switch occurs while a remote test access session is active and a tester is connected to the TransMUX unit now becoming standby.

Glossary

Symbols

µ
Microns

µm
Micrometer

Numerics

0x1 Line Operation
0x1 means unprotected operation. The connection between network elements has one bidirectional line (no protection line).

1+1 Line Protection
A protection architecture in which the transmitting equipment transmits a valid signal on both the working and protection lines. The receiving equipment monitors both lines. Based on performance criteria and OS control, the receiving equipment chooses one line as the active line and designates the other as the standby line.

12NC (12-digit Numerical Code)
Used to uniquely identify an item or product. The first ten digits uniquely identify an item. The eleventh digit is used to specify the particular variant of an item. The twelfth digit is used for the revision issue. Items with the first eleven digits the same, are functionally equal and may be exchanged.

1xN Equipment Protection
1xN protection pertains to N number of circuit pack/port units protected by one circuit pack or port unit. When a protection switch occurs, the working signals are routed from the failed pack to the protection pack. When the fault clears, the signals revert to the working port unit.

A

ABN
Abnormal (condition)

ABS (Absent)
Used to indicate that a given circuit pack is not installed.

AC

Alternating Current

ACO (Alarm Cut-Off)

A button on the user panel used to silence audible alarms.

ACT (Active)

Used to indicate that a circuit pack or module is in-service and currently providing service functions.

Adaptive-rate tributary operation of a port (Pipe mode)

Mode of operation of a port in which tributaries are *not* explicitly provisioned for the expected signal rates. The signal rates are automatically identified.

ADM (Add/Drop Multiplexer)

The term for a synchronous network element capable of combining signals of different rates and having those signals added to or dropped from the stream.

AEL

Accessible Emission Limits

Agent

Performs operations on managed objects and issues events on behalf of these managed objects. All SDH managed objects will support at least an agent. Control of distant agents is possible via local “Managers”.

AGNE

Alarm Gateway Network Element

AID (Access Identifier)

A technical specification for explicitly naming entities (both physical and logical) of an NE using a grammar comprised of ASCII text, keywords, and grammar rules.

AIS (Alarm Indication Signal)

A code transmitted downstream in a digital network that indicates that an upstream failure has been detected and alarmed if the upstream alarm has not been suppressed.

AITS

Acknowledged Information Transfer Service: Confirmed mode of operation of the LAPD protocol.

Alarm

Visible or audible signal indicating that an equipment failure or significant event/condition has occurred.

Alarm Correlation

The search for a directly-reported alarm that can account for a given symptomatic condition.

Alarm Severity

An attribute defining the priority of the alarm message. The way alarms are processed depends on the severity.

Alarm Suppression

Selective removal of alarm messages from being forwarded to the GUI or to network management layer OSs.

Alarm Throttling

A feature that automatically or manually suppresses autonomous messages that are not priority alarms.

Aligning

Indicating the head of a virtual container by means of a pointer, for example, creating an Administrative Unit (AU) or a Tributary Unit (TU).

AMI (Alternate Mark Inversion)

A line code that employs a ternary signal to convert binary digits, in which successive binary ones are represented by signal elements that are normally of alternative positive and negative polarity but equal in amplitude and in which binary zeros are represented by signal elements that have zero amplitude.

Anomaly

A difference between the actual and desired operation of a function.

ANSI

American National Standards Institute

APD

Avalanche Photo Diode

Apparatus code (app. code , item code)

A unique string composed of letters and numbers to identify a piece of hardware.

APS (Automatic Protection Switch)

A protection switch that occurs automatically in response to an automatically detected fault condition.

ASCII (American Standard Code for Information Interchange)

A standard 7-bit code that represents letters, numbers, punctuation marks, and special characters in the interchange of data among computing and communications equipment.

ASN.1

Abstract Syntax Notation 1

ASON (Automatically Switched Optical Network)

Assembly

Gathering together of payload data with overhead and pointer information (an indication of the direction of the signal).

Association

A logical connection between manager and agent through which management information can be exchanged.

Asynchronous

The essential characteristic of time-scales or signals such that their corresponding significant instants do not necessarily occur at the same average rate.

Asynchronous side of a TransMUX card

The functions related to the DS3 side of the TransMUX card, i.e. the part handling the STS-1 carrying the channelized DS3 containing 28 DS1s.

Asynchronous Virtual Port (TMA port)

In order to be able to address the entities of the asynchronous side of a TransMUX card 48 virtual ports (TMA ports) are introduced.

ATM (Asynchronous Transfer Mode)

A high-speed transmission technology characterized by high bandwidth and low delay. It utilizes a packet switching and multiplexing technique which allocates bandwidth on demand.

Attribute

Alarm indication level: critical, major, minor, or no alarm.

AU (Administrative Unit)

Carrier for TUs.

AU PTR (Administrative Unit Pointer)

Indicates the phase alignment of the VC-N with respect to the STM-N frame. The pointer position is fixed with respect to the STM-N frame.

AUG

Administrative Unit Group

AUTO (Automatic)

One possible state of a port or slot. When a port is in the AUTO state and a good signal is detected, the port automatically enters the IS (in-service) state. When a slot is in the AUTO state and a circuit pack is detected, the slot automatically enters the EQ (equipped) state.

Autolock

Action taken by the system in the event of circuit pack failure/trouble. System switches to protection and prevents a return to the working circuit pack even if the trouble clears. Multiple protection switches on a circuit pack during a short period of time cause the system to autolock the pack.

Autonomous Message

A message transmitted from the controlled Network Element to the OMS which was not a response to an OMS originated command.

AVAIL

Available

B

Bandwidth

The difference in Hz between the highest and lowest frequencies in a transmission channel. The data rate that can be carried by a given communications circuit.

Baud Rate

Transmission rate of data (bits per second) on a network link.

BER (Bit Error Rate)

The ratio of error bits received to the total number of bits transmitted.

Bidirectional Line

A transmission path consisting of two fibers that handle traffic in both the transmit and receive directions.

Bidirectional Ring

A ring in which both directions of traffic between any two nodes travel through the same network elements (although in opposite directions).

Bidirectional Switch

Protection switching performed in both the transmit and receive directions.

BIP-N (Bit Interleaved Parity-N)

A method of error monitoring over a specified number of bits (BIP-3 or BIP-8).

Bit

The smallest unit of information in a computer, with a value of either 0 or 1.

Bit Error Rate Threshold

The point at which an alarm is issued for bit errors.

BLD OUT LG

Build-Out Lightguide

Break-out cable

Bundle of several, typically 12, rather thin optical fibers.

Bridge Cross-Connection

The setting up of a cross-connection leg with the same input tributary as that of an existing cross-connection leg. Thus, forming a 1:2 bridge from an input tributary to two output tributaries.

Broadband Communications

Voice, data, and/or video communications at greater than 2 Mbit/s rates.

Broadband Service Transport

STM-1 concatenation transport over the 1675 LambdaUnite MSS for ATM applications.

Byte

Refers to a group of eight consecutive binary digits.

C

C

Container

CC (Clear Channel)

A digital circuit where no framing or control bits are required, thus making the full bandwidth available for communications.

CC (Cross-Connection)

Path-level connections between input and output tributaries or specific ports within a single NE. Cross-connections are made in a consistent way even though there are various types of ports and various types of port protection. Cross-Connections are re-configurable interconnections between tributaries of transmission interfaces.

Cell Relay

Fixed-length cells. For example, ATM with 53 octets.

CEPT

Conférence Européenne des Administrations des Postes et des Télécommunications

Channel

A sub-unit of transmission capacity within a defined higher level of transmission capacity.

Circuit

A set of transmission channels through one or more network elements that provides transmission of signals between two points, to support a single communications path.

CIT or *WaveStar*® CIT (Craft Interface Terminal)

The user interface terminal used by craft personnel to communicate with a network element.

CL

Clear

CLEI

Common Language Equipment Identifier

Client

Computer in a computer network that generally offers a user interface to a server.

CLLI

Common Language Location Identifier

Closed Ring Network

A network formed of a ring-shaped configuration of network elements. Each network element connects to two others, one on each side.

CM (Configuration Management)

Subsystem that configures the network and processes messages from the network.

CMI

Coded Mark Inversion

CMIP

Common Management Information Protocol. OSI standard protocol for OAM&P information exchange.

CMISE

Common Management Information Service Element

CO (Central Office)

A building where common carriers terminate customer circuits.

Co-Resident

A hardware configuration where two applications can be active at the same time independently on the same hardware and software platform without interfering with each others functioning.

Collocated

System elements that are located in the same location.

Command Group

An administrator-defined group that defines commands to which a user has access.

Concatenation

A procedure whereby multiple virtual containers are associated one with each other resulting in a combined capacity that can be used as a single container across which bit sequence integrity is maintained.

Correlation

A process where related hard failure alarms are identified.

CP

Circuit Pack

CPE

Customer Premises Equipment

CPU

Central Processing Unit

CR (Critical (alarm))

Alarm that indicates a severe, service-affecting condition.

CRC

Cyclical Redundancy Check

Cross-connection Map

Connection map for an SDH Network Element; contains information about how signals are connected between high speed time slots and low speed tributaries.

Crosstalk

An unwanted signal introduced into one transmission line from another.

CSMA/CD

Carrier Sense Multiple Access with Collision Detection

CTIP

Customer Training and Information Products

Current Value

The value currently assigned to a provisionable parameter.

D

DACS/DCS

Digital Access Cross-Connect System

Data

A collection of system parameters and their associated values.

Database Administrator

A user who administers the database of the application.

dB

Decibels

DC

Direct Current

DCC (Data Communications Channel)

The embedded overhead communications channel in the synchronous line, used for end-to-end communications and maintenance. The DCC carries alarm, control, and status information between network elements in a synchronous network.

DCE (Data Communications Equipment)

The equipment that provides signal conversion and coding between the data terminating

equipment (DTE) and the line. The DCE may be separate equipment or an integral part of the DTE or of intermediate equipment. A DCE may perform other functions usually performed at the network end of the line.

DCF

Data Communications Function; Dispersion Compensation Fiber

DCM (Dispersion Compensation Module)

A device used to compensate the dispersion, the pulse spreading properties of an optical fiber. DCMs are necessary for very-long-haul applications and high bit rates.

DCN

Data Communications Network

Default

An operation or value that the system or application assumes, unless a user makes an explicit choice.

Default Provisioning

The parameter values that are pre-programmed as shipped from the factory.

Defect

A limited interruption of the ability of an item to perform a required function. It may or may not lead to maintenance action depending on the results of additional analysis.

Demultiplexing

A process applied to a multiplexed signal for recovering signals combined within it and for restoring the distinct individual channels of these signals.

DEMUX (Demultiplexer)

A device that splits a combined signal into individual signals at the receiver end of transmission.

Deprovisioning

The inverse order of provisioning. To manually remove/delete a parameter that has (or parameters that have) previously been provisioned.

Digital Link

A transmission span such as a point-to-point 2 Mbit/s, 34 Mbit/s, 140 Mbit/s, VC-12, VC-3 or VC-4 link between controlled network elements.

Digital Multiplexer

Equipment that combines by time-division multiplexing several digital signals into a single composite digital signal.

Digital Section

A transmission span such as an STM-N signal. A digital section may contain multiple digital channels.

Disassembly

Splitting up a signal into its constituents as payload data and overhead (an indication of the direction of a signal).

Dispersion

Time-broadening of a transmitted light pulse.

Dispersion Shifted Optical Fiber

1330/1550 nm minimum dispersion wavelength.

Divergence

When there is unequal amplification of incoming wavelengths, the result is a power divergence between wavelengths.

DNI (Dual Node Ring Interworking)

A topology in which two rings are interconnected at two nodes on each ring and operate so that inter-ring traffic is not lost in the event of a node or link failure at an interconnecting point.

Doping

The addition of impurities to a substance in order to attain desired properties.

Downstream

At or towards the destination of the considered transmission stream, for example, looking in the same direction of transmission.

DPLL

Digital Phase Locked Loop

DRAM

Dynamic Random Access Memory

Drop and Continue

A circuit configuration that provides redundant signal appearances at the outputs of two network elements in a ring. Can be used for Dual Node Ring Interworking (DNI) and for video distribution applications.

Drop-Down Menu

A menu that is displayed from a menu bar.

DS1

Digital Signal - Level 1 (1.544 Mbit/s)

DS3

Digital Signal - Level 3 (44.736 Mbit/s)

DS3 line

A DS3 line is, acc. to the ANSI T1.231 standard, a a physical transport vehicle that provides the means of moving digital information between two points at the nominal rate of 44.736 Mbit/s. A

DS3 line is characterized by bipolar 3-zero substitution (B3ZS) coding and transmission on a metallic medium. A DS3 line is normally an intra-office transport vehicle used to connect DS3 facilities and equipment.
Details of DS3 line and path formats can be found in the ANSI T1.102, T1.404, and T1.107 standards.

DS3 path

A DS3 path is, acc. to the ANSI T1.231 standard, a a framed digital signal between two points at a nominal rate of 44.736 Mbit/s. A DS3 path is independent of the physical transport system(s) over which it is carried. The basic DS3 frame format is called the M-Frame format, described in ANSI T1.107.
Details of DS3 line and path formats can be found in the ANSI T1.102, T1.404, and T1.107 standards.

DSNE (Directory Service Network Element)

A designated Network Element that is responsible for administering a database that maps Network Elements names (node names) to addresses (node Id). There can be one DSNE per (sub)network.

DTE (Data Terminating Equipment)

The equipment that originates data for transmission and accepts transmitted data.

DTMF

Dual Tone Multifrequency

DUR

Dual Unit Row (subrack)

DUS

Do not Use for Synchronization

DWDM (Dense Wavelength Division Multiplexing)

Transmitting two or more signals of different wavelengths simultaneously over a single fiber.

E

EBER (Excessive Bit Error Rate)

The calculated average bit error rate over a data stream.

ECC

Embedded Control Channel

EEPROM

Electrically Erasable and Programmable Read-Only Memory

EIA (Electronic Industries Association)

A trade association of the electronic industry that establishes electrical and functional standards.

EM (Event Management)

Subsystem of OMS that processes and logs event reports of the network.

EMC (Electromagnetic Compatibility)

A measure of equipment tolerance to external electromagnetic fields.

EMI (Electromagnetic Interference)

High-energy, electrically induced magnetic fields that cause data corruption in cables passing through the fields.

EMS

Element Management System

Entity

A specific piece of hardware (usually a circuit pack, slot, or module) that has been assigned a name recognized by the system.

Entity Identifier

The name used by the system to refer to a circuit pack, memory device, or communications link.

EPROM

Erasable Programmable Read-Only Memory

EQ (Equipped)

Status of a circuit pack or interface module that is in the system database and physically in the frame, but not yet provisioned.

ES (Errored Seconds)

A performance monitoring parameter. ES “type A” is a second with exactly one error; ES “type B” is a second with more than one and less than the number of errors in a severely errored second for the given signal. ES by itself means the sum of the type A and type B ESs.

ESD

Electrostatic Discharge

ESP

Electrostatic Protection

Establish

A user initiated command, at the *WaveStar*® CIT, to create an entity and its associated attributes in the absence of certain hardware.

ETSI

European Telecommunications Standards Institute

Event

A significant change. Events in controlled Network Elements include signal failures, equipment failures, signals exceeding thresholds, and protection switch activity. When an event occurs in a controlled Network Element, the controlled Network Element will generate an alarm or status message and send it to the management system.

Event Driven

A required characteristic of network element software system: NEs are reactive systems, primarily viewed as systems that wait for and then handle events. Events are provided by the external interface packages, the hardware resource packages, and also by the software itself.

Externally Timed

An operating condition of a clock in which it is locked to an external reference and is using time constants that are altered to quickly bring the local oscillator's frequency into approximate agreement with the synchronization reference frequency.

Extra traffic

Unprotected traffic that is carried over protection channels when their capacity is not used for the protection of working traffic.

F

Fault

Term used when a circuit pack has a hard (not temporary) fault and cannot perform its normal function.

Fault Management

Collecting, processing, and forwarding of autonomous messages from network elements.

FCC

Federal Communications Commission

FDA/CDRH

The Food and Drug Administration`s Center for Devices and Radiological Health.

FDDI (Fiber Distributed Data Interface)

Fiber interface that connects computers and distributes data among them.

FE (Far End)

Any other network element in a maintenance subnetwork other than the one the user is at or working on. Also called remote.

FEBE (Far-End Block Error)

An indication returned to the transmitting node that an errored block has been detected at the receiving node. A block is a specified grouping of bits.

FEC (Forward Error Correction)

An error correction technique in which redundant bits are added to the payload signal enabling the receiving station to detect and correct bit errors that unavoidably occur when an optical line signal is transmitted over longer distances over an optical fiber. FEC is used to increase the transmission span length.

FEPROM (Flash EPROM)

A technology that combines the non-volatility of EPROM with the in-circuit re-programmability of EEPROM.

FERF (Far-End Receive Failure)

An indication returned to a transmitting Network Element that the receiving Network Element has detected an incoming section failure. Also known as RDI.

FIT (Failures in Time)

Circuit pack failure rates per 10^9 hours as calculated using the method described in Reliability Prediction Procedure for Electronic Equipment, BellCore Method I, Issue 6, December 1997.

Fixed-rate tributary operation of a port

Mode of operation of a port in which tributaries are provisioned for the expected signal rates. This provisioning information is used for cross-connection rate validation and for alarm handling (for example “Loss of Pointer”).

Folded Rings

Folded (collapsed) rings are rings without fiber diversity. The terminology derives from the image of folding a ring into a linear segment.

Forced

Term used when a circuit pack (either working or protection) has been locked into a service-providing state by user command.

FR (Frame Relay)

A form of packet switching that relies on high-quality phone lines to minimize errors. It is very good at handling high-speed, bursty data over wide area networks. The frames are variable lengths and error checking is done at the end points.

Frame

The smallest block of digital data being transmitted.

Framework

An assembly of equipment units capable of housing shelves, such as a bay framework.

Free Running

An operating condition of a clock in which its local oscillator is not locked to an internal synchronization reference and is using no storage techniques to sustain its accuracy.

G

GARP

Generic Attribute Registration Protocol

GB

Gigabytes

Gbit/s

Gigabits per second

GHz

Gigahertz

Global Wait to Restore Time

Corresponds to the time to wait before switching back to the timing reference. It occurs after a timing link failure has cleared. This time applies for all timing sources in a system hence the name global. This can be between 0 and 60 minutes, in increments of one minute.

GMPLS

Generalized Multi Protocol Label Switching

GNE (Gateway Network Element)

A network element that passes information between other network elements and management systems through a data communication network.

Grooming

In telecommunications, the process of separating and segregating channels, as by combing, such that the broadest channel possible can be assembled and sent across the longest practical link. The aim is to minimize de-multiplexing traffic and reshuffling it electrically.

GVRP

Generic VLAN Registration Protocol

H

Hard Failure

An unrecoverable non-symptomatic (primary) failure that causes signal impairment or interferes with critical network functions, such as DCC operation.

HDB3 (High Density Bipolar 3 Code)

Line code for 2 Mbit/s transmission systems.

HDLC (High Level Data Link Control)

OSI reference model datalink layer protocol.

HMI

Human Machine Interface

HML (Human Machine Language)

A standard language developed by the ITU for describing the interaction between humans and dumb terminals.

HO

Higher Order

Holdover

An operating condition of a clock in which its local oscillator is not locked to an external reference but is using storage techniques to maintain its accuracy with respect to the last known frequency comparison with a synchronization reference.

Hot Standby

A circuit pack ready for fast, automatic placement into operation to replace an active circuit pack. It has the same signal as the service going through it, so that choice is all that is required.

HPA (Higher Order Path Adaptation)

Function that adapts a lower-order Virtual Container to a higher-order Virtual Container by processing the Tributary Unit pointer which indicates the phase of the lower-order Virtual Container Path Overhead relative to the higher-order Virtual Container Path Overhead and assembling/disassembling the complete higher-order Virtual Container.

HPC (Higher Order Path Connection)

Function that provides for flexible assignment of higher-order Virtual Containers within an STM-N signal.

HPT (Higher Order Path Termination)

Function that terminates a higher-order path by generating and adding the appropriate Virtual Container Path Overhead to the relevant container at the path source and removing the Virtual Container Path Overhead and reading it at the path sink.

HS

High Speed

HW

Hardware

Hz

Hertz

I

I-NNI

Internal Network Node Interface

I/O

Input/Output

IAO LAN

Intraoffice Local Area Network

ID

Identifier

IEC

International Electro-Technical Commission

IEEE

Institute of Electrical and Electronics Engineers

IETF

Internet Engineering Task Force

IMF

Infant Mortality Factor

Insert

To physically insert a circuit pack into a slot, thus causing a system initiated restore of an entity into service and/or creation of an entity and associated attributes.

Interface Capacity

The total number of STM-1 equivalents (bidirectional) tributaries in all transmission interfaces with which a given transmission interface shelf can be equipped at one time. The interface capacity varies with equipage.

IS (Intermediate System)

A system which routes/relays management information. A Network Element may be a combined intermediate and end system.

IS (In-Service)

A memory administrative state for ports. IS refers to a port that is fully monitored and alarmed.

IS-IS Routing

The Network Elements in a management network route packets (data) between each other, using an *IS-IS level protocol*. The size of a network running IS-IS Level 1 is limited, and therefore certain mechanisms are employed to facilitate the management of larger networks.
For STATIC ROUTING, the capability exists for disabling the protocol over the LAN connections, effectively causing the management network to be partitioned into separate IS-IS Level 1 *areas*. In order for the network management system to communicate with a specific Network Element in one of these areas, the network management system must identify through which so-called *Gateway Network Element* this specific Network Element is connected to the LAN. All packets to this specific Network Element are routed directly to the Gateway Network Element by the network management system, before being re-routed (if necessary) within the Level 1 area.
For DYNAMIC ROUTING an IS-IS Level 2 routing protocol is used allowing a number of Level 1 areas to *interwork*. The Network Elements which connect an IS-IS area to another area are set to run the IS-IS Level 2 protocol within the Network Element and on the connection between other Network Elements. Packets can now be routed between IS-IS areas and the network management system does not have to identify the Gateway Network Elements.

ISDN

Integrated Services Digital Network

ITM

Integrated Transport Management

ITM-NM

Integrated Transport Management Network Module

ITU

International Telecommunications Union

ITU-T

International Telecommunications Union — Telecommunication standardization sector. Formerly known as CCITT: Comité Consultatif International Télégraphique & Téléphonique; International Telegraph and Telephone Consultative Committee.

J

Jitter

Short term variations of amplitude and frequency components of a digital signal from their ideal position in time.

K

kbit/s

Kilobits per second

L

LAN (Local Area Network)

A communications network that covers a limited geographic area, is privately owned and user administered, is mostly used for internal transfer of information within a business, is normally contained within a single building or adjacent group of buildings, and transmits data at a very rapid speed.

LAPD (Link Access Procedure D-bytes)

Protocol used on Data Link Layer (OSI layer two) according to ITU-T Q.921.

LBC

Laser Bias Current

LBFC

Laser Backface Currents

LBO (Lightguide Build-Out)

An attenuating (signal-reducing) element used to keep an optical output signal strength within desired limits.

LCN

Local Communications Network

LCS

Local Customer Support

LED

Light-Emitting Diode

LH

Long Haul

Line

A transmission medium, together with the associated equipment, required to provide the means of transporting information between two consecutive network elements. One network element originates the line signal; the other terminates it.

Line Protection

The optical interfaces can be protected by line protection. Line protection switching protects against failures of line facilities, including the interfaces at both ends of a line, the optical fibers, and any equipment between the two ends. Line protection includes protection of equipment failures.

Line Timing

Refers to a network element that derives its timing from an incoming STM-N signal.

Link

The mapping between in-ports and out-ports. It specifies how components are connected to one another.

LMP

Link Management Protocol

LO

Lower Order

Location

An identifier for a specific circuit pack, interface module, interface port, or communications link.

Lockout of Protection

The *WaveStar*® CIT command that prevents the system from switching traffic to the protection line from a working line. If the protection line is active when a "Lockout of Protection" is entered – this command causes the working line to be selected. The protection line is then locked from any Automatic, Manual, or Forced protection switches.

Lockout State

The Lockout State shall be defined for each working or protection circuit pack.

The permitted states are:

- None – meaning no lockout is set for the circuit pack
- Set – meaning the circuit pack has been locked out

The values (None, Set) shall be taken independently for each working or protection circuit pack.

LOF (Loss of Frame)

A failure to synchronize an incoming signal.

LOM

Loss Of Multiframe

Loop Timing

A special case of line timing. It applies to network elements that have only one OC-N/STM-N interface. For example, terminating nodes in a linear network are loop timed.

Loopback

Type of diagnostic test used to compare an original transmitted signal with the resulting received signal. A loopback is established when the received optical or electrical external transmission signal is sent from a port or tributary input directly back toward the output.

LOP (Loss of Pointer)

A failure to extract good data from a signal payload.

LOS (Loss of Signal)

The complete absence of an incoming signal.

Loss Budget

Loss (in dB) of optical power due to the span transmission medium (includes fiber loss and splice losses).

LOXC (Lower-order cross-connection unit)

Optional circuit pack for cross-connections on lower-order signal levels: VT1.5, VC-12 and VC-3 (lower order).

LPA (Lower-order Path Adaptation)

Function that adapts a PDH signal to a synchronous network by mapping the signal into or de-mapping the signal out of a synchronous container.

LPC (Lower Order Path Connection)

Function that provides for flexible assignment of lower-order VCs in a higher-order VC.

LPT (Lower Order Path Termination)

Function that terminates a lower-order path by generating and adding the appropriate VC POH to the relevant container at the path source and removing the VC POH and reading it at the path sink.

LS

Low Speed

LTE

Line Terminating Equipment

M

MAF

Management Application Function

Maintenance Condition

An equipment state in which some normal service functions are suspended, either because of a problem or to perform special functions (copy memory) that can not be performed while normal service is being provided.

Management Connection

Identifies the type of routing used (STATIC or DYNAMIC), and if STATIC is selected allows the gateway network element to be identified.

Manager

Capable of issuing network management operations and receiving events. The manager communicates with the agent in the controlled network element.

Manual Switch State

A protection group shall enter the Manual Switch State upon the initiation and successful completion of the Manual Switch command. The protection group leaves the Manual Switch state by means of the Clear or Forced Switch commands. While in the Manual Switch state the system may switch the active unit automatically if required for protection switching.

Mapping

The logical association of one set of values, such as addresses on one network, with quantities or values of another set, such as devices or addresses on another network.

MB

Megabytes

Mbit/s

Megabits per second

MCF (Message Communications Function)

Function that provides facilities for the transport and routing of Telecommunications Management Network messages to and from the Network Manager.

MD (Mediation Device)

Allows for exchange of management information between Operations System and Network Elements.

MDI

Miscellaneous Discrete Input

MDO

Miscellaneous Discrete Output

MEC (Manufacturer Executable Code)

Network Element system software in binary format that after being downloaded to one of the stores can be executed by the system controller of the network element.

MEM

Memory

Mid-Span Meet

The capability to interface between two lightwave network elements of different vendors. This applies to high-speed optical interfaces.

MIPS

Millions of Instructions Per Second

Miscellaneous Discrete Interface

Allows an operations system to control and monitor equipment collocated within a set of input and output contact closures.

MJ (Major (alarm))

Indicates a service-affecting failure, main or unit controller failure, or power supply failure.

MMF

Multi-Mode Fiber

MMI

Man-Machine Interface

MML

Human-Machine Language

MN (Minor (alarm))

Indicates a non-service-affecting failure of equipment or facility.

MO

Managed Object

MPLS

Multi Protocol Label Switching

MS

Multiplexer Section

ms

Millisecond

MS-SPRING (Multiplexer Section Shared Protection Ring)

A protection method used in Add-Drop Multiplexer Network Elements.

MSA

Multisource Agreement

MSOH (Multiplexer Section OverHead)

Part of the Section Overhead. Is accessible only at line terminals and multiplexers.

MSP (Multiplexer Section Protection)
Provides capability for switching a signal from a working to a protection section.

MST (Multiplexer Section Termination)
Function that generates the Multiplexer Section OverHead in the transmit direction and terminates the part of the Multiplexer Section overhead that is acceptable in the receive direction.

MTBF
Mean Time Between Failures

MTBMA
Mean Time Between Maintenance Activities

MTIE
Maximum Time Interval Error

MTPI
Multiplexer Timing Physical Interface

MTS (Multiplexer Timing Source)
Function that provides timing reference to the relevant component parts of the multiplex equipment and represents the SDH Network Element clock.

MTTR
Mean Time To Repair

Multiplexer
A device (circuit pack) that combines two or more transmission signals into a combined signal on a shared medium.

Multiplexing
A procedure by which multiple lower-order path layer signals are adapted into a higher-order path, or the multiple higher-order path layer signals are adapted into a multiplex section.

N

NA
Not Applicable

***Navis*® Optical NMS**
Optical Network Management System

NE (Network Element)
A node in a telecommunication network that supports network transport services and is directly manageable by a management system.

NEBS
Network Equipment-Building System

nm

Nanometer (10^{-9} meters)

NMON (Not Monitored)

A provisioning state for equipment that is not monitored or alarmed.

No Request State

This is the routine-operation quiet state in which no external command activities are occurring.

Node

A network element in a ring or, more generally, in any type of network. In a network element supporting interfaces to more than one ring, node refers to an interface that is in a particular ring. Node is also defined as all equipment that is controlled by one system controller. A node is not always directly manageable by a management system.

Non-Revertive Switching

In non-revertive switching, an active and stand-by line exist on the network. When a protection switch occurs, the standby line is selected to support traffic, thereby becoming the active line. The original active line then becomes the stand-by line. This status remains in effect even when the fault clears. That is, there is no automatic switch back to the original status.

Non-Synchronous

The essential characteristic of time-scales or signals such that their corresponding significant instants do not necessarily occur at the same average rate.

NORM

Normal

NPI

Null Pointer Indication

NPPA (Non-Preemptible Protection Access)

Non-preemptible protection access increases the available span capacity for traffic which does not require protection by a ring, but which cannot be preempted.

NRZ

Nonreturn to Zero

NSA

Non-Service Affecting

NSAP Address (Network Service Access Point Address)

Network Service Access Point Address (used in the OSI network layer 3). An automatically assigned number that uniquely identifies a Network Element for the purposes of routing DCC messages.

NTP

Network Time Protocol

NVM (Non-Volatile Memory)

Memory that retains its stored data after power has been removed. An example of NVM would be a hard disk.

O

O&M

Operation and Maintenance

OA

Optical Amplifier

OAM&P

Operations, Administration, Maintenance, and Provisioning

OC-12

Optical Carrier, Level 12 Signal (622.08 Mbit/s)

OC-192

Optical Carrier, Level 192 (9953.28 Mbit/s) (10 Gbit/s)

OC-3

Optical Carrier, Level 3 Signal (155 Mbit/s)

OC-48

Optical Carrier, Level 48 (2488.32 Mbit/s) (2.5 Gbit/s)

OC-768

Optical Carrier, Level 768 (39813.12 Mbit/s) (40 Gbit/s)

OC, OC-n

Optical Carrier

OI (Operations Interworking)

The capability to access, operate, provision, and administer remote systems through craft interface access from any site in an SDH network or from a centralized operations system.

OIF

Optical Internetworking Forum

OLS

Optical Line System

OOF

Out-of-Frame

OOS (Out-of-Service)

The circuit pack is not providing its normal service function (removed from either the working or protection state) either because of a system problem or because the pack has been removed from service.

Open Ring Network

A network formed of a linear chain-shaped configuration of network elements. Each network element connects to two others, one on each side, except for two network elements at the ends which are connected on only one side. A closed ring can be formed by adding a connection between the two end nodes.

Operations Interface

Any interface providing you with information on the system behavior or control. These include the equipment LEDs, user panel,*WaveStar*® CIT, office alarms, and all telemetry interfaces.

Operator

A user of the system with operator-level user privileges.

Optical Channel

A STM-N wavelength within an optical line signal. Multiple channels, differing by 1.5 µm in wavelength, are multiplexed into one signal.

Optical Line Signal

A multiplexed optical signal containing multiple wavelengths or channels.

Original Value Provisioning

Preprogramming of a system's original values at the factory. These values can be overridden using local or remote provisioning.

OS (Operations System)

A central computer-based system used to provide operations, administration, and maintenance functions.

OSF

Open Software Foundation; Operations System Function

OSI (Open Systems Interconnection)

Referring to the OSI reference model, a logical structure for network operations standardized by the International Standards Organization (ISO).

Outage

A disruption of service that lasts for more than 1 second.

OW (Orderwire)

A dedicated voice-grade line for communications between maintenance and repair personnel.

P

Parameter

A variable that is given a value for a specified application. A constant, variable, or expression that is used to pass values between components.

Parity Check

Tests whether the number of ones (or zeros) in an array of binary bits is odd or even; used to determine that the received signal is the same as the transmitted signal.

Pass-Through

Paths that are cross-connected directly across an intermediate node in a network.

Path

A logical connection between the point at which a standard frame format for the signal at the given rate is assembled, and the point at which the standard frame format for the signal is disassembled.

Path Terminating Equipment

Network elements in which the path overhead is terminated.

PCB

Printed Circuit Board

PCM

Pulse Code Modulation

PDH

Plesiochronous Digital Hierarchy

PDU (Protocol Data Unit)

A packet of information that is delivered as a unit between peer entities of a network and that may contain control information.

PI

Physical Interface

Pipe mode (Adaptive-rate tributary operation of a port)

Mode of operation of a port in which tributaries are *not* explicitly provisioned for the expected signal rates. The signal rates are automatically identified.

Platform

A family of equipment and software configurations designed to support a particular application.

Plesiochronous Network

A network that contains multiple subnetworks, each internally synchronous and all operating at the same nominal frequency, but whose timing may be slightly different at any particular instant.

PM (Performance Monitoring)

Measures the quality of service and identifies degrading or marginally operating systems (before an alarm would be generated).

PMD (Polarization Mode Dispersion)

Output pulse broadening due to random coupling of the two polarization modes in an optical fiber.

POH (Path Overhead)

Informational bytes assigned to, and transported with the payload until the payload is de-multiplexed. It provides for integrity of communication between the point of assembly of a virtual container and its point of disassembly.

Pointer

An indicator whose value defines the frame offset of a virtual container with respect to the frame reference of the transport entity on which it is supported.

POP

Point of Presence

Port (also called Line)

The physical interface, consisting of both an input and output, where an electrical or optical transmission interface is connected to the system and may be used to carry traffic between network elements. The words “port” and “line” may often be used synonymously. “Port” emphasizes the physical interface, and “line” emphasizes the interconnection. Either may be used to identify the signal being carried.

Port State Provisioning

A feature that allows a user to suppress alarm reporting and performance monitoring during provisioning by supporting multiple states (automatic, in-service, and not monitored) for low-speed ports.

POTS

Plain Old Telephone Service

PP

Pointer Processing

PRC (Primary Reference Clock)

The main timing clock reference in SDH equipment.

Preprovisioning

The process by which the user specifies parameter values for an entity in advance of some of the equipment being present. These parameters are maintained only in NVM. These modifications are initiated locally or remotely by either *WaveStar*® CIT or *Navis*® OMS. Preprovisioning provides for the decoupling of manual intervention tasks (for example, install circuit packs) from those tasks associated with configuring the node to provide services (for example, specifying the entities to be cross-connected).

PRI

Primary

Proactive Maintenance

Refers to the process of detecting degrading conditions not severe enough to initiate protection switching or alarming, but indicative of an impending signal fail or signal degrade defect.

Protection Access

To provision traffic to be carried by protection tributaries when the port tributaries are not being used to carry the protected working traffic.

Protection Group Configuration

The members of a group and their roles, for example, working protection, line number, etc.

Protection Path

One of two signals entering a path selector used for path protection switching or dual ring interworking. The other is the working path. The designations working and protection are provisioned by the user, whereas the terms active path and standby path indicate the current protection state.

Protection State

When the working unit is currently considered active by the system and that it is carrying traffic. The "active unit state" specifically refers to the receive direction of operation — since protection switching is unidirectional.

PROTN (Protection)

Extra capacity (channels, circuit packs) in transmission equipment that is not intended to be used for service, but rather to serve as backup against equipment failures.

PROV (Provisioned)

Indicating that a circuit pack is ready to perform its intended function. A provisioned circuit pack can be active (ACT), in-service (IS), standby (STBY), provisioned out-of-service (POS), or out-of-service (OOS).

PSDN

Public Switched Data Network

PSTN

Public Switched Telephone Network

PTE

Path Terminating Equipment

PTR

Pointer

PWR

Power

PWR ON

Power On

Q

Q-LAN

Thin Ethernet LAN which connects the manager to Gateway Network Elements so that management information between Network Elements and management systems can be exchanged.

QL (Quality Level)

The quality of the timing signal(s) provided to synchronize a Network Element. In case of optical line timing the level can be provided by the Synchronization Status Message (S-1 byte). If the System and Output Timing Quality Level mode is “Enabled”, and if the signal selected for the Station Clock Output has a quality level below the Acceptance Quality Level, the Network Element “squelches” the Station Clock Output Signal, which means that no signal is forwarded at all.

QOS

Quality of Service

R

RAM

Random Access Memory

RDI (Remote Defect Indication)

An indication returned to a transmitting terminal that the receiving terminal has detected an incoming section failure. [Previously called far-end-receive failure (FERF).]

Reactive Maintenance

Refers to detecting defects/failures and clearing them.

Receive-Direction

The direction towards the Network Element.

Regeneration

The process of reconstructing a digital signal to eliminate the effects of noise and distortion.

Regenerator Loop

Loop in a Network Element between the Station Clock Output(s) and one or both Station Clock Inputs, which can be used to de-jitterize the selected timing reference in network applications.

Regenerator Section Termination (RST)

Function that generates the Regenerator Section Overhead (RSOH) in the transmit direction and terminates the RSOH in the receive direction.

Reliability

The ability of a software system performing its required functions under stated conditions for a stated period of time. The probability for an equipment to fulfill its function. Some of the ways in which reliability is measured are: MTBF (Mean Time Between Failures) expressed in hours; Availability = (MTBF)/(MTBF+MTTR)(%) [where MTTR = mean time to restore]; outage in minutes per year; failures per hour; percentage of failures per 1,000 hours.

Remote Network Element

Any Network Element that is connected to the referenced Network Element through either an electrical or optical link. It may be the adjacent node on a ring, or N nodes away from the reference. It also may be at the same physical location but is usually at another (remote) site.

Restore Timer

Counts down the time (in minutes) during which the switch waits to let the worker line recover before switching back to it. This option can be set to prevent the protection switch continually switching if a line has a continual transient fault.

Revertive

A protection switching mode in which, after a protection switch occurs, the equipment returns to the nominal configuration (that is, the working equipment is active, and the protection equipment is standby) after any failure conditions that caused a protection switch to occur, clear, or after any external switch commands are reset. (See “Non-Revertive”.)

Revertive Switching

In revertive switching, there is a working and protection high-speed line, circuit pack, etc. When a protection switch occurs, the protection line, circuit pack, etc. is selected. When the fault clears, service “reverts” to the working line.

Ring

A configuration of nodes comprised of network elements connected in a circular fashion. Under normal conditions, each node is interconnected with its neighbor and includes capacity for transmission in either direction between adjacent nodes. Path switched rings use a head-end bridge and tail-end switch. Line switched rings actively reroute traffic over the protection capacity.

Route

A series of contiguous digital sections.

Router

An interface between two networks. While routers are like bridges, they work differently. Routers provide more functionality than bridges. For example, they can find the best route between any two networks, even if there are several different networks in between. Routers also provide network management capabilities such as load balancing, partitioning of the network, and trouble-shooting.

RSOH

Regenerator Section OverHead; part of SOH

RST

Regenerator Section Termination

RT

Remote Terminal

RTRV

Retrieve

RZ (Return to Zero)

A code form having two information states (termed zero and one) and having a third state or an at-rest condition to which the signal returns during each period.

S

SA

Service Affecting

SA

Section Adaptation

SD

Signal Degrade

SDH (Synchronous Digital Hierarchy)

A hierarchical set of digital transport structures, standardized for the transport of suitable adapted payloads over transmission networks.

SDS

Standard Directory Service based on ANSI recommendation T1.245

SEC

Secondary

SEC

SDH Equipment Clock

Section

The portion of a transmission facility, including terminating points, between a terminal network element and a line-terminating network element, or two line-terminating network elements.

Section Adaptation

Function that processes the AU-pointer to indicate the phase of the VC-3/4 POH relative to the STM-N SOH and assembles/disassembles the complete STM-N frame.

Self-Healing

A network's ability to automatically recover from the failure of one or more of its components.

SEMF (Synchronous Equipment Management Function)

Function that converts performance data and implementation specific hardware alarms into object-oriented messages for transmission over the DCC and/or Q-interface. It also converts object-oriented messages related to other management functions for passing across the S reference points.

Server

Computer in a computer network that performs dedicated main tasks which generally require sufficient performance.

Service

The operational mode of a physical entity that indicates that the entity is providing service. This designation will change with each switch action.

SES (Severely Errored Seconds)

This performance monitoring parameter is a second in which a signal failure occurs, or more than a preset amount of coding violations (dependent on the type of signal) occurs.

SFF (Small Form Factor)

Fiber-optical connector, designed to be both small and low-cost.

SFP (Small Form Factor Pluggable)

A new generation of optical modular transceivers, designed for use with small form factor (SFF) connectors, offering high speed and physical compactness. They are hot-swappable.

SH

Short Haul

Single-Ended Operations

Provides operations support from a single location to remote Network Elements in the same SDH subnetwork. With this capability you can perform operations, administration, maintenance, and provisioning on a centralized basis. The remote Network Elements can be those that are specified for the current release.

Site Address

The unique address for a Network Element.

Slot

A physical position in a shelf designed for holding a circuit pack and connecting it to the backplane. This term is also used loosely to refer to the collection of ports or tributaries connected to a physical circuit pack placed in a slot.

SM or SMF (Single-Mode Fiber)

A low-loss, long-span optical fiber typically operating at either 1310 nm, 1550 nm, or both.

SMN

SDH Management Network

SNC/I

SubNetwork Connection (protection) / Inherent monitoring

SNC/N

SubNetwork Connection (protection) / Non-Intrusive Monitoring

SNR (Signal-to-Noise Ratio)

The relative strength of signal compared to noise.

Software Backup

The process of saving an image of the current network element's databases, which are contained in its NVM, to a remote location. The remote location could be the*WaveStar*® CIT or OMS.

Software Download

The process of transferring a generic (full or partial) or provisioned database from a remote entity to the target network element's memory. The remote entity may be the*WaveStar*® CIT or OMS. The download procedure uses bulk transfer to move an un-interpreted binary file into the network element.

Software ID

Number that provides the software version information for the system.

SOH (Section Overhead)

Capacity added to either an AU-4 or assembly of AU-3s to create an STM-1. Contains always STM-1 framing and optionally maintenance and operational functions. SOH can be subdivided in MSOH (multiplex section overhead) and RSOH (regenerator section overhead).

SONET (Synchronous Optical Network)

The North American standard for the rates and formats that defines optical signals and their constituents.

Span

An uninterrupted bidirectional fiber section between two network elements.

Span Growth

A type of growth in which one wavelength is added to all lines before the next wavelength is added.

SPE

Synchronous Payload Envelope

SPF (Single point of failure)

A single failure in the OSI-network (DCC, LAN or node), that causes isolation of more than one node in the OSI-network. The use of IS-IS areas, without obeying all rules & guidelines, increases the risk of a single point of failure in the network.

SPI

SDH Physical Interface

Squelch Map

This map contains information for each cross-connection in a ring and indicates the source and destination nodes for the low-speed circuit that is part of the cross-connection. This information is used to prevent traffic misconnection in rings with isolated nodes or segments.

SSM

Synchronization Status Marker

SSU_L

Synchronization Supply Unit — Local

SSU_T

Synchronization Supply Unit — Transit

Standby Path

One of two signals entering a constituent path selector, the standby path is the path not currently being selected.

State

The state of a circuit pack indicates whether it is defective or normal (ready for normal use).

Station Clock Input

An external clock may be connected to a Station Clock Input.

Status

The indication of a short-term change in the system.

STBY (Standby)

The circuit pack is in service but is not providing service functions. It is ready to be used to replace a similar circuit pack either by protection or by duplex switching.

STM

Synchronous Transport Module (SDH)

STM-N (Synchronous Transport Module, Level N)

A building block information structure that supports SDH section layer connections, where N represents a multiple of 155.52 Mbit/s. Normally N = 1, 4, 16, 64 or 256.

Stratum (Synchronization quality level)

Stratum is a measure for synchronization quality. Opposed to jitter or delay, Stratum is a more static measure. Basically (and from the perspective from a client) it is the number of servers to a reference clock. So a reference clock itself appears at Stratum 0, while the closest servers are at Stratum 1. On the network there is no valid NTP message with Stratum 0. A server synchronized to a Stratum n server will be running at Stratum n + 1. The upper limit for Stratum is 15. The purpose of Stratum is to avoid synchronization loops by preferring servers with a lower Stratum.

Stream (Line; aggregate)

A synchronous high rate connection between multiplexers, typically 10 or 40 Gbit/s.

STS

Synchronous Transport Signal (SONET)

Subnetwork

A group of interconnected/interrelated Network Elements. The most common connotation is a synchronous network in which the Network Elements have data communications channel (DCC) connectivity.

Supervisor

A user of the application with supervisor user privileges.

Suppression

A process where service-affecting alarms that have been identified as an “effect” are not displayed to a user.

SYNC

Synchronizer

Synchronization Messaging

Synchronization messaging is used to communicate the quality of network timing, internal timing status, and timing states throughout a subnetwork.

Synchronous

The essential characteristic of time scales or signals such that their corresponding significant instances occur at precisely the same average rate, generally traceable to a single Stratum 1 source.

Synchronous Network

The synchronization of transmission systems with synchronous payloads to a master (network) clock that can be traced to a reference clock.

Synchronous Payload

Payloads that can be derived from a network transmission signal by removing integral numbers of bits from every frame. Therefore, no variable bit-stuffing rate adjustments are required to fit the payload in the transmission signal.

Synchronous side of a TransMUX card

The functions related to the VT1.5 side of the TransMUX card, i.e. the part handling the STS-1 carrying 28 VT1.5s containing a DS1 each.

Synchronous Virtual Port (TMS port)

In order to be able to address the entities of the synchronous side of a TransMUX card 48 virtual ports (TMS ports) are defined.

System Administrator

A user of the computer system on which the system’s OS software application can be installed.

T

TARP

Target Identifiers Address Resolution Protocol

TBD

To Be Determined

TCA (Threshold-Crossing Alert)

A message type sent from a Network Element that indicates that a certain performance monitoring parameter has exceeded a specified threshold.

TDM (Time Division Multiplexing)

A technique for transmitting a number of separate data, voice, and/or video signals simultaneously over one communications medium by interleaving a portion of each signal one after another.

Through (or Continue) Cross-Connection

A cross-connection within a ring, where the input and output tributaries have the same tributary number but are in lines opposite each other.

Through Timing

Refers to a network element that derives its transmit timing in the east direction from a received line signal in the east direction and its transmit timing in the west direction from a received line signal in the west direction.

THz

Terahertz (10^{12} Hz)

TID (Target Identifier)

A provisionable parameter that is used to identify a particular Network Element within a network. It is a character string of up 20 characters where the characters are letters, digits, or hyphens (-).

TL1 (Transaction Language One)

A subset of ITU's human-machine language.

TM (Terminal Multiplexer)

An Add/Drop Multiplexer with only one stream interface.

TMA port (Asynchronous Virtual Port)

In order to be able to address the entities of the asynchronous side of a TMUX card 48 virtual ports (TMA ports) are defined.

TMS port (Synchronous Virtual Port)

In order to be able to address the entities of the synchronous side of a TMUX card 48 virtual ports (TMS ports) are defined.

Transmit-Direction

The direction outwards from the Network Element.

TransMUX (Transmultiplexing)

The process of remapping channelized DS3 signals carrying DS1s into VT1.5 signals and vice versa.

Tributary

A signal of a specific rate (e.g. 2 Mbit/s, 34 Mbit/s, 140 Mbit/s, VC-12, VC-3, VC-4, STM-1, or STM-4) that may be added to or dropped from a line signal.

Tributary

A path-level unit of bandwidth within a port, or the constituent signal(s) being carried in this unit of bandwidth, for example, an STM-1 tributary within an STM-N port.

Tributary Unit Pointer

Indicates the phase alignment of the VC with respect to the TU in which it resides. The pointer position is fixed with respect to the TU frame.

True Wave™ Optical Fiber

Alcatel-Lucent's fiber generally called non-zero dispersion-shift fiber, with a controlled amount of chromatic dispersion designed for amplified systems in the 1550/1310 nm range.

TSA (Time Slot Assignment)

A capability that allows any tributary in a ring to be cross-connected to any tributary in any lower-rate, non-ring interface or to the same-numbered tributary in the opposite side of the ring.

TSI (Time Slot Interchange)

The ability of the user to assign cross-connections between any tributaries of any lines within a Network Element. Three types of TSI can be defined: Hairpin TSI, Interring TSI (between rings), and intra-ring TSI (within rings).

TSO

Technical Support Organization

TSS

Technical Support Service within Alcatel-Lucent

TTP

Trail Termination Point

TU (Tributary Unit)

An information structure which provides adaptation between the lower-order path layer and the higher path layer. Consists of a VC-n plus a tributary unit pointer (TU PTR).

TUG

Tributary Unit Group

Two-Way Point-to-Point Cross-Connection

A two-legged interconnection, that supports two-way transmission, between two and only two tributaries.

Two-Way Roll

The operation which moves a two-way cross-connection between tributary i and tributary j to a two-way cross-connection between the same tributary i and a new tributary k with a single user command.

U

UAS (Unavailable Seconds)

In performance monitoring, the count of seconds in which a signal is declared failed or in which 10 consecutively severely errored seconds (SES) occurred, until the time when 10 consecutive non-SES occur.

UITS (Unacknowledged Information Transfer Service)

Unconfirmed mode of LAPD operation.

UNEQ

Path Unequipped

UNI

User Network Interface

UNITE

UNIversal high speed TDM Equipment

Upstream

At or towards the source of the considered transmission stream, for example, looking in the opposite direction of transmission.

User Privilege

Permissions a user must perform on the computer system on which the system software runs.

UTC (Universal Time Coordinated)

A time-zone independent indication of an event. The local time can be calculated from the Universal Coordinated Time.

V

V

Volts

VAC

Volts Alternating Current

Value

A number, text string, or other menu selection associated with a parameter.

Variable

An item of data named by an identifier. Each variable has a type, such as int or Object, and a scope.

VC (Virtual Container)

Container with path overhead.

VC-12

Virtual Container 1 2 (SDH payload; 2 Mbit/s capacity)

VC-3

Virtual Container 3 (SDH payload; 34 or 45 Mbit/s capacity)

VDC

Volts Direct Current

VF

Voice frequency

Virtual

Refers to artificial objects created by a computer to help the system control shared resources.

Virtual Circuit

A logical connection through a data communication (for example, X.25) network.

VLAN

Virtual Local Area Network

Voice Frequency (VF) Circuit

A 64 kilobit per second digitized signal.

Volatile Memory

Type of memory that is lost if electrical power is interrupted.

VT1.5

Virtual Tributary at the 1.5 level (SONET payload, 1.728 Mbit/s capacity).

W

WAD

Wavelength Add/Drop

WAN (Wide Area Network)

A communication network that uses common-carrier provided lines and covers an extended geographical area.

Wander

Long term variations of amplitude frequency components (below 10 Hz) of a digital signal from their ideal position in time possibly resulting in buffer problems at a receiver.

WANPHY (Wide Area Network Physical layer)
An OSI layer 1 WAN Ethernet interface type.

Wavelength Interchange
The ability to change the wavelength associated with an STM-N signal into another wavelength.

***WaveStar*® OLS 1.6T (400G/800G)**
WaveStar® Optical Line System 1.6 Terabit/s (400Gbit/s/800Gbit/s)

WDCS
Wideband Digital Cross-Connect System

WDM (Wavelength Division Multiplexing)
A means of increasing the information-carrying capacity of an optical fiber by simultaneously transmitting signals at different wavelengths.

Wideband Communications
Voice, data, and/or video communication at digital rates from 64 kbit/s to 2 Mbit/s.

Working
Label attached to a physical entity. In case of revertive switching the working line or unit is the entity that is carrying service under normal operation. In case of nonrevertive switching the label has no particular meaning.

Working State
The working unit is currently considered active by the system and that it is carrying traffic.

WRT (Wait to Restore Time)
Corresponds to the time to wait before switching back after a failure has cleared, in a revertive protection scheme. This can be between 0 and 15 minutes, in increments of one minute.

WS
Work Station

WTR (Wait to Restore)
Applies to revertive switching operation. The protection group enters the WTR state when all Equipment Fail (EF) conditions are cleared, but the system has not yet reverted back to its working line. The protection group remains in the WTR state until the Wait-to-Restore timer completes the WTR time interval.

X

X.25
An ITU standard defining the connection between a terminal and a public packet-switched network

X.25 Interface/Protocol
The ITU packet-switched interface standard for terminal access that specifies three protocol layers: physical, link, and packet for connection to a packet-switched data network.

XC (Cross-connection and timing unit; , main switching unit)

A circuit pack basically consisting of the system switching matrix and the system internal timing source; available switching capacities: 160, 320, or 640 Gbit/s.

Z Zero Code Suppression

A technique used to reduce the number of consecutive zeros in a line-coded signal (B3ZS, B8ZS).

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